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# FDS4435BZ P-Channel PowerTrench<sup>®</sup> MOSFET

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# FDS4435BZ P-Channel PowerTrench<sup>®</sup> MOSFET -30V, -8.8A, 20mΩ

### Features

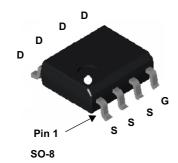
- Max  $r_{DS(on)}$  = 20m $\Omega$  at V<sub>GS</sub> = -10V, I<sub>D</sub> = -8.8A
- Max  $r_{DS(on)}$  = 35m $\Omega$  at V<sub>GS</sub> = -4.5V, I<sub>D</sub> = -6.7A
- Extended V<sub>GSS</sub> range (-25V) for battery applications
- HBM ESD protection level of ±3.8KV typical (note 3)
- High performance trench technology for extremely low r<sub>DS(on)</sub>
- High power and current handling capability
- Termination is Lead-free and RoHS compliant

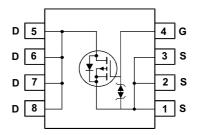


# April 2009

### **General Description**

This P-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench<sup>®</sup> process that has been especially tailored to minimize the on-state resistance. This device is well suited for Power Management and load switching applications common in Notebook Computers and Portable Battery Packs.





## **MOSFET Maximum Ratings** T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units V		
V <sub>DS</sub>	Drain to Source Voltage				-30	
V <sub>GS</sub>	Gate to Source Voltage			±25	V	
ID	Drain Current -Continuous	T <sub>A</sub> = 25°C	(Note 1a)	-8.8	Α	
	-Pulsed			-50		
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> = 25°C	(Note 1a)	2.5	14/	
	Power Dissipation	T <sub>A</sub> = 25°C	(Note 1b)	1.0	W	
E <sub>AS</sub>	Single Pulse Avalanche Energy		(Note 4)	24	mJ	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperation	ature Range		-55 to +150	°C	

## **Thermal Characteristics**

$R_{\theta JC}$	ermal Resistance, Junction to Case 25		°C/W
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient (Note 1	a) 50	C/W

### **Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity	
FDS4435BZ	FDS4435BZ	SO-8	13"			

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	cteristics					
3V <sub>DSS</sub>	Drain to Source Breakdown Voltage	I <sub>D</sub> = -250μA, V <sub>GS</sub> = 0V	-30			V
ΔBV <sub>DSS</sub> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D = -250\mu$ A, referenced to 25°C		-21		mV/°C
DSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> = -24V, V <sub>GS</sub> = 0V			1	μA
GSS	Gate to Source Leakage Current	$V_{GS}$ = ±25V, $V_{DS}$ = 0V			±10	μA
On Chara	cteristics					
V <sub>GS(th)</sub>	Gate to Source Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = -250μA	-1	-2.1	-3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_{II}}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = -250 \mu A$ , referenced to 25°C		6		mV/°C
0		V <sub>GS</sub> = -10V, I <sub>D</sub> = -8.8A		16	20	
DS(on)	Static Drain to Source On Resistance	$V_{GS} = -4.5V, I_D = -6.7A$		26	35	mΩ
20(01)		V <sub>GS</sub> = -10V, I <sub>D</sub> = -8.8A, T <sub>J</sub> = 125°C		22	28	
Ĵfs	Forward Transconductance	V <sub>DS</sub> = -5V, I <sub>D</sub> = -8.8A		24		S
<b>Oynamic</b>	Characteristics					
C <sub>iss</sub>	Input Capacitance			1385	1845	pF
C <sub>oss</sub>	Output Capacitance	─V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, f = 1MHz		275	365	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			230	345	pF
۲ <sub>g</sub>	Gate Resistance	f = 1MHz		4.5		Ω
Switching	Characteristics					
d(on)	Turn-On Delay Time			10	20	ns
r	Rise Time	$V_{DD} = -15V, I_D = -8.8A,$		6	12	ns
d(off)	Turn-Off Delay Time	$-V_{GS} = -10V, R_{GEN} = 6\Omega$		30	48	ns
f	Fall Time			12	22	ns
Q <sub>q</sub>	Total Gate Charge	$V_{GS} = 0V \text{ to } -10V$ $V_{GS} = 0V \text{ to } -5V$ $V_{DD} = -15V,$ $I_D = -8.8A$		28	40	nC
2 <sup>ª</sup>	Total Gate Charge	$V_{GS} = 0V \text{ to } -5V$ $V_{DD} = -15V,$		16	23	nC
ລັ ລັ	Gate to Source Charge	I <sub>D</sub> = -8.8A		5.2		nC
ວ <sub>gd</sub>	Gate to Drain "Miller" Charge			7.4		nC
ງ Drain-Sou	urce Diode Characteristics					
V <sub>SD</sub>	Source to Drain Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = -8.8A (Note 2)		-0.9	-1.2	V
rr	Reverse Recovery Time	I <sub>F</sub> = -8.8A, di/dt = 100A/μs		29	44	ns
11	Reverse Recovery Charge	$F_{\rm F} = -0.0$ A, ui/ul = 100 A/µs		23	35	nC

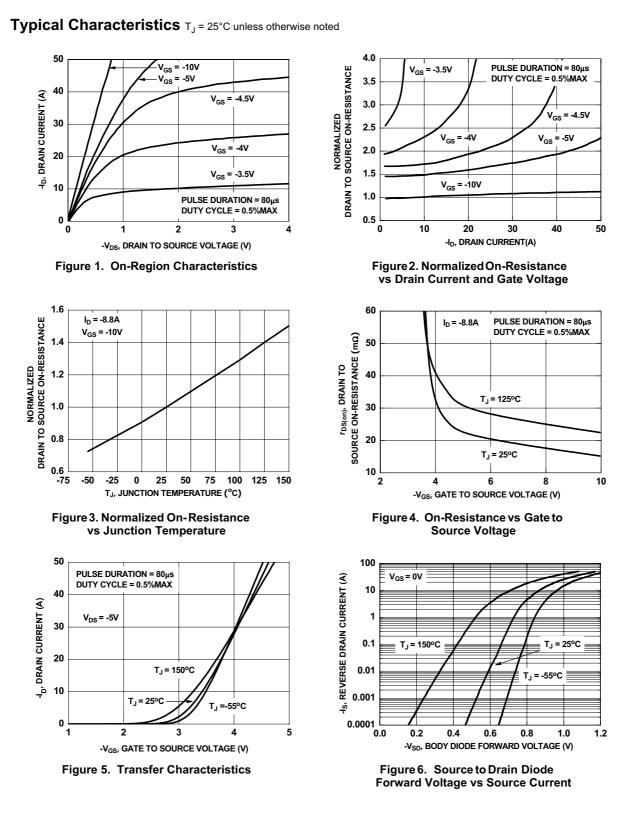
2. Pulse Test: Pulse Width < 300 $\mu$ s, Duty cycle < 2.0%.

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3. The diode connected between the gate and source serves only as protection against ESD. No gate overvoltage rating is implied.

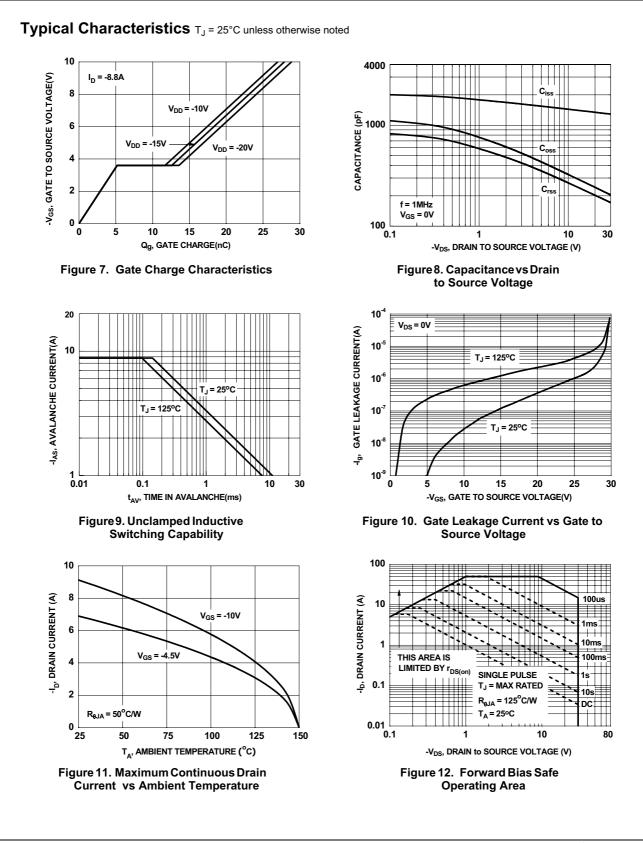
4. Starting  $T_J$  = 25°C, L = 1mH,  $I_{AS}$  = -7A,  $V_{DD}$  = -30V,  $V_{GS}$  = -10V





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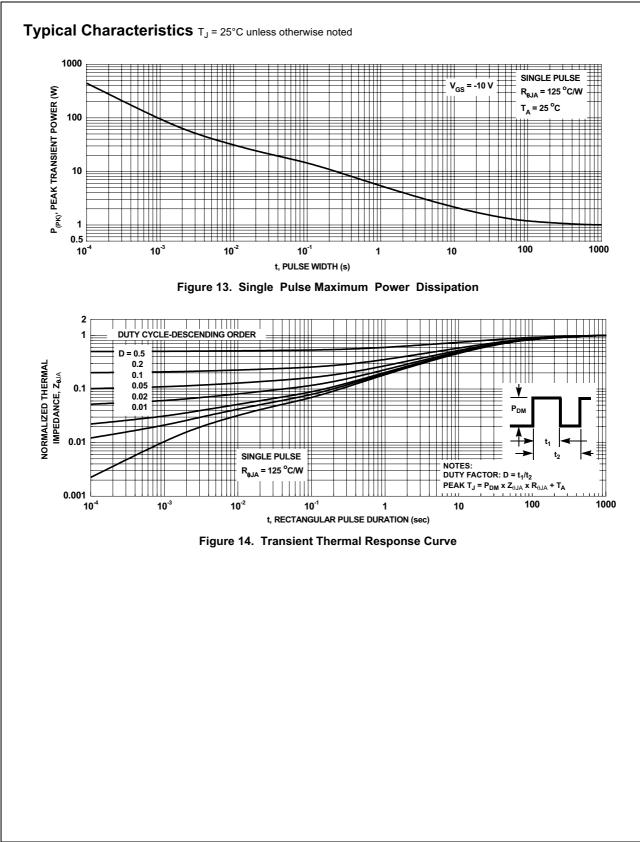
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FDS4435BZ P-Channel PowerTrench<sup>®</sup> MOSFET

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