

## 25 A high voltage Triacs

#### **Features**

- On-state current (I<sub>T(RMS)</sub>): 25 A
- Max. blocking voltage (V<sub>DRM</sub>/V<sub>RRM</sub>): 1200 V
- Gate current (I<sub>GT</sub>): 150 mA
- Commutation @ 10 V/µs: up to 88 A/ms
- Noise immunity: 2 kV/µs
- Insulated package:
  - 2,500 V rms (UL recognized: E81734).

#### **Description**

The TPDVxx25 series use high performance alternistor technology.

Featuring very high commutation levels and high surge current capability, these devices are well adapted to power control for inductive and resistive loads (motor, transformer...) especially on three-phase power grid. Targeted three-phase applications include heating systems, motor starters, and induction motor speed control (especially for fans).

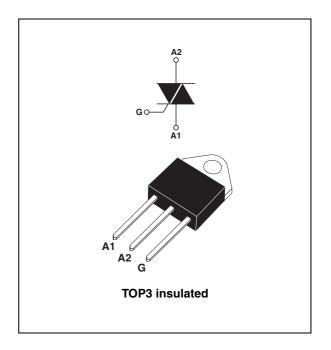


Table 1. Device summary

Parameter	TPDV825RG	TPDV1025RG	TPDV1225RG	
Blocking voltage V <sub>DRM</sub> /V <sub>RRM</sub>	800 V	1000 V	1200 V	
On-state current I <sub>T(RMS)</sub>	25 A			
Gate current I <sub>GT</sub>	150 mA			

Characteristics TPDVxx25

### 1 Characteristics

Table 2. Absolute maximum ratings (limiting values)

Symbol	Parameter			Value	Unit	
I <sub>T(RMS)</sub>	On-state rms current (180° conduction angle) T <sub>c</sub> = 85 °C		25	Α		
	Non repetitive surge peak on-state current	$t_p = 2.5 \text{ ms}$		390		
I <sub>TSM</sub>		$t_p = 8.3 \text{ ms}$	T <sub>j</sub> = 25 °C	250	Α	
		$t_p = 10 \text{ ms}$		230		
l <sup>2</sup> t	I <sup>2</sup> t value for fusing	t <sub>p</sub> = 10 ms	T <sub>j</sub> = 25 °C	265	A <sup>2</sup> s	
dI/dt	Critical rate of rise of on-state current $I_G = 500$ mA, $dI_G/dt = 1$ A/ $\mu$ s	F = 50 Hz		100	A/µs	
		TPDV825		800		
V <sub>DRM</sub> V <sub>RRM</sub>	Repetitive peak off-state voltage	TPDV1025	T <sub>j</sub> = 125 °C	1000	V	
		TPDV1225		1200		
T <sub>stg</sub> T <sub>j</sub>	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125	°C	
V <sub>INS(RMS)</sub> <sup>(1)</sup>	Insulation rms voltage			2500	V	

<sup>1.</sup> A1, A2, gate terminals to case for 1 minute

Table 3. Electrical Characteristics ( $T_i = 25$  °C, unless otherwise specified)

Symbol	Test conditions		Quadrant		Value	Unit
I <sub>GT</sub>	$V_{\rm D} = 12 \text{ V DC}, R_{\rm I} = 33 \Omega$		1 - 11 - 111	MAX.	150	mA
V <sub>GT</sub>	VD = 12 V DO, NL = 33 12		1 - 11 - 111	MAX.	1.5	V
$V_{GD}$	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $T_j = 125 \text{ °C}$		1 - 11 - 111	MIN.	0.2	٧
t <sub>gt</sub>	$V_D = V_{DRM} I_G = 500 \text{ mA} \text{ d}I_G/\text{d}t = 3 \text{ A/}\mu\text{s}$		1 - 11 - 111	TYP.	2.5	μs
I <sub>H</sub> <sup>(1)</sup>	I <sub>T</sub> = 500 mA Gate open			TYP.	50	mA
	$I_G = 1.2 \times I_{GT}$		I - III	TYP.	100	mA
IL			II		200	
dV/dt	Linear slope up to: $V_D = 67\% V_{DRM}$ Gate open $T_j = 125 ^{\circ}\text{C}$			MIN.	2000	V/µs
V <sub>TM</sub> <sup>(1)</sup>	$I_{TM} = 35 \text{ A}$ $t_p = 380  \mu\text{s}$			MAX.	1.8	V
V <sub>to</sub> <sup>(1)</sup>	Threshold voltage $T_j = 125  ^{\circ}\text{C}$			MAX.	1.1	V
R <sub>d</sub> <sup>(1)</sup>	Dynamic resistance $T_j = 125 ^{\circ}\text{C}$			MAX.	19	mΩ
I <sub>DRM</sub>	V - V	T <sub>j</sub> = 25 °C		MAY	20	μΑ
I <sub>RRM</sub>	$V_{\text{DRM}} = V_{\text{RRM}}$ $T_{j}$			MAX.	8	mA
(dl/dt)c (1)	(dV/dt)c = 200 V/μs	T _ 105 °C		MIN.	20	A /m s
(ui/ut)C · /	$T_{j} = 125 \text{ °C}$			IVIIIN.	88	A/ms

<sup>1.</sup> For either polarity of electrode A<sub>2</sub> voltage with reference to electrode A<sub>1</sub>.

TPDVxx25 Characteristics

Table 4. Gate characteristics (maximum values)

Symbol	Parameter	Value	Unit	
P <sub>G(AV)</sub>	Average gate power dissipation		1	W
P <sub>GM</sub>	Peak gate power dissipation	t <sub>p</sub> = 20 μs	40	W
I <sub>GM</sub>	Peak gate current $t_p = 20 \mu s$		8	Α
$V_{GM}$	Peak positive gate voltage	t <sub>p</sub> = 20 μs	16	V

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>th(j-a)</sub>	Junction to ambient	50	°C/W
R <sub>th(j-c)</sub> DC	Junction to case for DC	1.5	°C/W
R <sub>th(j-c)</sub> AC	Junction to case for 360 °Conduction angle (F = 50 Hz)	1.1	°C/W

Figure 1. Max. rms power dissipation versus Figure 2. on-state rms current (F = 50Hz). (curves limited by (dl/dt)c)

Max. rms power dissipation and max. allowable temperatures ( $T_{amb}$  and  $T_{case}$ ) for various  $R_{th}$ 

T<sub>case</sub>(°C)

95

105

115

L 125

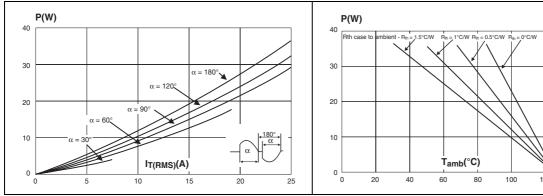
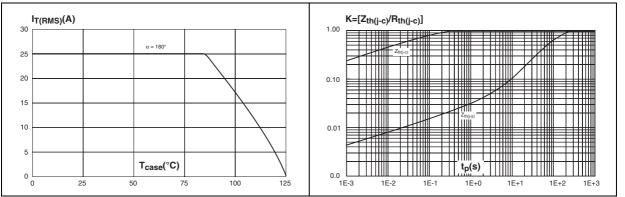


Figure 3. On-state rms current versus case temperature

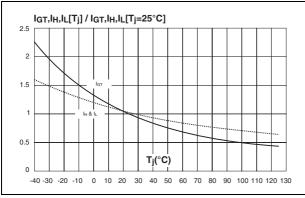
Figure 4. Relative variation of thermal impedance versus pulse duration



Characteristics TPDVxx25

Figure 5. Relative variation of gate trigger current and holding current versus junction temperature

Figure 6. Non repetitive surge peak on-state current versus number of cycles



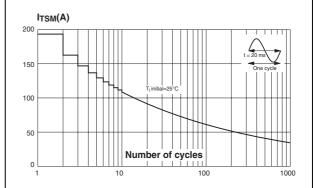
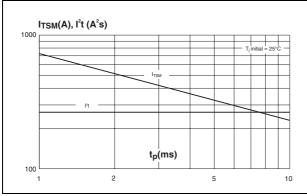


Figure 7. Non-repetitive surge peak on-state Figure 8. current for a sinusoidal pulse and corresponding values of I<sup>2</sup>t

Figure 8. On-state characteristics (maximum values)



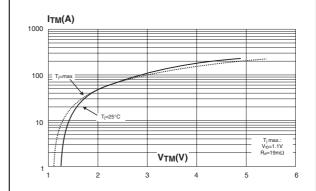
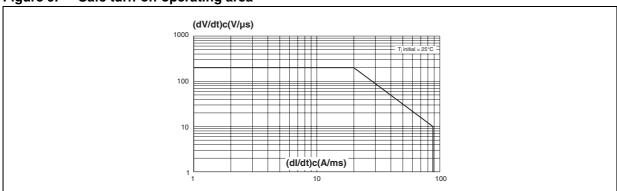


Figure 9. Safe turn-off operating area

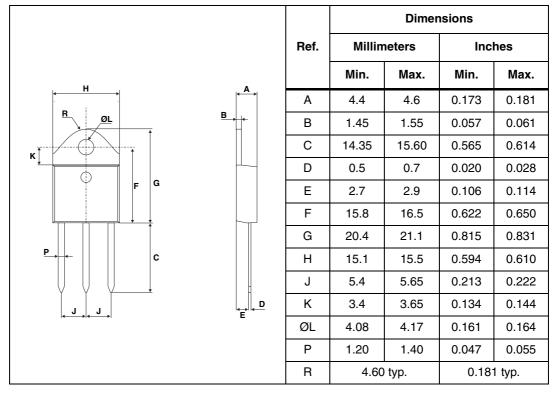


### 2 Package information

- Epoxy meets UL94,V0
- Cooling method: C (by conduction)
- Recommended torque value: 0.9 to 1.2 N⋅m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Table 6. TOP3 insulated dimensions



Ordering information TPDVxx25

# **3** Ordering information

 Table 7.
 Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
TPDV825RG	TPDV825				
TPDV1025RG	TPDV1025	TOP3 insulated		30	Tube
TPDV1225RG	TPDV1225				

## 4 Revision history

Table 8. Document revision history

Date	Revision	Changes	
30-Mar-2011	1	First issue.	
13-Jan-2012	2	Updated dl/dt in <i>Table 2</i> and added V <sub>to</sub> and R <sub>d</sub> to <i>Table 3</i> .	

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