

Medium Power Transistor



Pin Configuration

1. Emitter
2. Base
3. Collector

Features:

- High performance, low frequency devices typically with current ratings 2A. Up to 1W power dissipation
- PNP Epitaxial Planar Silicon Transistors

Absolute Maximum Ratings:

(T_A = 25°C unless otherwise specified)

Characteristic	Symbol	Value	Unit
Collector-Emitter Voltage I _b =0	V _{CEO(sus)}	60	V
Collector-Emitter Voltage R _{BE} = 100Ω	V _{CER}	75	
Emitter-Base Voltage	V _{EBO}	5	
Collector Current	I _C	1	A
Collector Peak Current	I _{CM}	2	
Base Current	I _B	0.5	
Power Dissipation at T _A = 25°C at T _C = 25°C	P _{tot}	1 10	W
Operating Storage Temperature Range	T _J , T _{stg}	-65 to +200	°C

Thermal Resistance

Junction to Ambient	R _{th(j-a)}	17.5	°C/W
Junction to Case	R _{th(j-c)}		



Medium Power Transistor



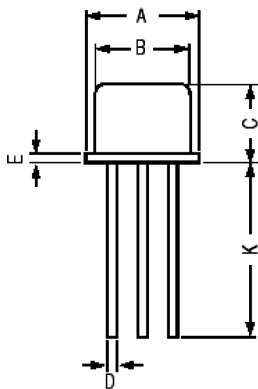
Electrical Characteristics:

($T_a = +25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Max.	Unit
Collector-Cut off Current $R_{BE} = 100\Omega$	I_{CBO}	$V_{CB} = 40\text{V}, I_E = 0$	-	100	nA
	I_{CER}	$V_{CE} = 60\text{V}$		10	μA
Collector-Emitter Voltage	V_{CEO}^*	$I_C = 10\text{mA}, I_B = 0$	60	-	V
Emitter-Base Voltage	V_{EBO}	$I_E = 100\mu\text{A}, I_C = 0$	5		
DC Current Gain	h_{FE}^*	$I_C = 500\text{mA}, V_{CE} = 4\text{V}$	40	250	-
Collector Emitter Saturation Voltage	$V_{CE(sat)}^*$	$I_C = 1,000\text{mA}, I_B = 100\text{mA}$	-	1	V
Base Emitter Saturation Voltage	$V_{BE(sat)}^*$	$I_C = 1\text{A}, I_B = 100\text{mA}$		1.5	
Transition Frequency	f_t	$I_C = 50\text{mA}, V_{CE} = 4\text{V}, f = 1\text{MHz}$	50	-	MHz

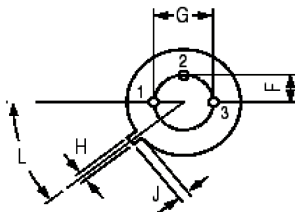
*Pulse : Pulse Duration = 300 μs , Duty Cycle = 1%

TO-39 Metal Can Package



Dim.	Min.	Max.
A	8.5	9.39
B	7.74	8.5
C	6.09	6.6
D	0.4	0.53
E	-	0.88
F	2.41	2.66
G	4.82	5.33
H	0.71	0.86
J	0.73	1.02
K	12.7	-
L	42°	48°

Dimensions : Millimetres



Pin Configuration

1. Emitter
2. Base
3. Collector

Part Number Table

Description	Part Number
Transistor, PNP, TO-39	BC461

Important Notice : This data sheet and its contents (the "Information") belong to the members of the Premier Farnell group of companies (the "Group") or are licensed to it. No licence is granted for the use of it other than for information purposes in connection with the products to which it relates. No licence of any intellectual property rights is granted. The Information is subject to change without notice and replaces all data sheets previously supplied. The Information supplied is believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this data sheet should check for themselves the Information and the suitability of the products for their purpose and not make any assumptions based on information included or omitted. Liability for loss or damage resulting from any reliance on the Information or use of it (including liability resulting from negligence or where the Group was aware of the possibility of such loss or damage arising) is excluded. This will not operate to limit or restrict the Group's liability for death or personal injury resulting from its negligence. Multicomp is the registered trademark of the Group. © Premier Farnell Limited 2016.