

CY5676A PROCTM BLE 256KB MODULE

CY5676A PRoC™ BLE 256KB Module is a complete solution to add BLE connectivity to any embedded system with Bluetooth® 4.2 features. The module features a PRoC BLE device (CYBL11573-56LQXI), 24-MHz and 32.768-kHz crystals, a PCB antenna, and other passives while providing access to all GPIOs of the device.

For more information on Cypress's BLE solutions visit www.cypress.com/BLE

PRoC BLE is a single-chip solution with a 48-MHz ARM® Cortex®-M0 CPU, Bluetooth 4.2 radio, CapSense® technology for touch-sensing, two serial communication blocks (SCBs), a 12-bit ADC, four Timer/Counter/PWMs, four additional PWMs, I2S for digital audio, and segment LCD direct-drive capability.

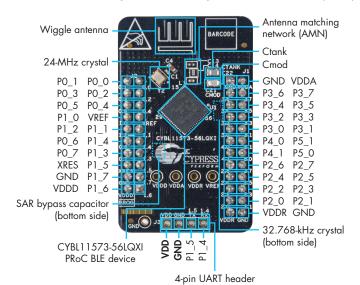


Fig-1: Pinout Description for CY5676A

Note: To apply power to the module, apply 1.9 V - 5.0 V to any of the VDD pins (VDD, VDDD, VDDA, or VDDR) and 0 V to any GND pin

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STANDALONE USE WITH PSoC CREATOR™ IDE AND CY8CKIT-002 MINIPROG3

Step 1: Select the 5-pin connector setting in PSoC Creator or PSoC Programmer.

Step 2: Use 0.1" male headers (not included) or jumper wires to connect the MiniProg3 to the module.



Pin Mapping	
CY8CKIT-002	CY5676A
VTARG	VDDD
GND	GND
RES	XRES
SCLK	PO_7
SDAT	P0_6

Fig-2: Programming and Debugging with CY8CKIT-002 MiniProg3

USING THE PROC BLE MODULE WITH THE CY8CKIT-042-BLE PIONEER KIT

Step 1: Plug the module on the CY8CKIT-042-BLE Pioneer Kit Baseboard.

Step 2: Create your design in PSoC Creator IDE and use the BLE Pioneer Kit to program and debug.



Fig-3: Programming and Debugging with CY8CKIT-042-BLE Pioneer Baseboard

The CY5676A module is intended for development purposes only. Users are advised to test and evaluate this module in an RF development environment. This module is not a finished product and when assembled may not be resold or otherwise marketed unless all required authorizations are first obtained.

Contact support@cypress.com for details.



For more information about this kit, visit www.cypress.com/CY5676A

