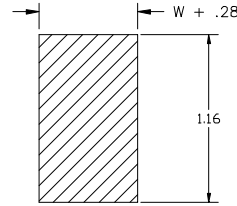


# ITT Pomona Model 6109 44 Pin SOIC Clip® Test Clip Lead Spacing: 0.050"

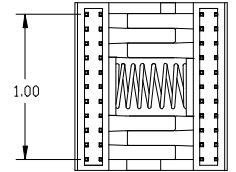
These new test clips are designed especially for in-circuit testing on 44 pin Small Outline Integrated Circuits (SOIC & SOJ). The SOIC Clip® test clip assures a positive connection to all chip leads and provides hands-free testing.

|    |    |    |    |
|----|----|----|----|
| 1  | 2  | 44 | 43 |
| 3  | 4  | 42 | 41 |
| 5  | 6  | 40 | 39 |
| 7  | 8  | 38 | 37 |
| 9  | 10 | 36 | 35 |
| 11 | 12 | 34 | 33 |
| 13 | 14 | 32 | 31 |
| 15 | 16 | 30 | 29 |
| 17 | 18 | 28 | 27 |
| 19 | 20 | 26 | 25 |
| 21 | 22 | 24 | 23 |

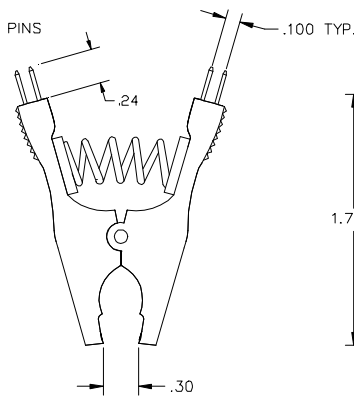
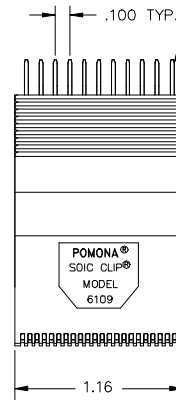
PIN NUMBERING



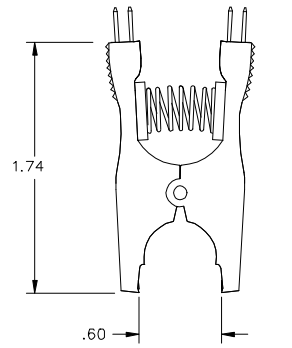
SPACE REQUIRED  
W = CHIP WIDTH



- ◆ Upper contacts are .025" sq. pins on 0.100" centers. Pins are perfect for sq. pin receptacles, wire-wrap or soldered wires.
- ◆ Upper contacts fit standard double row ribbon cable connectors.
- ◆ Gold plated lower contacts assure reliable, noise free connections.
- ◆ Molded barriers between each contact allow connections to be made on live boards without accidental shorting to adjacent chip leads.
- ◆ Open back of clip allows probing of chip leads while the clip is attached.
- ◆ Fits all 44 pin surface mounted SOIC & SOJ chips from .30" to .60" wide. (This clip will also work on 42 pin SOIC & SOJ chips.)
- ◆ Compact design requires a minimum of board space. Clips can be placed on chips that are spaced only .040" apart, end-to-end. The clip requires .14" beyond each side of the chip for placement.



MINIMUM OPENING



MAXIMUM OPENING