MAX44298 Evaluation Kit

General Description

The MAX44298 evaluation kit (EV kit) provides a proven design to evaluate the MAX44298 current, voltage, and power monitor. The device offers a precision power monitor with very low offset for low-side monitoring. This EV kit demonstrates the MAX44298 in an ultra-small, 2.4mm x 2.4mm, 16-bump wafer-level package (WLP) with 0.5mm bump spacing.

The EV kit PCB is preconfigured with the CSA full-scale input voltage range (V_{SENSE}) of 10mV and 100 μ A of output current ranges, but can be reconfigured to 10mV/5mV of FS V_{SENSE} or 50 μ A of output current by changing a few jumpers.

The EV kit comes with a MAX44298EWE+ and a voltage divider to provide a forced full-scale V_{SENSE} of 10mV range at the CSA inputs installed.

Features

- Precision Real-Time, Low-Side Current/Voltage/ Power Monitoring
- +3V to +5.5V Single-Supply Voltage Range
- Proven PCB Layout
- Fully Assembled and Tested

Ordering Information appears at end of data sheet.

Quick Start

Required Equipment

Before beginning, the following equipment is needed:

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- MAX44298 EV kit
- +3V to +5.5V, 100mA DC power supply
- +0.4V to +1.005V DC power supply
- Precision DC voltage source
- Five digital multimeters (DMMs)

Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation. Caution: Do not turn on power supply until all connections are made.

- Set the +3V to +5.5V supply to +3.3V and turn it off. Connect the positive terminal of the supply to the VDD test point and the negative terminal of the supply to the nearest GND test point.
- Set the +0.4V to +1.005V supply to +1V and turn it off. Connect the positive terminal of the supply to the VIN test point and the negative terminal of the supply to the nearest GND test point.
- 3) Set the precision DC voltage source to 5mV and turn it off. Apply this voltage source across the V_{SENSE} inputs as a forced voltage V_{SENSE} (i.e., connect the positive terminal of the DC voltage source to the RS+ test point and connect its negative terminal to the RS- test point).
- Connect one DMM across RS+ and RS- to monitor the V_{SENSE} input. Connect each DMM to each output of the device (IOUT, VOUT, POUT, and REF) to monitor the output voltages.
- 5) Enable all supplies.
- Observe the output voltage from all four digital voltmeter displays. Verify that V_{IOUT} = 1.2V, V_{VOUT} = 2.4V, V_{POUT} = 1.2V and V_{REF} = 2.4V.



Table 1. Jumper Description

JUMPER	SHUNT POSITION	DESCRIPTION
	1-2	Sets the full-scale output current to 50µA.
JU1 (ISET)	1-3	Reserved
(1021)	1-4*	Sets the full-scale output current to 100µA.
JU2	1-2	Enters calibration mode, sets the output current to 10µA (for IOUT, VOUT, and POUT) and full-scale output current set by JU1 (for the REF).
(CAL)	1-3	Reserved
	1-4*	Sets the device in normal operation mode and the output current full scale is set by JU1.
	1-2	Connects G0 to logic 1 to set the full-scale sensing voltage range (VFS). See Table 2.
JU3 (G0)	1-3	Reserved
(30)	1-4*	Connects G0 to logic 0 to set the full-scale sensing voltage range (VFS). See Table 2.
	1-2*	Connect G1 to logic1 to set the full-scale sensing voltage range (VFS). See Table 2.
JU4 (G1)	2-3	Reserved
(31)	1-4	Connect G1 to logic 0 to set the full-scale sensing voltage range (VFS). See Table 2.

^{*}Default configuration

Table 2. Full-Scale V_{SENSE} Range Selection

G1 (JU4)	G0 (JU3)	FS V _{SENSE}	R _{SENSE} = 1mΩ	R _{SENSE} = 2mΩ	R _{SENSE} = 10mΩ	
0	1	5mV	5A	2.5A	0.5A	
1*	0*	10mV*	10A	5A	1A	
1	1	20mV 20A 10A 2A				
0	0	Device enters power-down mode				

^{*}Default configuration

Detailed Description of Hardware

The MAX44298 EV kit low-side current-sensing measures the load current by using a precision CSA, allowing accurate full-scale V_{SENSE} ranges of 5mV, 10mV, and 20mV, providing scaled output current at IOUT. The floating source voltage is measured through a user-selectable resistive-divider (dividing the source input voltage down to a full-scale VIN of 1.00V) and provides scaled output current at VOUT. The device monitors the instantaneous input power by internally multiplying the scaled load current by a scaled fraction of the load voltage, providing scaled output current at POUT. The device also provides a reference output current at the REF output. All four output currents are converted to voltages by using scaled resistors (R3–R6).

Output-Scaling Resistors: R3, R4, R5, R6, and the ISET Input

The output-scaling resistors should all be the same value and be of a type with very low temperature coefficients, with metal film types being recommended. Metal foil is more effective, though significantly more expensive. The chosen values of these resistors will depend on the ISET setting. JU1 determines the full-scale output current range. When ISET is connected to ground (JU1: 1-2), the full-scale output current from all four outputs will be $100\mu A$. When ISET is connected to VDD (JU1: 1-4), the full-scale current will be $50\mu A$. This can be a simple and convenient way to change all four scaling resistors simultaneously. The EV kit is shipped with all four $24k\Omega$ scaling resistors.

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Calibration

JU2 sets the device in operational mode (or CAL) mode. The EV kit is shipped with JU2 in 1-4 position for operational mode. Set JU2 in 1-2 position to place the device in CAL mode.

Applying the Source Voltage

The two options for applying source voltage are through an external voltage divider (R1 and R2) or as a direct voltage input (VIN). For using an R1–R2 voltage-divider: apply the source voltage to IN+ and IN-. For a maximum input source voltage of say 57V, R1 could be $560k\Omega$ with R2 being $10k\Omega$. The voltage-divider, formed by R1 and R2, provides 1V at the VIN input test point. The second option is to apply the voltage directly to the VIN test point. In either configuration, care must be taken not to apply a voltage greater than 1V to the VIN input of the device. The EV kit is shipped with the voltage-divider formed by R1 ($560k\Omega$) and R2 ($10k\Omega$), and a VIN test point as well.

Measuring the Low-Side Load Current

The device measures the unidirection load current (flowing from RS+ to RS-) as a voltage drop (V_{SENSE}) across an external sense resistor (R14, not installed) and provides scaled output at IOUT. To ensure proper load current measurements, the sense resistor must be chosen so that its voltage drop does not exceed the full-scale sense voltage of the device. The full-scale sense voltage should be reached when the full-scale load current is being supplied to the load. The external sense resistor R14 is determined by setting the full-scale load current and selecting a full-scale sense voltage that does not exceed the full-scale sense-voltage rating of the IC:

$$R_{14} = \frac{V_{SENSE_FS}}{I_{LOAD_FS}}$$

The EV kit supports a full-scale sense voltage drop of 5mV, 10mV, and 20mV (see <u>Table 2</u> for selections). The EV kit defaults to the 10mV full-scale setting. For different full-scale sense ranges and full-scale load current arrangements, the equation above can be used to determine the appropriate sense-resistor value.

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Other options to evaluate the current-sensing capability of the MAX44298 are to create a voltage across RS+ and RS- without applying heavy load current going through these two test points. Apply a negative voltage to the VNEG test point (using the resistive-divider R12 and R13) or a direct precision voltage source across RS+ and RS-. In either case, ensure that the sense voltage across RS+ and RS- is within its set full-scale range. The device is shipped with a voltage-divider formed by R12 = $4.99k\Omega$ and R13 = 10Ω . When using this option, the users should note that there exists approximately 100µA bias current coming out of the RS- pin due to the CSA internal gain settings. This input bias current should be taken into account for error when sensing small currents in milli-ampere ranges. Therefore, measuring the actual drop across RS+ and RS- is necessary for accuracy. For example, applying a negative supply voltage of -5V at the VNEG test point would give about 9mV across RS+ and RS- instead of 10mV, theoretically.

For 5mV and 20mV full-scale sense ranges, R12 and R13 should be re-scaled accordingly.

Monitoring the Load Power

The device monitors the instantaneous input power by internally multiplying the scaled load current and a scaled fraction of the load voltage and provides scaled output at POUT.

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MAX44298 Evaluation Kit

Component List, PCB Layout, and Schematic

See the following links for component information, PCB layout diagrams, and schematics.

- MAX44298 EV BOM
- MAX44298 EV PCB Layout
- MAX44298 EV Schematic

Ordering Information

PART	TYPE	
MAX44298EVKIT#	EV Kit	

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#RoHS-compliant

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MAX44298 Evaluation Kit

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	11/15	Initial release	_

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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TITLE: Bill of Materials

DATE: 09/30/2015

DESIGN	: max44298_evkit_b	T		T	Ι	Ī	T	Ι
TEM	REF_DES	DNI/DNP	QTY	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION	COMMENTS
1	C3-C6	-	4	C0603X7R500103JNP; C0603C103J5	КЕМЕТ	0.01UF	CAPACITOR; SMT (0603); CERAMIC CHIP; 0.01UF; 50V; TOL=5%; MODEL=X7R; TG=-55 DEGC TO +125 DEGC; TC=+/	
2	C7	-	1	CGA5L1C0G2A683J160	TDK	0.068UF	CAPACITOR; SMT (1206); CERAMIC CHIP; 0.068UF; 100V; TOL=5%; TG=-55 DEGC TO +125 DEGC; TC=C0G AUTO	
3	C8	-	1	GCM3195C2A103JA16D	MURATA	0.01UF	CAPACITOR; SMT (1206); CERAMIC CHIP; 0.01UF; 100V; TOL=5%; MODEL=; TG=-55 DEGC TO +125 DEGC; TC=C0G	
4	C10	-	1	ECJ-1VB1H104K; GRM188R71H104KA; CGJ3E2X7R1H104K080AA; C1608X7R1H104K080AA	PANASONIC/MURATA/TD K	0.1UF	CAPACITOR; SMT (0603); CERAMIC CHIP; 0.1UF; 50V; TOL=10%; TG=-55 DEGC TO +125 DEGC; TC=X7R;	
5	C11	-	1	C1608X5R1E475K080AC	TDK	4.7UF	CAPACITOR; SMT (0603); CERAMIC CHIP; 4.7UF; 25V; TOL=10%; MODEL=C SERIES; TG=-55 DEGC TO +85 DEGC; TC=X5R	
6	C13	-	1	C0603C100K1GAC	KEMET	10PF	CAPACITOR; SMT (0603); CERAMIC CHIP; 10PF; 100V; TOL=10%; MODEL=COG; TG=-55 DEGC TO +125 DEGC; TC=+/	
7	CF, VIN, IOUT, POUT, VOUT, VREF	-	6	5000	KEYSTONE	N/A	TEST POINT; PIN DIA=0.1IN; TOTAL LENGTH=0.3IN; BOARD HOLE=0.04IN; RED; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH;	
	IN+, IN-, LOAD+, LOAD-	-	4	108-0740-001	EMERSON NETWORK POWER	108-0740-001	CONNECTOR; MALE; PANELMOUNT; BANANA JACK; STRAIGHT; 1PIN	
9	JU1-JU4	-	4	PEC04SAAN	SULLINS ELECTRONICS CORP.	PEC04SAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT; 4PINS RESISTOR; 0603; 560K OHM; 0.1%; 25PPM; 0.063W; THIN	
10	R1	-	1	1-1879417-3; CPF0603B560KE TNPW060310K0BE;	TE CONNECTIVITY VISHAY DALE/KOA SPEER	560K	FILM RESISTOR; 0603; 10K OHM; 0.1%; 25PPM; 0.1W; THICK	
11	R2	-	1	RN731JTTD1002B	ELECTRONICS	10K	FILM RESISTOR; 0603; 24K OHM; 0.1%; 25PPM; 0.1W; THIN	
	R3-R6	-		RG1608P-243-B	SUSUMU CO LTD.	24K	FILM RESISTOR; 0603; 4.7K OHM; 5%; 200PPM; 0.10W; THICK	
	R7-R10 R12	-		ERJ-3GEYJ472V RG1608P-4991-B	PANASONIC SUSUMU CO LTD.	4.7K 4.99K	FILM RESISTOR; 0603; 4.99K OHM; 0.1%; 25PPM; 0.1W; THIN FILM	
	R13	-	1	CRT0603-BY-10R0ELF	BOURNS	10	RESISTOR; 0603; 10 OHM; 0.1%; 25PPM; 0.063W; THIN FILM	
16	RS+, RS-, VCC, VDD, VNEG	-	5	5010	?	5010	TESTPOINT WITH 1.80MM HOLE DIA, RED, MULTIPURPOSE	

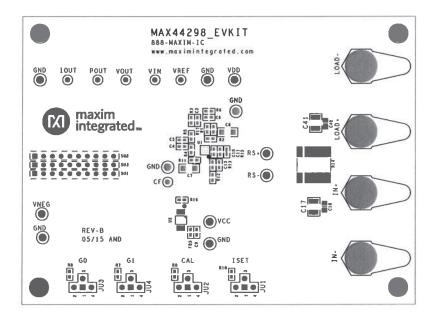
		1	1		ı			ı
17	SU4-SU7	-	4	STC02SYAN	SULLINS ELECTRONICS CORP.	STC02SYAN	TEST POINT; JUMPER; STR; TOTAL LENGTH=0.256IN; BLACK; INSULATION=PBT CONTACT=PHOSPHOR BRONZE; COPPER PLATED TIN OVERALL	
							TEST POINT; PIN DIA=0.125IN; TOTAL LENGTH=0.445IN;	
							BOARD HOLE=0.063IN; BLACK; PHOSPHOR BRONZE WIRE	
18	TP1-TP6	-	6	5011	?	5011	SILVER PLATE FINISH;	
							EVKIT PART-IC; PACKAGE CODE: WLP162P2+1; DOC NO:	
19	U1	-	1	MAX44298	MAXIM	MAX44298	21-1000005	
20	C9	DNP		ECJ-1VB1H104K; GRM188R71H104KA; CGJ3E2X7R1H104K080AA; C1608X7R1H104K080AA	PANASONIC/MURATA/TD	0.1UF	CAPACITOR; SMT (0603); CERAMIC CHIP; 0.1UF; 50V; TOL=10%; TG=-55 DEGC TO +125 DEGC; TC=X7R;	OPEN
	<u> </u>			C1000X/ X11110 IX000/ V	IX.	0.101	CAPACITOR; SMT (1210); CERAMIC CHIP; 2.2UF; 100V;	OI LIV
				GRM32ER72A225KA35;			TOL=10%; MODEL=GRM SERIES; TG=-55 DEGC to +125	
21	C17, C41	DNP		•	MURATA/TDK	2.2UF	DEGC; TC=X7R	OPEN
	017, 011		_	CG/10113// TE/12231/230	WIGHT HAT TER	2.201	CAPACITOR; SMT; 0805; CERAMIC; 0.1uF; 100V; 5%; X7R; -	OI EIV
22	C18, C42	DNP	2	C0805C104J1RAL	KEMET	0.1UF	55degC to + 125degC	OPEN
	010, 0.12	J	 	00000010 101111112	NEW E	0.120.	INDUCTOR; SMT (0603); FERRITE-BEAD; 2500; TOL=+/-	0. 2
23	FB3	DNP	1	BLM18BD252SN1	MURATA	2500	25%; 0.05A	OPEN
	-			TNPW060310K0BE;	VISHAY DALE/KOA SPEER		RESISTOR; 0603; 10K OHM; 0.1%; 25PPM; 0.1W; THICK	
24	R11	DNP		,	ELECTRONICS	10K	FILM	OPEN
							RESISTOR; 3637; 0.001 OHM; 0.5%; 25PPM; 3W; METAL	
25	R14	DNP	1	Y14880R00100D9	VISHAY FOIL RESISTOR	0.001	FOIL	OPEN
26	R15	DNP	1	N/A	N/A	SHORT	PACKAGE OUTLINE 0603 RESISTOR	
							RESISTOR; 0603; 20 OHM; 0.1%; 25PPM; 0.10W; THICK	
27	R16	DNP	1	TNPW060320R0BE	VISHAY DALE	20	FILM	OPEN
							RESISTOR; 0603; 89.8K OHM; 0.1%; 25PPM; 0.15W; THIN	
28	R17	DNP	1	PATT0603E8982BG	VISHAY DALE	89.8K	FILM	OPEN
							IC-SOCKET;SIP; STANDARD SOLDER TAIL; 801 SERIES;	
						801-93-010-10-	0.024D/0.118L; 0.1IN GRID; STRAIGHT SOCKET; OPEN	
29	SU1-SU3	DNP	3	801-93-010-10-001000	MILL-MAX	001000	FRAME; 10PINS	OPEN
							IC; AMP; DUAL-CHANNEL; HIGH-PRECISION; HIGH-	
						MAX44285TAUA	VOLTAGE; CURRENT-SENSE AMPLIFIER; GAIN=20V/V;	
30	U2	DNP	1	MAX44285TAUA+	MAXIM	+	UMAX8	OPEN

TOTAL 66

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HARDWARE NAME:MAX44298_EVKIT_B	
HARDWARE NUMBER:	
ENGINEER:	DESIGNER:
DATE: 04/29/2015	ODB++/GERBER: SILK_TOP

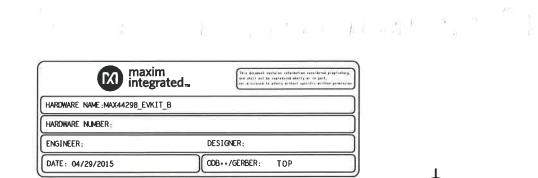






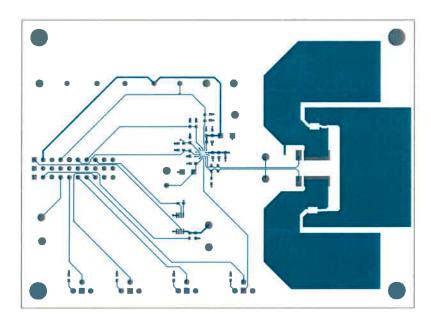






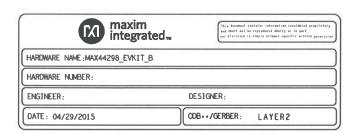






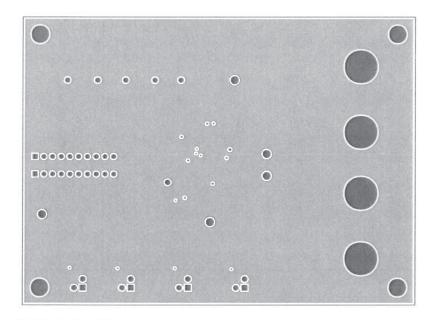








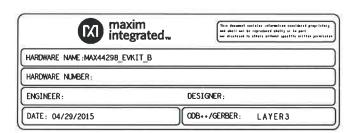






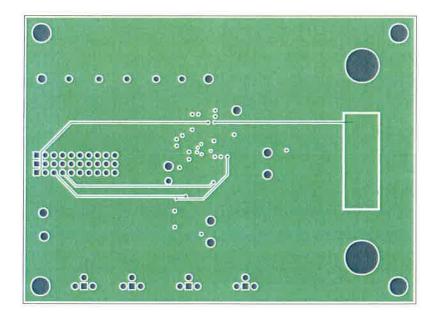
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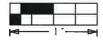












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