


MCOT128064E1V-WM	128 x 64	White	OLED Module
Specification			
Version: 2		Date: 07/06/2017	
Revision			
1	27/05/2015	First Release.	
2	01/06/2016	Modify Static Electricity Test.	

Display Features			Box Quantity	Weight / Display
Resolution	128 x 64			
Appearance	White on Black			
Logic Voltage	3V			
Interface	Parallel / SPI / I2C			
Module Size	60.50 x 37.00 x 2.15 mm			
Operating Temperature	-40°C ~ +80°C			
Construction	TAB	---	---	

* - For full design functionality, please use this specification in conjunction with the SSD1309Z specification. (Provided Separately)

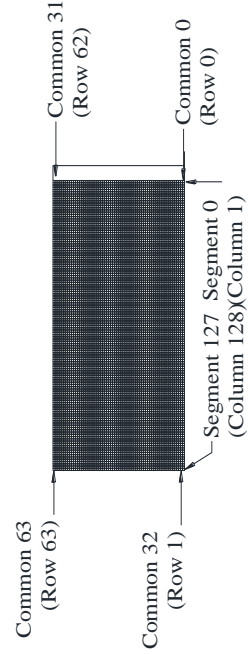
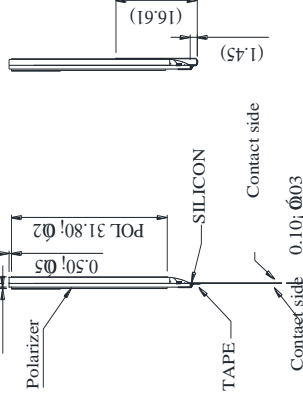
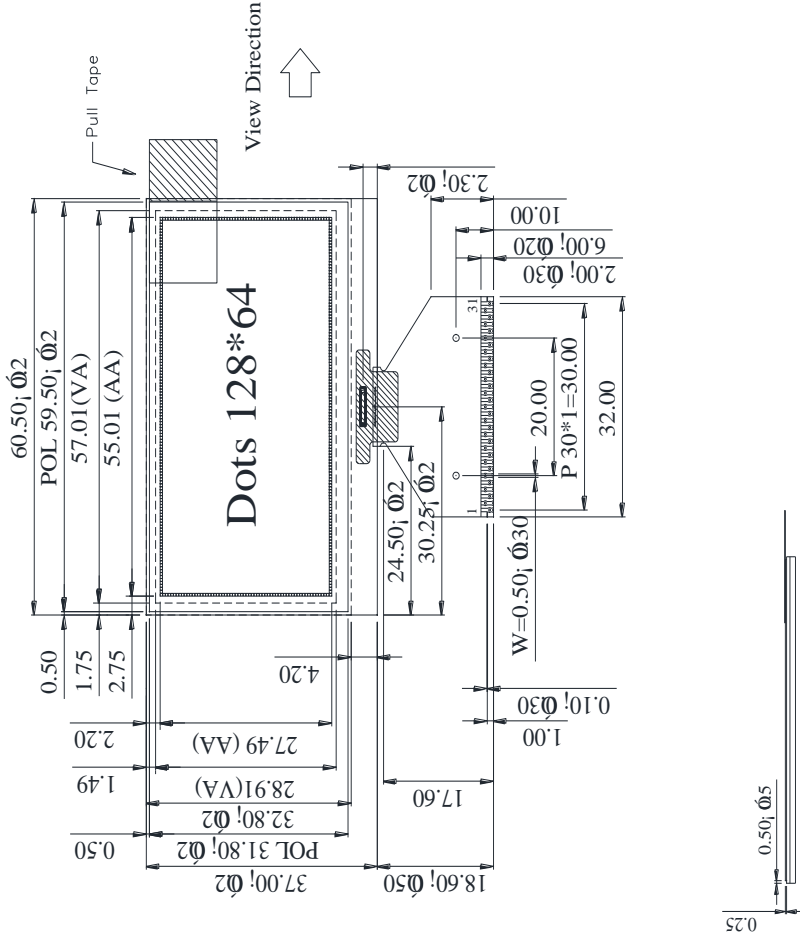
Display Accessories	
Part Number	Description
MPBV2	Direct solder-to-2mm pitch DIL pinout interface board. Compatible with any display with up to 51-way FFC 0.3~0.5mm pitch.
MCIB12	UC32 Breakout Board with SD card and LED back light driver. Used in conjunction with MPBV6.

Optional Variants	
Appearance	Voltage
Yellow on Black	
Green on Black	
Blue on Black	

Mechanical Specifications

Module Size	60.50 x 37.00 x 2.15 (Without Backlight)				W x H x D mm
Active Area	55.01 x 27.49	W x H mm	Hole-to-Hole	---	W x H mm
Dot Size	0.40 x 0.40	W x H mm	Dot Pitch	0.43 x 0.43	W x H mm

PIN NO.	SYMBOL	PIN NO.	SYMBOL
1	NC(GND)	17	CS#
2	VCC	18	NC
3	VCOMH	19	BS2
4	IREF	20	BS1
5	D7	21	VDD
6	D6	22	NC
7	D5	23	NC
8	D4	24	NC
9	D3	25	NC
10	D2	26	NC
11	D1	27	NC
12	D0	28	NC
13	E/RD#	29	NC
14	R/W#	30	VSS
15	D/C#	31	NC(GND)
16	RES#		



The non-specified tolerance of dimension is $\pm 0.3\text{mm}$.

Detail DOTS Scale 1:10

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Pin layout			
Pin	Symbol	Description	Remarks
1	NC	No Connection.	
2	VCC	Power supply driving voltage.	
3	VCOMH	COM signal deselected voltage level. Connect capacitor between here and VSS.	
4	IREF	Segment output current reference pin. Supplied externally.	
5-12	D0~D7	Bi-directional data bus connecting to the MCU data bus. Unused pins to tie Low. SPI Mode = D0 is Serial Clock input (SCLK) D1 will be Serial Data input (SDIN), D2 to be kept NC. I2C Mode = D2, D1 tied together serving as SDAout. SDAin in application and D0 is the Serial Clock input (SCL).	
13	E/RD#	MCU interface input. 6800 selected = Pin used as Enable (E) Signal. Read/write initiated when pin pulled High and chip selected. 8080 selected = Pin receives Read (RD#) Signal. Read initiated when pin pulled Low and chip selected. I2C / SPI selected = Connect to VSS.	
14	R/W#	Read / Write control input connecting to MCU interface. 6800 Mode = Pin used as Read/write (R/W#) selection input. Read mode when pin is pulled High; Write mode when pulled Low. 8080 Mode = Pin used as Write (WR#) input. Data Write initiated when pin pulled Low and chip selected. I2C / SPI selected = Connect to VSS.	
15	D/C#	Data / Command control pin connecting to MCU. Pulled High= D(7:0) interpreted as data. Pulled Low = D(7:0) transferred to a command register. I2C Mode = Pin acts as SA0 for slave address selection. 3-Wire SPI Mode = Connect to VSS	
16	RES#	Reset Signal Input. Initialisation executed when pulled Low. Keep pulled High during normal operation.	
17	CS#	Chip Select Input connecting to the MCU. Chip is enabled when CS# is pulled Low.	
18	NC	No Connection.	
19	BS2	MCU bus interface pins. Select appropriate logic settings: I2C: BS1= 1 BS2= 0	
20	BS1	4-Wire SPI: BS1= 0 BS2= 0 6800 Parallel: BS1= 0 BS2= 1 8800 Parallel: BS1= 1 BS2= 1	
21	VDD	Power Supply pin for core logic operation.	
22~29	NC	No Connection	
30	VSS	Ground	
31	NC	No Connection.	

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Absolute Maximums Ratings					
Item	Symbol	Minimum	Typical	Maximum	Unit
Supply Voltage for Logic	VDD	-0.30	---	4.00	V
Supply Voltage for Display	VCC	0.00	---	15.00	V
Operating Temperature	TOP	-40	---	80	°C
Storage Temperature	TSTG	-40	---	80	°C

Electronic Characteristics						
Item	Symbol	Condition	Minimum	Typical	Maximum	Unit
Input High Voltage	VIH	---	0.80xVDD	---	VDD	V
Input Low Voltage	VIL	---	GND	---	0.20xVDD	V
Output High Voltage	VOH	---	0.90xVDD	---	VDD	V
Output Low Voltage	VOL	---	GND	---	0.10xVDD	V
Supply Voltage for Logic	VDD	---	2.80	3.00	3.30	V
Supply Voltage for Display	VCC	---	12.00	13.00	14.00	V
50% Checkboard Operating Current.	IDD	VDD=13V	15	18	22	mA
CIE _x (White)	---	(CIE1931)	0.26	0.28	0.30	---
CIE _y (White)	---	(CIE1931)	0.30	0.32	0.34	---

OLED Characteristics						
Item	Symbol	Condition	Minimum	Typical	Maximum	Unit
Viewing Angle	(V)θ	---	160	---	---	Deg
	(H)φ	---	160	---	---	Deg
Contrast Ratio	CR	Dark	2000:1	---	---	---
Response Time	T Rise	---	---	10	---	μs
	T Fall	---	---	10	---	μs
Display with 50% Checkboard Brightness			70	90	---	cd/m ²

OLED Life Time			
Item	Conditions	Typical	Remark
Operating Life Time	T _a =25°C. Initial checkboard brightness, 50%.	50,000 Hours	---

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