

## Product Overview

### NL27WZ17: Dual Non-Inverting Buffer with Schmitt Trigger Input

For complete documentation, see the data sheet.

The NL27WZ17 is a high performance dual buffer operating from a 1.65 to 5.5 supply. At  $V_{CC} = 3.0$  V, high impedance TTL compatible inputs significantly reduce current loading to input drivers while the TTL compatible outputs offer improved switching noise performance.

### Features

- Extremely High Speed:  $t_{pd}$  2.0 ns (typical) at  $V_{CC} = 5.0$  V
- Designed for 1.65 V to 5.5 V  $V_{CC}$  Operation
- Overvoltage Tolerant Inputs
- LVTTTL Compatible - Interface Capability with 5.0 V TTL Logic with  $V_{CC} = 3.0$  V (2.7-3.3)
- LVCMOS Compatible
- 24 mA Balanced Output Sink and Source Capability at  $V_{CC} = 3.0$  V
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- Chip Complexity: FET = 72; Equivalent Gate = 18
- Hysteresis at the input for improved noise immunity

### Part Electrical Specifications

Product	Compliance	Status	Channels	Output	$V_{CC}$ Min (V)	$V_{CC}$ Max (V)	$t_{pd}$ Max (ns)	$I_O$ Max (mA)	Package Type
NL27WZ17DFT2G	Pb-free Halide free	Active	2	CMOS	1.65	5.5	2	24	SC-88-6 / SC-70-6 / SOT-363-6
NLV27WZ17DFT2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	2	CMOS	1.65	5.5	2	24	SC-88-6 / SC-70-6 / SOT-363-6

For more information please contact your local sales support at [www.onsemi.com](http://www.onsemi.com).

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