

12500 TI Boulevard, MS 8640, Dallas, Texas 75243

Notification# 20171204001 Datasheet for TUSB544 Information Only

Date: December 06, 2017 **To:** PREMIER FARNELL PCN

Dear Customer:

This is an information-only announcement of a change to the datasheet for a device that is currently offered by Texas Instruments.

The changes discussed within this notification are for your information only.

Any negotiated alternative change requirements will be provided via the customer's defined process. Customers with previously negotiated, special requirements will be handled separately. Any inquiries should be directed to your local Field Sales Representative.

For questions regarding this notice, contact your local Field Sales Representative or the PCN Manager (PCN www admin team@list.ti.com).

Sincerely,

PCN Team SC Business Services

Information Only Attachments

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, these are the devices that you have purchased within the past twenty-four (24) months. The corresponding customer part number is also listed, if available.

DEVICE TUSB544IRNQT **CUSTOMER PART NUMBER**

null

Technical details of this Product Change follow on the next page(s).

PCN	Number:	20171204001			PCN Date:	D	December 06, 2017			
Title: Datasheet for TUSB544										
Customer Contact: PCN Mana			<u>ager</u>				Dept:		Quality Services	
Cha	nge Type:									
	Assembly Site			Design				Wafer	Bump Site	Ì
	Assembly Process		\boxtimes	Data She	et			Wafer	Bump Material	
	Assembly Material	Materials Part number change Wafer Bump Process								
	Mechanical Specifi	cation		Test Site				Wafer	Fab Site	Ī
	Packing/Shipping/	Labeling		Test Proc	ess			Wafer	Fab Materials	
								Wafer	Fab Process	Ì
Notification Details										
Des	cription of Chang	je:								i
Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.										

TEXAS INSTRUMENTS

TUSB544

SLLSEZ0D - APRIL 2017 - REVISED OCTOBER 2017

CI	hanges from Revision C (October 2017) to Revision D	Page
,	Changed text of the second paragraph in the DESCRIPTION From: "cable and board trace loss due to intersymbol interference (ISI)" To: "inter symbol interference (ISI) due to cable and board trace loss."	1
	Changed Pin 2 and Pin 35 text From: "When I2C_EN !=0," To: "In I2C mode," in the Pin Functions	4
	Changed Pin 14 text From: "levels for the GPIO configuration" To: "levels for the 2-level GPIO configuration" in the <i>Pin Functions</i>	
	Changed Pin 17 in the text From: 0 = GPIO Mode (I ² C disabled) To: 0 = GPIO Mode AUX Snoop enabled (I ² C disabled) in the <i>Pin Functions</i>	5
	Changed Pins 21, 22, and 23 From: "When I2C_EN !=0," To: "In GPIO mode," in the Pin Functions	5
	Removed "When I2C_EN = 0" from pin 32.	5
	In pin 32, changed 2ms to t _{CTL1_DEBOUNCE}	5
	From: DEQ1 sets the high-frequency equalizer gain for downstream facing URX1, URX2, UTX1, UTX2 receivers. To: DEQ1 sets the high-frequency equalizer gain for downstream facing DRX1, DRX2, DTX1, DTX2 receivers	
	Deleted the MAX value of 10 ms from t _{CTL1_DEBOUNCE} in the <i>Switching Characteristics</i>	10
	Added test Condition " DP lanes will be disabled if low for greater than min value" for t _{CTL1_DEBOUNCE} in the Switching Characteristics	
	Changed text From: "There is an internal 30 kΩ pull-up and a 94kΩ pull-down." To: "There are internal pull-up and a pull-down resisters." in 4-Level Inputs	
	Changed text From: "when I2C_EN = "0"." To: "when I2C_EN = "0" or "F"." in the first paragraph of Device Configuration in GPIO Mode	19
	Changed Table 4	21
	Changed text From: "when I2C_EN is not equal to "0"." To: "when I2C_EN is equal to "1". " in Device Configuration in I2C Mode	28
	Changed text From: "When I2C_EN is '0'," To: :In I2C mode," in DisplayPort Mode	
	Changed text From: "When I2C_EN is '0'," To: :In GPIO mode," in Custom Alternate Mode	
	Deleted the Cable Mode section and all "cable mode" from datasheet.	

Changed Table 12							
Changed Bit 5-2 Type From: R/WU To: R/W in Table 15							
Changed Bit 7-0 Type From: R/WU To: R/W in Figure 25 and Table 16							
Changed Bit 7-0 Type From: R/WU To: R/W in Figure 26 and Table 17							
Changed Bit 6-0 Type From: RU To: RH in Figure 27 and Table 18							
Changed Figure 29 and Table 20							
Changed Bit 7-0 Type From: R/WU To: R/W in Figure 30 and Table 21							
Changed Bit 3-0 Type From: R/WU To: R/W in Figure 31 and Table 22							
Changed bit 7 From: R/WU To: RH in Figure 32 and Table 23							
USB3.1_# register default changed to 4h from 0h.							
Changed USB3.1_4 register default to 23h from 00h.							
Changed SBU1, and SBU2 pin labels on the Sink side of Figure 40							
Changed SBU1, and SBU2 pin labels on the Sink side of Figure 41							
Changed SBU1, and SBU2 pin labels on the Sink side of Figure 42							
Changed SBU1, and SBU2 pin labels on the Sink side of Figure 48							
Changed SBU1, and SBU2 pin labels on the Sink side of Figure 49							
Changed SBU1, and SBU2 pin labels on the Sink side of Figure 50							
The datasheet number will be changing.							
Device Family	Change From:	Change To:					
TUSB544	SLLSEZ0C	SLLSEZ0D					
These changes may be reviewed at the c	latasheet links provided.						
http://www.ti.com/product/TUSB544							
Reason for Change:							
To accurately reflect device characteristics.							
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):							
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.							
Changes to product identification resulting from this PCN:							
None.							
Product Affected:							

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

TUSB544IRNQT

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com

TUSB544RNQR

TUSB544IRNQR

TUSB544RNQT