# U2J Dielectric, 10 - 50 VDC (Commercial & Automotive Grade)



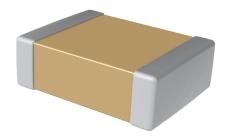
#### **Overview**

KEMET's U2J dielectric features a maximum operating temperature of 125°C and is considered stable. The Electronics Industries Alliance (EIA) characterizes U2J dielectric as a Class I material. Components of this classification are temperature compensating and are suited for resonant circuit applications or those where Q and stability of capacitance characteristics are required. U2J is an extremely stable dielectric material that exhibits a negligible shift in capacitance with respect to voltage and boasts a predictable and linear change in capacitance with reference to ambient temperature with no aging effect. In addition, U2J dielectric extends the available capacitance range of Class I MLCCs to achieve values previously only available using Class II dielectric materials like X7R, X5R, Y5V and Z5U.

U2J is not sensitive to DC Bias as compared to Class II dielectric materials and retains over 99% of nominal capacitance at full rated voltage. KEMET automotive grade series capacitors meet the demanding Automotive Electronics Council's AEC-Q200 qualification requirements. Capacitance change is limited to -750 ±120 ppm/°C from -55°C to +125°C. These devices are Lead-free, RoHS and REACH compliant without exception and are capable of withstanding multiple passes through a Lead-free solder reflow profile.

#### **Benefits**

- AEC-Q200 automotive qualified
- Low dissipation factor DF < 0.1%
- Low noise solution similar to COG
- · Low ESR and ESL
- High thermal stability
- High ripple current capability
- Preferred capacitance solution at line frequencies and into the MHz range
- Retains over 99% of nominal capacitance at full rated voltage



## **Ordering Information**

C	1206	C	104	J	3	J	A	C	TU
Ceramic	Case Size (L" x W")	Specification/ Series <sup>1</sup>	Capacitance Code (pF)	Capacitance Tolerance <sup>2</sup>	Rated Voltage (VDC)	Dielectric	Failure Rate/ Design	Termination Finish <sup>3</sup>	Packaging/ Grade (C-Spec)
	0402 0603 0805 1206 1210 1812	C = Standard	Two significant digits + number of zeros.	F = ±1% G = ±2% J = ±5% K = ±10% M = ±20%	8 = 10 4 = 16 3 = 25 5 = 50	J = U2J	A = N/A	C = 100% Matte Sn	See "Packaging C-Spec Ordering Options Table" below

<sup>&</sup>lt;sup>1</sup> Flexible termination option is available. Please see FT-CAP product bulletin C1087\_U2J\_FT-CAP\_SMD

<sup>&</sup>lt;sup>2</sup> Additional capacitance tolerance offerings may be available. Contact KEMET for details.



## **Packaging C-Spec Ordering Options Table**

Packaging Type <sup>1</sup>	Packaging/Grade Ordering Code (C-Spec)
Comme	rcial Grade <sup>1</sup>
Bulk Bag/Unmarked	Not required (Blank)
7" Reel/Unmarked	TU
13" Reel/Unmarked	7411 (EIA 0603 and smaller case sizes) 7210 (EIA 0805 and larger case sizes)
7" Reel/Unmarked/2 mm pitch <sup>2</sup>	7081
13" Reel/Unmarked/2 mm pitch <sup>2</sup>	7082
Automo	tive Grade <sup>3</sup>
7" Reel	AUTO
13" Reel/Unmarked	AUT07411 (EIA 0603 and smaller case sizes) AUT07210 (EIA 0805 and larger case sizes)
7" Reel/Unmarked/2mm pitch <sup>2</sup>	3190
13" Reel/Unmarked/2mm pitch <sup>2</sup>	3191

<sup>&</sup>lt;sup>1</sup> Default packaging is "Bulk Bag". An ordering code C-Spec is not required for "Bulk Bag" packaging.

#### Benefits cont'd

- Small predictable and linear capacitance change with respect to temperature
- Operating temperature range of -55°C to +125°C
- Capacitance up to 470 nF
- DC voltage ratings up to 50 V
- · Lead (Pb)-free, RoHS and REACH compliant
- Non-polar device, minimizing installation concerns
- 100% pure matte Tin-plated termination finish allowing for excellent solderability

<sup>&</sup>lt;sup>1</sup> The terms "Marked" and "Unmarked" pertain to laser marking option of capacitors. All packaging options labeled as "Unmarked" will contain capacitors that have not been laser marked. The option to laser mark is not available on these devices. For more information see "Capacitor Marking".

<sup>&</sup>lt;sup>2</sup> The 2 mm pitch option allows for double the packaging quantity of capacitors on a given reel size. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

<sup>&</sup>lt;sup>3</sup> Reeling tape options (paper or plastic) are dependent on capacitor case size (I" x w") and thickness dimensions. See "Chip Thickness/Tape & Reel Packaging Quantities" and "Tape & Reel Packaging Information".

<sup>&</sup>lt;sup>3</sup> For additional information regarding "AUTO" C-Spec options, see "Automotive C-Spec Information".

<sup>&</sup>lt;sup>3</sup> All automotive packaging C-Specs listed exclude the option to laser mark components. The option to laser mark <u>is not available</u> on these devices. For more information see "Capacitor Marking".



### **Applications**

Typical applications include critical timing, tuning, circuits requiring low loss, circuits with pulse, high current, decoupling, bypass, filtering, transient voltage suppression and blocking, as well as energy storage in critical and safety relevant circuits without (integrated) current limitation, including those subject to high levels of board flexure or temperature cycling.

#### **Qualification/Certification**

Commercial Grade products are subject to internal qualification. Details regarding test methods and conditions are referenced in Table 4, Performance and Reliability.

Automotive Grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in the document AEC-Q200, Stress Test Qualification for Passive Components. For additional information regarding the Automotive Electronics Council and AEC-Q200, please visit their website at www.aecouncil.com.

#### **Environmental Compliance**

Lead (Pb)-free, RoHS, and REACH compliant without exemptions.



### **Automotive C-Spec Information**

KEMET Automotive Grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC-Q200, Stress Test Qualification for Passive Components. These products are supported by a Product Change Notification (PCN) and Production Part Approval Process warrant (PPAP).

Automotive products offered through our distribution channel have been assigned an inclusive ordering code C-Spec, "AUTO". This C-Spec was developed in order to better serve small and medium sized companies that prefer an automotive grade component without the requirement to submit a customer Source Controlled Drawing (SCD) or specification for review by a KEMET engineering specialist. This C-Spec is therefore not intended for use by KEMET's OEM Automotive customers and are not granted the same "privileges" as other automotive C-Specs. Customer PCN approval and PPAP request levels are limited (see details below).

#### **Product Change Notification (PCN)**

The KEMET Product Change Notification system is used to communicate primarily the following types of changes:

- Product/process changes that affect product form, fit, function, and/or reliability
- · Changes in manufacturing site
- Product obsolescence

KEMET Automotive	Customer Notifica	tion due to:	Days prior to
C-Spec	Process/Product change	Obsolescence*	implementation
KEMET assigned <sup>1</sup>	Yes (with approval and sign off)	Yes	180 days Minimum
AUT0	Yes (without approval)	Yes	90 days Minimum

<sup>&</sup>lt;sup>1</sup> KEMET assigned C-Specs require the submittal of a customer SCD or customer specification for review. For additional information contact KEMET.

#### **Production Part Approval Process (PPAP)**

The purpose of the Production Part Approval Process is:

- To ensure that supplier can meet the manufacturability and quality requirements for the purchased parts.
- To provide the evidence that all customer engineering design record and specification requirements are properly understood and fulfilled by the manufacturing organization.
- To demonstrate that the established manufacturing process has the potential to produce the part

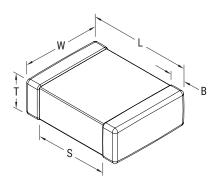
KEMET Automotive	I	PPAP (Product	Part Approval	Process) Leve	l
C-Spec	1	2	3	4	5
KEMET assigned <sup>1</sup>	•	•	•	•	•
AUT0	0		0		

<sup>&</sup>lt;sup>1</sup> KEMET assigned C-Specs require the submittal of a customer SCD or customer specification for review. For additional information contact KEMET.

- Part Number specific PPAP available
- Product family PPAP only



## **Dimensions - Millimeters (Inches)**



EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separation Minimum	Mounting Technique
0402	1005	1.00 (0.040) ±0.05 (0.002)	0.50 (0.020) ± 0.05 (0.002)		0.30 (0.012) ±0.10 (0.004)	0.30 (0.012)	Solder reflow only
0603	1608	1.60 (0.063) ±0.15 (0.006)	0.80 (0.032) ±0.15 (0.006)		0.35 (0.014) ±0.15 (0.006)	0.70 (0.028)	Colderwove
0805	2012	2.00 (0.079) ±0.20 (0.008)	1.25 (0.049) ±0.20 (0.008)	See Table 2	0.50 (0.02) ±0.25 (0.010)	0.75 (0.030)	Solder wave or Solder reflow
1206	3216	3.20 (0.126) ±0.20 (0.008)	1.60 (0.063) ±0.20 (0.008)	for thickness	0.50 (0.02) ±0.25 (0.010)	N/A	Solder Tellow
1210	3225	3.20 (0.126) ±0.20 (0.008)	2.50 (0.098) ±0.20 (0.008)		0.50 (0.02) ±0.25 (0.010)		Solder reflow
1812	4532	4.50 (0.177) ±0.30 (0.012)	3.20 (0.126) ±0.30 (0.012)		0.60 (0.024) ±0.35 (0.014)		only

## **Electrical Parameters/Characteristics**

Item	Parameters/Characteristics
Operating Temperature Range	-55°C to +125°C
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	-750 ±120 ppm/°C
Aging Rate (Maximum % Capacitance Loss/Decade Hour)	0.1%
Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5 ±1 seconds and charge/discharge not exceeding 50 mA)
Dissipation Factor (DF) Maximum Limit at 25°C	0.1%
Insulation Resistance (IR) Limit at 25°C	1,000 megohm microfarads or 100 GΩ (Rated voltage applied for 120 ±5 seconds at 25°C)

To obtain IR limit, divide  $M\Omega$ - $\mu$ F value by the capacitance and compare to  $G\Omega$  limit. Select the lower of the two limits. Capacitance and dissipation factor (DF) measured under the following conditions:

1 MHz  $\pm$ 100 kHz and 1.0 Vrms  $\pm$ 0.2 V if capacitance  $\leq$  1,000 pF

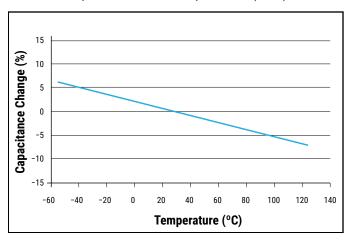
Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

<sup>1</sup> kHz ±50 Hz and 1.0 Vrms ±0.2 V if capacitance > 1,000 pF

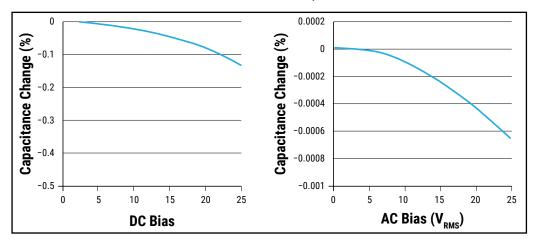


## **Electrical Characteristics (Typical)**

### Capacitance vs. Temperature (TCC)



## DC & AC Bias Effective Capacitance



## **Post Environmental Limits**

F	High Temperature Life, Biased Humidity, Moisture Resistance														
Dielectric	Rated DC Voltage	Capacitance Value	Dissipation Factor (Maximum %)	Capacitance Shift	Insulation Resistance										
U2J	All	All	0.5	0.3% or ±0.25 pF	10% of Initial Limit										



## Table 1A - Capacitance Range/Selection Waterfall (0402 - 1812 Case Sizes)

100 pF 110 pF 110 pF 120 pF 130 pF 150 pF 160 pF 180 pF 200 pF 220 pF 240 pF 270 pF 330 pF 360 pF 390 pF 470 pF 510 pF 560 pF 680 pF	Cap Code 101 111 121 131 151 161 181 201 221 241 271 301 331 361 391 431 471 511 561	Ca	I Vol	Itag citageran	K I K I K I K I K I K I K I K I K I K I		8 BB	4 91 BB BB BB BB BB BB BB BB BB	3	5 05 BB BB BB BB BB BB	10	Proc Se	3 52 Juct A	5 G Avail ole 2	8 2 abilit for C	4 ≗ y and hip T	3 52 Chir hicki	5 C Thio	8 2 cknes Dime	4 9 ss Co	3 Sodes	5 09	10 8	4 92	25 8	5 09	10 8	4 91	52 ε	5 09
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110 pF 120 pF 130 pF 150 pF 160 pF 180 pF 200 pF 220 pF 240 pF 270 pF 330 pF 330 pF 330 pF 340 pF 470 pF 510 pF 560 pF	111 121 131 151 161 181 201 221 241 271 301 331 361 391 431 471 511	Ca	Toler G	cita erar i	K I K I K I K I K I K I K I K I K I K I		BB BB BB BB BB BB BB	BB BB BB BB BB BB	BB BB BB BB BB BB	BB BB BB BB BB	-	Proc	luct	Avail	abilit	y and	Chip	Thic	cknes	ss Co	des				- 2	- C		_	7	LI,
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750 pF	751		FG		1 1		BB	BB	BB	BB	i																			
820 pF	821		FG		K		BB	BB	BB	BB	i																			
910 pF	911		FG				BB	BB	BB	BB	i																			
1,000 pF	102		FG				BB	BB	BB	BB	CF	CF	CF	CF																
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1,800 pF	182		F G	J	K	M	BB	BB	BB	BB	CF	CF	CF	CF																
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2,200 pF	222		FG	J	K	и	BB	BB	BB		CF	CF	CF	CF																
2,400 pF	242		FG	J	K	И					CF	CF	CF	CF																
2,700 pF	272		FG	J	_	И					CF	CF	CF	CF																
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3,600 pF	362		FG	- 1		۱ I					CF	CF	CF	CF																
3,900 pF	392		FG								CF	CF	CF	CF																
4,300 pF	432		F G		K						CF	CF	CF	CF	DN	110	D11	DN												
4,700 pF	472				K						CF	CF	CF			DN														
5,100 pF	512		FG								CF	CF	CF	CF	DN	DN	DN	DN												
5,600 pF	562		FG								CF	CF	CF	CF	DN	DN	DN	DN												
6,200 pF 6,800 pF	622		F G F G								CF CF	CF CF	CF CF	CF CF	DN	DN	DN	DN												
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12,000 pF	123				K						CF	CF	CF	OI	DN	DN	DN	DN	EB	EB	EB	EB	FB	FB	FB	FB	GB	GB	GB	
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Capacitance C	Cap Code				ode		8	4	3	5	8	4	3	5	8	4	3	5	8	4	3	5	8	4	3	5	8	4	3	5
' "		Cas				es		C04	02C			C06	020			C08		$\dashv$			06C	-+	-	C12			┢		12C	



## Table 1A - Capacitance Range/Selection Waterfall (0402 - 1812 Case Sizes) cont'd

	Con	Case Size/ Series		C04	020	;		C06	030	;		C08	050	;		C12	060	;		C12	100	;		C18	12C	
Capacitance	Cap	Voltage Code	8	4	3	5	8	4	3	5	8	4	3	5	8	4	3	5	8	4	3	5	8	4	3	5
	Code	Rated Voltage (VDC)	10	16	25	20	10	16	25	20	10	16	25	20	10	16	25	20	10	16	25	20	10	16	25	20
		Capacitance Tolerance												p Thi ness												
15,000 pF	153	F G J K M					CF	CF	CF		DN	DN	DN	DN	EB	EB	EB	EB	FB	FB	FB	FB	GB	GB	GB	
18,000 pF	183	F G J K M									DN	DN	DN	DN	EB	EB	EB	EB	FB	FB	FB	FB	GB	GB	GB	
22,000 pF	223	F G J K M									DN	DN	DN	DP	EB	EB	EB	EB	FB	FB	FB	FB	GB	GB	GB	
27,000 pF	273	F G J K M									DP	DP	DP	DP	EB	EB	EB	EB	FB	FB	FB	FB	GB	GB	GB	
33,000 pF	333	F G J K M									DP	DP	DP	DG	EB	EB	EB	EB	FB	FB	FB	FB	GB	GB	GB	
47,000 pF	393	F G J K M									DG	DG	DG	DG	EB	EB	EB	EB	FB	FB	FB	FB	GB	GB	GB	
47,000 pF	473	F   G   J   K   M									DG	DG	DG	DG	EB	EB	EB	EB	FB	FB	FB	FB	GB	GB	GB	
56,000 pF	563	F G J K M									DG	DG	DG		EB	EB	EB	EC	FB	FB	FB	FB	GB	GB	GB	
68,000 pF	683	F G J K M													EC	EC	EC	EC	FB	FB	FB	FB	GB	GB	GB	
82,000 pF	823	F G J K M													EC	EC	EC	EE	FB	FB	FB	FB	GB	GB	GB	
100,000 pF	104	F G J K M													EC	EC	EC	EF	FB	FB	FB	FC	GB	GB	GB	
120,000 pF	124	F G J K M													EF	EF	EF	EH	FC	FC	FC	FE	GB	GB	GB	
150,000 pF	154	F G J K M													EF	EF	EF	EH	FE	FE	FE	FG	GB	GB	GB	
180,000 pF	184	F G J K M													EH	EH	EH		FG	FG	FG	FG	GB	GB	GB	
220,000 pF	224	F G J K M													EH	EH	EH		FG	FG	FG	FH	GB	GB	GB	
270,000 pF	274	F G J K M																	FH	FH	FH	FM	GB	GB	GB	
330,000 pF	334	F G J K M													1				FM	FM	FM		GC	GC	GC	
390,000 pF	394	F G J K M													1								GH	GH	GH	
470,000 pF	474	F G J K M																					GK	GK	GK	
		Rated Voltage (VDC)	2	16	25	20	10	16	25	20	10	16	25	20	10	9	25	20	10	16	25	20	9	16	25	20
Capacitance	Cap Code	Voltage Code	8	4	3	5	8	4	3	5	8	4	3	5	8	4	3	5	8	4	3	5	8	4	3	5
		Case Size/Series		C04	02C			C06	03C			C08	05C			C12	06C			C12	10C			C18	12C	



## Table 2A - Chip Thickness/Tape & Reel Packaging Quantities

Thickness	Case	Thickness ±	Paper Q	uantity <sup>1</sup>	Plastic (	Quantity
Code	Size <sup>1</sup>	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel
BB	0402	0.50 ±0.05	10,000	50,000	0	0
CF	0603	0.80 ±0.07*	4,000	15,000	0	0
DN	0805	0.78 ±0.10*	4,000	15,000	0	0
DP	0805	0.90 ±0.10*	4,000	15,000	0	0
DG	0805	1.25 ±0.15	0	0	2,500	10,000
EB	1206	0.78 ±0.10	4,000	10,000	4,000	10,000
EC	1206	0.90 ±0.10	0	0	4,000	10,000
EE	1206	1.10 ±0.10	0	0	2,500	10,000
EF	1206	1.20 ±0.15	0	0	2,500	10,000
EH	1206	1.60 ±0.20	0	0	2,000	8,000
FB	1210	0.78 ±0.10	0	0	4,000	10,000
FC	1210	0.90 ±0.10	0	0	4,000	10,000
FE	1210	1.00 ±0.10	0	0	2,500	10,000
FG	1210	1.25 ±0.15	0	0	2,500	10,000
FH	1210	1.55 ±0.15	0	0	2,000	8,000
FM	1210	1.70 ±0.20	0	0	2,000	8,000
GB	1812	1.00 ±0.10	0	0	1,000	4,000
GC	1812	1.10 ±0.10	0	0	1,000	4,000
GH	1812	1.40 ±0.15	0	0	1,000	4,000
GK	1812	1.60 ±0.20	0	0	1,000	4,000
Thickness	Case	Thickness ±	7" Reel	13" Reel	7" Reel	13" Reel
Code	Size <sup>1</sup>	Range (mm)	Paper Q	uantity¹	Plastic (	Quantity

Package quantity based on finished chip thickness specifications.

## Table 2B - Bulk Packaging Quantities

Dookogi	ing Type	Loose Pa	ackaging
Packagi	ing Type	Bulk Bag	(default)
Packagin	g C-Spec¹	N/	'A²
Case	Size	<b>Packaging Quantities (</b>	pieces/unit packaging)
EIA (in)	Metric (mm)	Minimum	Maximum
0402	1005		
0603	1608		
0805	2012	1	50,000
1206	3216	<u>'</u>	
1210	3225		
1812	4532		20,000

<sup>&</sup>lt;sup>1</sup> The "Packaging C-Spec" is a 4 to 8 digit code which identifies the packaging type and/or product grade. When ordering, the proper code must be included in the 15th through 22nd character positions of the ordering code. See "Ordering Information" section of this document for further details. Commercial Grade product ordered without a packaging C-Spec will default to our standard "Bulk Bag" packaging. Contact KEMET if you require a bulk bag packaging option for Automotive Grade products.

<sup>&</sup>lt;sup>1</sup> If ordering using the 2 mm Tape and Reel pitch option, the packaging quantity outlined in the table above will be doubled. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

<sup>&</sup>lt;sup>2</sup> A packaging C-Spec (see note 1 above) is not required for "Bulk Bag" packaging (excluding Anti-Static Bulk Bag and Automotive Grade products). The 15th through 22nd character positions of the ordering code should be left blank. All product ordered without a packaging C-Spec will default to our standard "Bulk Bag" packaging.



## Table 3 - Chip Capacitor Land Pattern Design Recommendations per IPC-7351

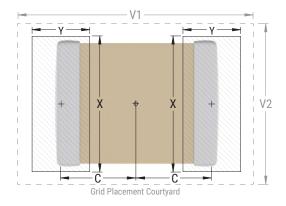
EIA Size	Size Size Code Code Code Code Code Code Code Cod							Media	sity Lev an (Nor otrusio		)	Density Level C: Minimum (Least) Land Protrusion (mm)							
Coue	Code	С	Υ	X	<b>V</b> 1	V2	С	Y	X	V1	V2	С	Y	X	V1	V2			
0402	1005	0.50	0.72	0.72	2.20	1.20	0.45	0.62	0.62	1.90	1.00	0.40	0.52	0.52	1.60	0.80			
0603	1608	0.90	1.15	1.10	4.00	2.10	0.80	0.95	1.00	3.10	1.50	0.60	0.75	0.90	2.40	1.20			
0805	2012	1.00	1.35	1.55	4.40	2.60	0.90	1.15	1.45	3.50	2.00	0.75	0.95	1.35	2.80	1.70			
1206	3216	1.60	1.35	1.90	5.60	2.90	1.50	1.15	1.80	4.70	2.30	1.40	0.95	1.70	4.00	2.00			
1210	3225	1.60	1.35	2.80	5.65	3.80	1.50	1.15	2.70	4.70	3.20	1.40	0.95	2.60	4.00	2.90			
1210¹	3225	1.50	1.60	2.90	5.60	3.90	1.40	1.40	2.80	4.70	3.30	1.30	1.20	2.70	4.00	3.00			
1812	4532	2.15	1.60	3.60	6.90	4.60	2.05	1.40	3.50	6.00	4.00	1.95	1.20	3.40	5.30	3.70			

<sup>&</sup>lt;sup>1</sup> Only for capacitance values ≥ 22 μF

**Density Level A:** For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805 and 1206 case sizes.

**Density Level B:** For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. **Density Level C:** For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC-7351).

Image below based on Density Level B for an EIA 1210 case size.





## **Soldering Process**

#### **Recommended Soldering Technique:**

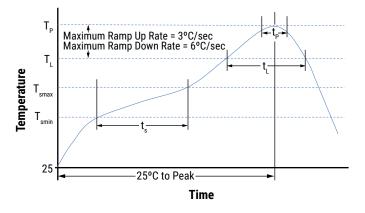
- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- · All other EIA case sizes are limited to solder reflow only

#### **Recommended Reflow Soldering Profile:**

KEMET's families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three reflow passes at these conditions.

Profile Feature	Termination Finish			
rionie i catale	SnPb	100% Matte Sn		
Preheat/Soak				
Temperature Minimum (T <sub>Smin</sub> )	100°C	150°C		
Temperature Maximum (T <sub>Smax</sub> )	150°C	200°C		
Time $(t_s)$ from $T_{smin}$ to $T_{smax}$	60 – 120 seconds	60 – 120 seconds		
Ramp-Up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second maximum	3°C/second maximum		
Liquidous Temperature (T <sub>L</sub> )	183°C	217°C		
Time Above Liquidous (t <sub>L</sub> )	60 - 150 seconds	60 - 150 seconds		
Peak Temperature (T <sub>P</sub> )	235°C	260°C		
Time Within 5°C of Maximum Peak Temperature (t <sub>P</sub> )	20 seconds maximum	30 seconds maximum		
Ramp-Down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second maximum	6°C/second maximum		
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum		

Note 1: All temperatures refer to the center of the package, measured on the capacitor body surface that is facing up during assembly reflow.





#### Table 4 - Performance & Reliability: Test Methods and Conditions

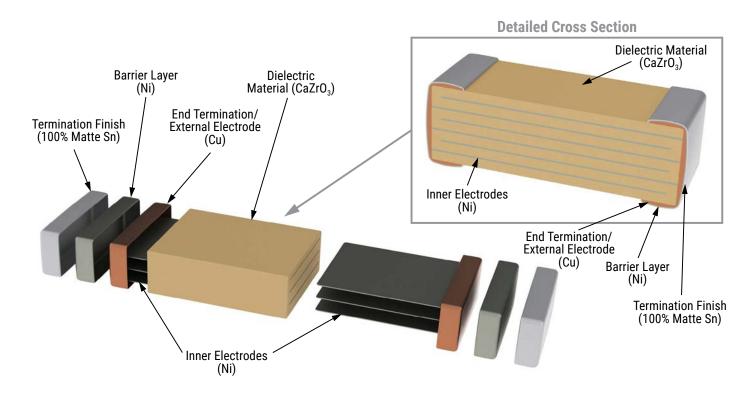
Stress	Reference	Test or Inspection Method							
			Package Size (L" x W")	Force	Duration				
Terminal Strength	JIS-C-6429	Appendix 1, Note:	0402	5 N (0.51 kg)					
			0603	10 N (1.02 kg)	60 seconds				
			≥ 0805	18 N (1.83 kg)					
Board Flex	JIS-C-6429	Appendix 2, Note: 3.0 mm	(minimum).						
		Magnification 50 X Condi	tions:						
Caldanah ilitu	L CTD 000	a) Method B, 4 hour	s at 155°C, dry heat at 235	5°C					
Solderability	J-STD-002	b) Method B at 215°C category 3							
		c) Method D, catego	c) Method D, category 3 at 260°C						
Temperature Cycling	JESD22 Method JA-104	1,000 cycles (-55°C to +125°C). Measurement at 24 hours ±4 hours after test conclusion.							
D: 111 :15	MII OTD 000 M .I. 1400	Load Humidity: 1,000 hou Measurement at 24 hours	rs 85°C/85% RH and rated ±4 hours after test conclu	voltage. Add 10 usion.	0 K ohm resistor.				
Biased Humidity	MIL-STD-202 Method 103	Low Volt Humidity: 1,000 Measurement at 24 hours			hm resistor.				
Moisture Resistance	MIL-STD-202 Method 106	t = 24 hours/cycle. Steps test conclusion.	t = 24 hours/cycle. Steps 7a and 7b not required. Measurement at 24 hours ±4 hours after						
Thermal Shock	MIL-STD-202 Method 107	-55°C/+125°C. Note: Num seconds. Dwell time - 15	ber of cycles required – 3 minutes. Air – Air.	00. Maximum tra	nsfer time – 20				
High Temperature Life	MIL-STD-202 Method 108/EIA -198	1,000 hours at 125°C with 2 X rated voltage applied.							
Storage Life	MIL-STD-202 Method 108	125°C, 0 VDC for 1,000 hours.							
Vibration	MIL-STD-202 Method 204	5 G's for 20 minutes, 12 cycles each of 3 orientations. Note: Use 8" X 5" PCB 0.031" thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted within 2" from any secure point. Test from 10 – 2,000 Hz							
Mechanical Shock	MIL-STD-202 Method 213	Figure 1 of Method 213, Condition F.							
Resistance to Solvents	MIL-STD-202 Method 215	Add aqueous wash chemical, OKEM clean or equivalent.							

## **Storage and Handling**

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature – reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



#### Construction



## **Capacitor Marking (Optional):**

Laser marking option is not available on:

- · COG, U2J, Ultra Stable X8R, and Y5V dielectric devices
- · EIA 0402 case size devices
- EIA 0603 case size devices with Flexible Termination option.
- KPS Commercial and Automotive grade stacked devices.

These capacitors are supplied unmarked only.



### **Tape & Reel Packaging Information**

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

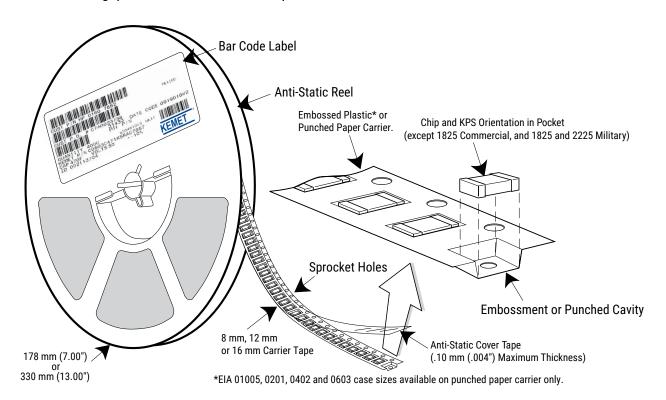


Table 5 - Carrier Tape Configuration, Embossed Plastic & Punched Paper (mm)

	Tape	Embosse	d Plastic	Punched Paper			
<b>EIA Case Size</b>	Size	7" Reel	13" Reel	7" Reel	13" Reel		
	(W)*	Pitch	Pitch (P <sub>1</sub> )*		Pitch (P <sub>1</sub> )*		
01005 - 0402	8			2	2		
0603	8			2/4	2/4 -		
0805	8	4	4	4	4		
1206 - 1210	8	4	4	4	4		
1805 - 1808	12	4	4				
≥ 1812	12	8	8				
KPS 1210	12	8	8				
KPS 1812 & 2220	16	12	12				
Array 0508 & 0612	8	4	4				

<sup>\*</sup>Refer to Figures 1 and 2 for W and  $P_1$  carrier tape reference locations.

#### **New 2 mm Pitch Reel Options\***

	Packaging Ordering Code (C-Spec)	Packaging Type/Options
1	C-3190	Automotive grade 7" reel unmarked
	C-3191	Automotive grade 13" reel unmarked
	C-7081	Commercial grade 7" reel unmarked
	C-7082	Commercial grade 13" reel unmarked

<sup>\* 2</sup> mm pitch reel only available for 0603 EIA case size. 2 mm pitch reel for 0805 EIA case size under development.

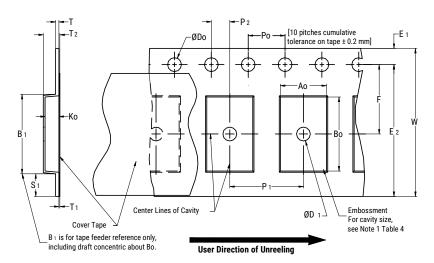
#### Benefits of Changing from 4 mm to 2 mm Pitching Spacing

- · Lower placement costs
- Double the parts on each reel results in fewer reel changes and increased efficiency
- Fewer reels result in lower packaging, shipping and storage costs, reducing waste

<sup>\*</sup>Refer to Tables 6 and 7 for tolerance specifications.



## Figure 1 - Embossed (Plastic) Carrier Tape Dimensions



## **Table 6 - Embossed (Plastic) Carrier Tape Dimensions**

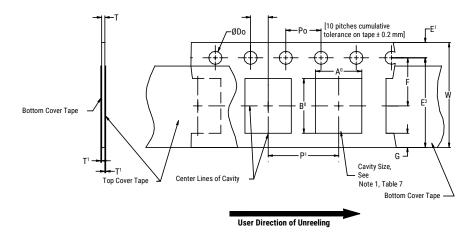
Metric will govern

	Constant Dimensions — Millimeters (Inches)								
Tape Size	D <sub>0</sub>	D <sub>1</sub> Minimum Note 1	E <sub>1</sub>	P <sub>0</sub>	P <sub>2</sub>	R Reference Note 2	S <sub>1</sub> Minimum Note 3	T Maximum	T <sub>1</sub> Maximum
8 mm		1.0 (0.039)				25.0 (0.984)			
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.5	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm	(0.059)		(0.059)			(1.181)			
	Variable Dimensions — Millimeters (Inches)								
Tape Size	Tape Size Pitch $B_1$ Maximum $E_2$ F $P_1$ $Maximum$ $A_0$ , $B_0$ and K							ınd K <sub>o</sub>	
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) and Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Not	e 5
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5 ±0.05 (0.138 ±0.002)	12.0 ±0.10 (0.157 ±0.004)	4.6 (0.181)	16.3 (0.642)		

- 1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.
- 2. The tape with or without components shall pass around R without damage (see Figure 6).
- 3. If  $S_1 < 1.0$  mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481 paragraph 4.3 section b).
- 4. B, dimension is a reference dimension for tape feeder clearance only.
- 5. The cavity defined by  $A_{\alpha}$ ,  $B_{\alpha}$  and  $K_{\alpha}$  shall surround the component with sufficient clearance that:
  - (a) the component does not protrude above the top surface of the carrier tape.
  - (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
  - (c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 3).
  - (d) lateral movement of the component is restricted to 0.5 mm maximum for 8 and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 4).
  - (e) for KPS Series product, A, and B, are measured on a plane 0.3 mm above the bottom of the pocket.
  - (f) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.



## Figure 2 - Punched (Paper) Carrier Tape Dimensions



## **Table 7 - Punched (Paper) Carrier Tape Dimensions**

Metric will govern

	Constant Dimensions — Millimeters (Inches)									
Tape Size	D <sub>0</sub>	E <sub>1</sub>	P <sub>0</sub>	P <sub>2</sub>	T <sub>1</sub> Maximum	G Minimum	R Reference Note 2			
8 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	0.10 (0.004) Maximum	0.75 (0.030)	25 (0.984)			
	Variable Dimensions — Millimeters (Inches)									
Tape Size	Tape Size Pitch E2 Minimum F P <sub>1</sub> T Maximum W Maximum A <sub>0</sub> B <sub>0</sub>									
8 mm	Half (2 mm)	6.25	3.5 ±0.05	2.0 ±0.05 (0.079 ±0.002)	1.1	8.3 (0.327)	Note 1			
8 mm	Single (4 mm)	(0.246)	(0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	(0.098)	8.3 (0.327)	Note 1			

- 1. The cavity defined by  $A_{n}$ ,  $B_{n}$  and T shall surround the component with sufficient clearance that:
  - a) the component does not protrude beyond either surface of the carrier tape.
  - b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
  - c) rotation of the component is limited to 20° maximum (see Figure 3).
  - d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4).
  - e) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.
- 2. The tape with or without components shall pass around R without damage (see Figure 6).



## **Packaging Information Performance Notes**

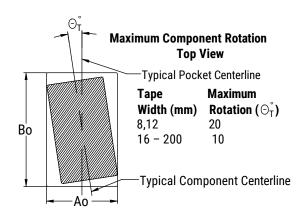
- 1. Cover Tape Break Force: 1.0 kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8 mm	0.1 to 1.0 Newton (10 to 100 gf)
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ±10 mm/minute.

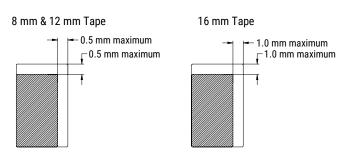
**3. Labeling:** Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards 556 and 624*.

### **Figure 3 – Maximum Component Rotation**



#### 

## Figure 4 - Maximum Lateral Movement



## Figure 5 - Bending Radius

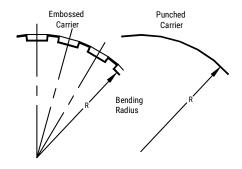
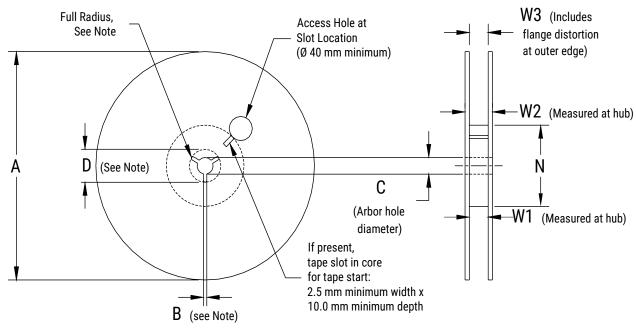




Figure 6 - Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

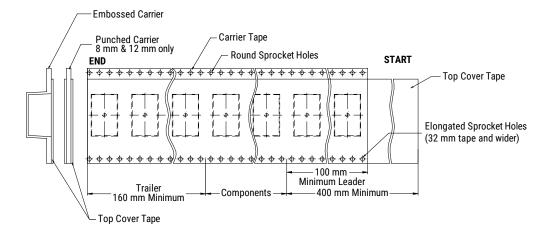
#### **Table 8 - Reel Dimensions**

Metric will govern

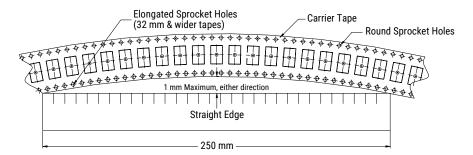
Constant Dimensions — Millimeters (Inches)									
Tape Size	A	B Minimum	С	D Minimum					
8 mm	178 ±0.20								
12 mm	(7.008 ±0.008) or	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)					
16 mm	330 ±0.20 (13.000 ±0.008)	(****)	(	()					
	Variable Dimensions — Millimeters (Inches)								
Tape Size	N Minimum	W <sub>1</sub>	W <sub>2</sub> Maximum	W <sub>3</sub>					
8 mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)						
12 mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference					
16 mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)						



## Figure 7 - Tape Leader & Trailer Dimensions



## Figure 8 – Maximum Camber





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