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## PRODUCT CHANGE NOTIFICATION

**PCN:** PCN181103

**Date:** March 18, 2018

**Subject:** Qualification of Texas Instruments' DMOS6 as an Additional Wafer Fab Site and Cypress's Test 25 as an Additional Wafer Sort Site for 4Mb Serial F-RAM Industrial-Grade Products

**To:** GENERAL INBOX  
PREMIER FARNELL  
ProductChangeNotices@premierfarnell.com

**Change Type:** Major

### **Description of Change:**

Cypress announces the qualification of Texas Instruments' DMOS6 as an additional wafer fab site and Test 25 as an additional wafer sort site for its 4Mb F-RAM Serial (SPI) Industrial-grade Product Family. This qualification is part of the flexible manufacturing initiative which allows Cypress to meet its delivery commitments in dynamic, changing market conditions.

### **Benefit of Change:**

Qualification of alternate manufacturing sites is part of the ongoing flexible manufacturing initiative announced by Cypress. The goal of the flexible manufacturing initiative is to provide the means for Cypress to continue to meet delivery commitments through dynamic, changing market conditions.

### **Part Numbers Affected: 5**

See the attached 'Affected Parts List' file for a list of all part numbers affected by this change. Note that any new parts that are introduced after the publication of this PCN will include all changes outlined in this PCN.

### **Qualification Status:**

The additional wafer fab and wafer sort sites have been qualified through a series of tests documented in Qualification Test Plans QTP#170902 and QTP#170503. These qualification reports can be found as attachments to this PCN or by visiting [www.cypress.com](http://www.cypress.com) and typing the QTP number in the keyword search window.

### **Sample Status:**

Qualification samples may not be built ahead of time for all part numbers affected by this change. Please review the attached 'Affected Parts List' file for a list of affected part numbers with their associated sample ordering part numbers. Samples are available now unless there is an indication that the sample ordering part numbers are subject to lead times. If you require

qualification samples, please contact your local Cypress sales representative as soon as possible, preferably within 30 days of the date of this PCN, to place any sample orders.

**Approximate Implementation Date:**

Effective 90 days from the date of this notification or upon customer approval, whichever comes first, all shipments of the affected part numbers in the attached file will be supplied from DMOS6 or other approved wafer fabrication sites and will be sort tested at Test 25 or other approved wafer sort sites.

**Anticipated Impact:**

Products fabricated at DMOS6 and sorted at Test 25 are completely compatible with the existing product from a form, fit, functional, parametric and quality performance perspectives.

**Method of Identification:**

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package and through a revision letter on the die.

**Response Required:**

No response is required.

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at [pcn\\_adm@cypress.com](mailto:pcn_adm@cypress.com).

Sincerely,

Cypress PCN Administration