

Title of Change:	Datasheet update for NCV5700/NCD5700/01 series, DESAT charge current.					
Effective date:	4 April 2018					
Contact information:	Contact your local ON Semiconductor Sales Office or < <u>Zarfaq.Jaffery@onsemi.com</u> >					
Type of notification:	This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with implementation of this change upon publication of this Product Bulletin.					
Change category:	🔲 Wafer Fab Change 🛛 Assembly Change	🛛 Test Change 🔲 Other				
Change Sub-Category(s):         Manufacturing Site Change/Addition         Manufacturing Process Change         Product specific change		<ul> <li>Datasheet/Product Doc change</li> <li>Shipping/Packaging/Marking</li> <li>Other:</li> </ul>				
Sites Affected:	ON Semiconductor Sites: ON Carmona, Philippines	External Foundry/Subcon Sites: None				
Description and Purpose:						

# Based on additional test data, we are widening the limits for parameter I<sub>DESAT-CHG</sub> to improve the yield. There is no change to the current product form, fit, or function. The new specification reflects a guard banded limit, enabling ON Semiconductor maintain its high quality standards.

## **New Datasheet**

#### DESAT PROTECTION

DESAT Threshold Voltage	V <sub>DESAT</sub> -THR	6.0	6.35	7.0	V
Blanking Charge Current	IDESAT-CHG	0.20	0.24	0.28	mA
Blanking Discharge Current	IDESAT-DIS		30		mA

## **Current Datasheet**

DESAT PROTECTION

DESAT Threshold Voltage	VDESAT-THR	6.0	6.35	7.0	۷
Blanking Charge Current	IDESAT-CHG	0.22	0.25	0.28	mA
Blanking Discharge Current	IDESAT-DIS		30		mA

### List of Affected Standard Parts:

NCD5700DR2G NCD5701ADR2G NCD5701BDR2G NCD5701CDR2G NCV5700DR2G SNCV5700DR2G