



FEATURES

- Peregrine Semiconductor PE22100 compatible
- 3.3V and 5V versions
- Isolation tested to 4kVDC
- Toroidal construction
- Low profile
- Industry-standard pinout
- UL 94 V-0 package materials
- Industrial temperature range
- J-STD-020D reflow (SM versions)
- RoHS compliant
- Backwards compatible with Sn/Pb soldering systems

DESCRIPTION

The 782100 series of converter transformers are specifically designed for use with the Peregrine Semiconductor PE22100 chip set to provide isolated power supplies for transceivers in isolated interface applications.

782100 Series

PE22100 Compatible Converter Transformers

SELECTION GUIDE							
Order Code	Input Voltage	· voltage		ation ² tage	Turns Ratio	Package Type	
7004 00 /001/0	V V Vr			1071 0007			
782100/33VC	3.3	3.3		000	1CT:1.33CT	DIL	
782100/53VC	5	3.3		000	1.14CT:1CT 1:2.14		
782100/35JVC	3.3	5.0		000		1:2.14 SM 1:1.33	
782100/55JVC	5.0	5.0	40	000	1:1.33		
ORDER CODE DETAILS							
Order Code	Package 1	Туре	Packaging Type		Quantity		
782100/XX(J)VC	6 Pin (SM) / DIL		Tube		50		
782100/XXJVC-R	6 Pin SM		Таре	Tape & Reel		500	
782100/33VC CHARACT	ERISTICS						
Parameter		Cond	Conditions		Max.	Units	
Primary Inductance, L _P ¹		100kl	100kHz, 20mV		310	μH	
Leakage Inductance, L ¹			100kHz, 250mV		8	μH	
Inter winding Capacitance, C _{ww}		100kl	100kHz, 250mV		11	pF	
Primary D.C. Resistance, R _{pc} ¹		<0.1	<0.1VDC		600	mΩ	
Volt-time Product, Et		Pins 1	Pins 1/2 or 2/3 13			Vµs	
782100/53VC CHARACT	FRISTICS						
Parameter		Cond	Conditions		Max.	Units	
Primary Inductance, L_p^{-1}			100kHz, 20mV		260	μΗ	
Leakage Inductance, L_{p}^{1}			100kHz, 250mV		8	μΗ	
Inter winding Capacitance, C _{ww}			100kHz, 250mV		8	pF	
Primary D.C. Resistance, R _{pc} ¹			<0.1VDC		600	mΩ	
Volt-time Product. Et		Pins 1	Pins 1/2 or 2/3 13			Vµs	
782100/35JVC CHARAC	TEDIOTION						
Parameter	TERISTICS	Cond	itions	Min.	Max.	Units	
			Conditions 100kHz, 20mV		203	μH	
Primary Inductance, L _p ¹ Leakage Inductance, L ₁ ¹			100kHz, 250mV		5	μΗ	
Inter winding Capacitance, C _{ww}			100kHz, 250mV		8	pF	
Primary D.C. Resistance, R_{pc}^{-1}			>0.1VDC		600	mΩ	
Volt-time Product, Et			Pins 1/2 or 2/3 18		000	Vµs	
		1 113		10		νμο	
782100/55JVC CHARAC	TERISTICS			Min.			
Parameter			Conditions			Units	
Primary Inductance, L _p ¹			100kHz, 20mV		335	μΗ	
Leakage Inductance, L ¹			100kHz, 250mV		8	μΗ	
Inter winding Capacitance, C _{ww}			100kHz, 250mV		8	pF	
Primary D.C. Resistance, R _{DC} ¹			>0.1VDC Pins 1/2 or 2/3 25		600	mΩ	
Volt-time Product, Et		PINS	/2 or 2/3	25		Vµs	

All specifications typical at $T_A=25^{\circ}C$

- 1. L_p , L_l and R_{pc} measured between pins 1-3.
- 2. Flash tested for 1 second



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ABSOLUTE MAXIMUM RATINGS	
Operating free air temperature range	-40°C to 85°C
Storage temperature range	-50°C to 125°C
VC VARIANTS SOLDERING INFORMATION	
Pin finish	Matte tin
Peak wave solder temperature	260°C for 10 seconds
JVC VARIANTS SOLDERING INFORMATION	
Pin finish	Matte tin
Max. peak reflow temperature	245°C
Moisture sensitivity level ³	1
Max. time above liquidous (217 °C)	100s

TECHNICAL NOTES

ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

All products in this series are 100% production tested at their stated isolation voltage. A question commonly asked is, "What is the continuous voltage that can be applied

across the part in normal operation?"

For a part holding no specific agency approvals both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. This series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enameled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognized parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

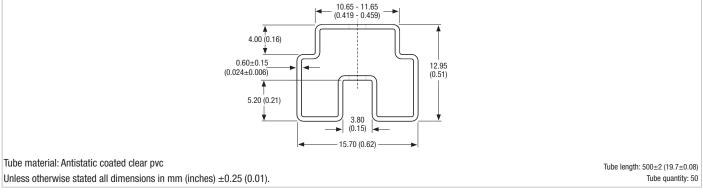
3. Representative samples of the product were subjected to the conditioning described in IPC/JEDEC J-STD-020D and passed electrical testing, package coplanarity and visual inspection which revealed no external cracks or changes in package body flatness.

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PACKAGE SPECIFICATIONS MECHANICAL DIMENSIONS **DIL Package** SM Package 12.70 (0.5) 9.52 9.77 (0.385) Max. Top View **Top View** (0.375) MAX Primary Secondary • Primary Secondary 1 🗆 □ 6 M 782100 • 1 0 6 9.0 (0.354) MAX□ 9.14 2 🗆 ⊐ 5 2 (XXJVC 5 (0.360) Max 0 782100/XXVC XYYWW 3 Г 3 Г **-** 4 XYYWW 6.35 (0.25) 6.35 (0.25) 3.44±0.50 (0.135±0.02) 0.30 (0.012) 0.30(0.012) _____0.63 (0.025) _____0.53 (0.021) 0.90 (0.035) -0.6 (0.024) 0.20 (0.008) 0.20(0.008 10.16 ± 0.40 2.54 (0.1) 2 5/ (0.40±0.016) (0.1) Package weight: 1.1g Typ. Unless otherwise stated all dimensions in mm (inches) ± 0.25 (0.01). All pins on a 2.54 (0.1) pitch and within ± 0.25 (0.01) of true position **RECOMMENDED FOOTPRINT DETAILS** VC Versions JVC Versions 0.10 (2.54) - 0.06 (1.60) 0.10 (2.54) -0.10 (2.54) 0.05 (1.20) +0.006 (0.15) -0.000 (0.00) 0.04 (1.00) 0.45 (11.50) Unless otherwise stated all dimensions in mm (inches) ± 0.25 (0.01). All pins on a 2.54 (0.1) pitch and within ± 0.25 (0.01) of true position TUBE OUTLINE DIMENSIONS



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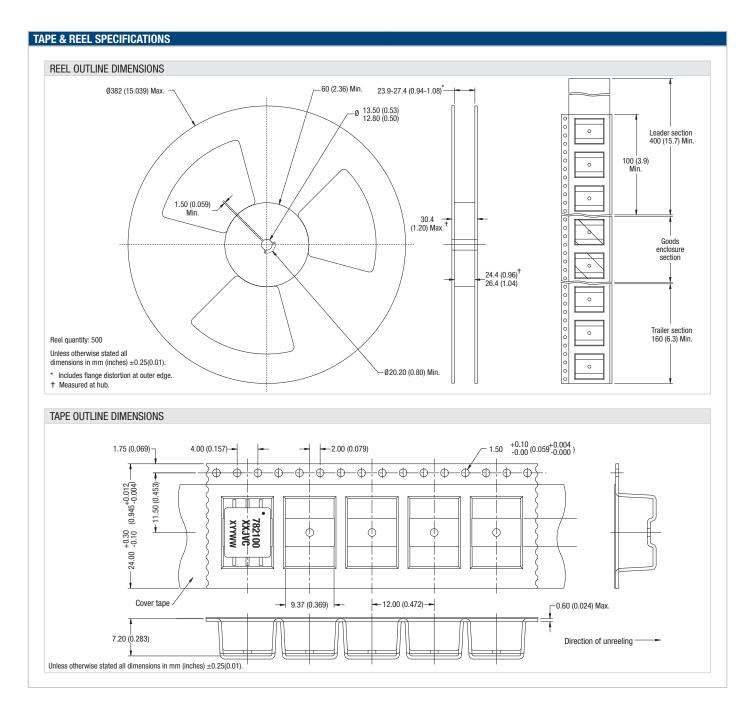
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KMP_782100_A02 Page 3 of 4

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KMP_782100_A02 Page 4 of 4