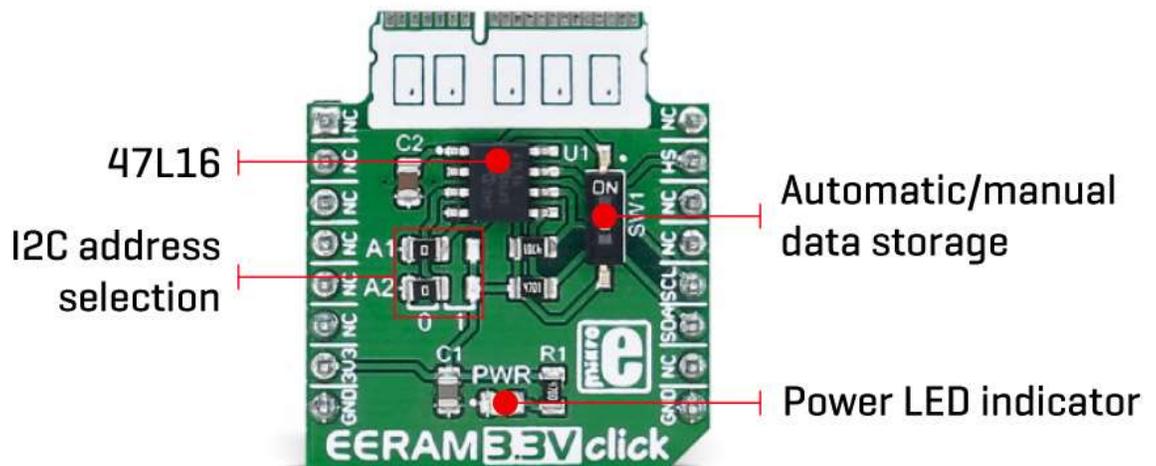




## How does it work?

The click board™ uses the [47L16](#), an I2C serial chip with 16 Kbit and EEPROM backup, from [Microchip](#). The memory cells are organized into 2048 bytes, each 8bit wide. The data is read and written by the I2C serial communication bus, routed to the respective pins of the mikroBUS™ (SCL and SDA pins). To access the device, the first byte sent from the host MCU should be the I2C slave address. In most cases, the master I2C device will be the host MCU itself. The slave IC2 address depends on the state of the hardware address pins on the EERAM 3.3V click. These pins are routed to the onboard SMD jumpers, labeled as A1 and A2, so they can be pulled either to a HIGH or to a LOW logic level. Besides the address pins, the I2C slave address is determined by the section of the device that needs to be accessed. There are two sections, accessed by a different slave address: SRAM section and the CONTROL REGISTER section. The datasheet of the 47L16 contains more information on these addresses and how to access certain groups of registers. However, provided click library functions allow easy and transparent operation with the EERAM 3.3V click. The provided example application demonstrates the usage of these library functions, and it can be used as a reference for future custom application development.



The EERAM 3.3V click can backup or restore its content, utilizing the integrated EEPROM non-volatile array, which is not directly accessible. There are several ways to achieve this:

- The most useful method is the auto-store method. To automatically store the data on power loss, the ASE bit of the status register should be set. This will allow the capacitor connected between the VCAP pin of the 47L16 I2C and GND to be used as a temporary power supply (roughly speaking), when the main power is lost (VCAP voltage drops under a certain threshold). It will provide enough power to store the data to the embedded EEPROM array.
- There is a hardware pin on the 47L16 IC, which is used to initiate the hardware (manual) store to EEPROM function. A low-to-high signal edge on the HS pin will cause the backup function to be executed. This pin is routed to the INT pin of the mikroBUS™.
- Auto-restore is initiated after every power on, when the capacitor voltage (VCAP) raises beyond the internally set threshold. It is automatically executed, and no special actions or procedures are needed to trigger this process. A small switch onboard, labeled as SW1 is used to disconnect the VCC power supply from the capacitor, preventing it from being charged and effectively disabling the auto-store and restore functions that way.

The store to EEPROM/backup function will not be executed if the SDRAM content has not been changed since the last time it was written to EEPROM. This is tracked by the AN bit of the status register.

The click board uses only 3.3V power rail from the mikroBUS™. The I2C lines are pulled to a HIGH logic level by the onboard resistors, so the device is ready to be used with 3.3V MCUs, right out of the box. Although the EERAM 3.3V click is designed to work only with 3.3V MCUs, there is also a 5V alternative version of this device. More information can be found on the product page of the [EERAM 5V click](#).

# Specifications

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<b>Type</b>	EEPROM
<b>Applications</b>	Provides an additional RAM memory for a wide range of embedded applications, especially useful if retaining of the working parameters is required upon the power failure or power off cycle
<b>On-board modules</b>	The 47C16 I2C, a serial EERAM from Microchip
<b>Key Features</b>	EERAM 3.3V click features 16 Kb of SRAM, with the automatic or manual backup and recall to/from the integrated EEPROM, upon power-down
<b>Interface</b>	I2C
<b>Input Voltage</b>	3.3V
<b>Click board size</b>	S (28.6 x 25.4 mm)

## Pinout diagram

This table shows how the pinout on **EERAM 3.3V click** corresponds to the pinout on the mikroBUS™ socket (the latter shown in the two middle columns).

Notes	Pin					Pin	Notes
		1	AN	PWM	16		
	NC	1	AN	PWM	16	NC	
	NC	2	RST	INT	15	HS	HW Store
	NC	3	CS	TX	14	NC	
	NC	4	SCK	RX	13	NC	
	NC	5	MISO	SCL	12	SCL	SCL I2C line
	NC	6	MOSI	SDA	11	SDA	SDA I2C line
Power supply	+3.3V	7	3.3V	5V	10	NC	
Ground	GND	8	GND	GND	9	GND	Ground

## Jumpers and settings

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<b>Designator</b>	<b>Name</b>	<b>Default Position</b>	<b>Default Option</b>	<b>Description</b>
JP1	A1	Left	0	Address select 1
JP2	A2	Left	0	Address select 2
SW1	SW1	Down	OFF	Automatic/manual data store and recall

## Buttons and LEDs

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<b>Designator</b>	<b>Name</b>	<b>Type</b>	<b>Description</b>
LD1	PWR	LED	Power Indication LED