

# PMN28UNE 20 V, N-channel Trench MOSFET 16 April 2018

**Product data sheet** 

### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

#### 2. Features and benefits

- Trench MOSFET technology •
- Low threshold voltage
- Very fast switching •
- ElectroStatic Discharge (ESD) protection > 2 kV HBM •

#### 3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

#### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	20	V
V <sub>GS</sub>	gate-source voltage			-8	-	8	V
I <sub>D</sub>	drain current	$V_{GS}$ = 4.5 V; $T_{amb}$ = 25 °C; t ≤ 5 s	[1]	-	-	7.1	А
Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS}$ = 4.5 V; I <sub>D</sub> = 5.5 A; T <sub>j</sub> = 25 °C		-	24	32	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

## nexperia

## 5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	D	drain		D			
2	D	drain					
3	G	gate		G A A A A A A A A A A A A A A A A A A A			
4	S	source	TSOP6 (SOT457)				
5	D	drain					
6	D	drain		s s			
				017aaa255			

## 6. Ordering information

Table 3. Ordering information						
Type number Package						
	Name	Description	Version			
PMN28UNE	TSOP6	plastic, surface-mounted package (SC-74)	SOT457			

## 7. Marking

Table 4. Marking codes	
Type number	Marking code
PMN28UNE	3G

### 8. Limiting values

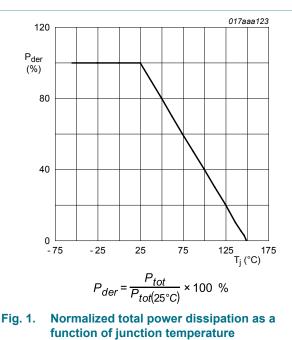
#### Table 5. Limiting values

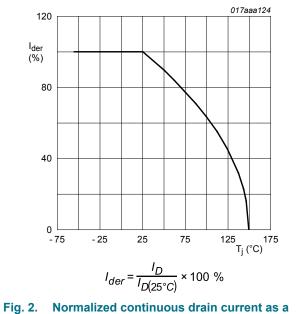
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	20	V
V <sub>GS</sub>	gate-source voltage			-8	8	V
ID	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	7.1	А
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	5.5	А
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	3.5	А
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	23	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	570	mW
			[1]	-	1.4	W
		T <sub>sp</sub> = 25 °C		-	6.25	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source Drai	n Diode		·		·	
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	1.3	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

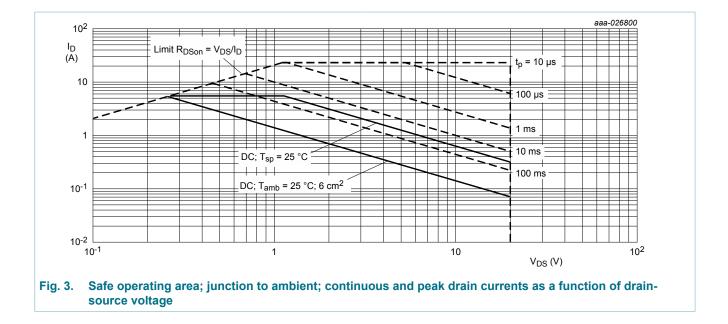






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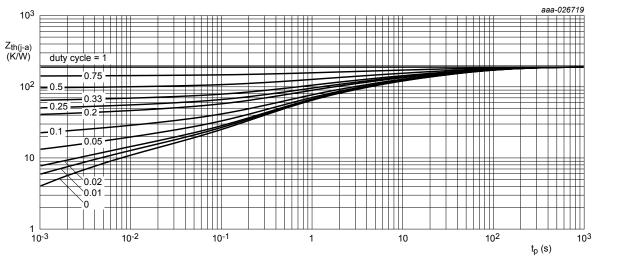
#### 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	-	[1]	-	190	220	K/W
			[2]	-	78	90	K/W
		in free air; t ≤ 5 s	[2]	-	47	54	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	15	20	K/W

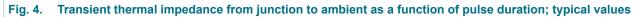
#### Table 6. Thermal characteristics

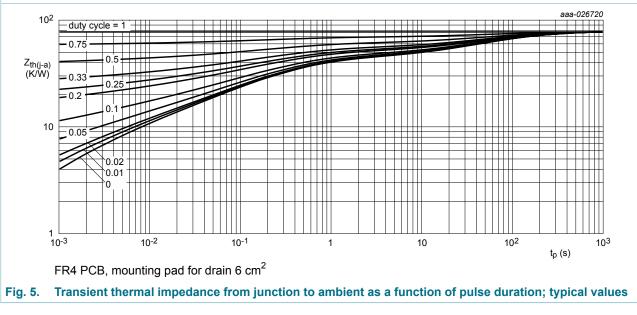
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.



FR4 PCB, standard footprint



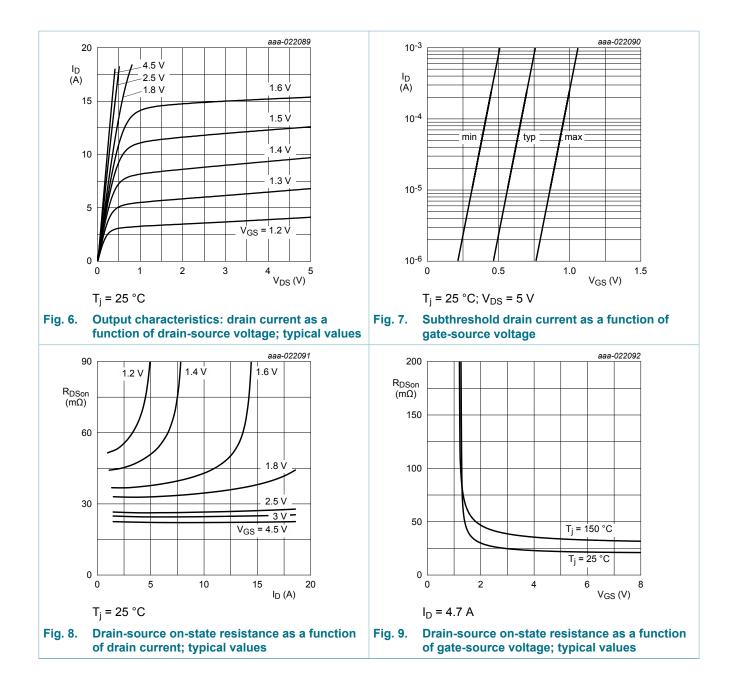


## **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C	20	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	$I_D$ = 250 µA; $V_{DS}$ = $V_{GS}$ ; $T_j$ = 25 °C	0.45	0.7	1	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = 20 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	-	1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	10	μA
		$V_{GS}$ = -20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-10	μA
		$V_{GS}$ = 4.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	5	μA
		$V_{GS}$ = -4.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-5	μA
		V <sub>GS</sub> = 2.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
		$V_{GS}$ = -2.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-100	nA
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 5.5 A; T <sub>j</sub> = 25 °C	-	24	32	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 5.5 A; T <sub>j</sub> = 150 °C	-	36	48	mΩ
		V <sub>GS</sub> = 2.5 V; I <sub>D</sub> = 4.7 A; T <sub>j</sub> = 25 °C	-	29	45	mΩ
		V <sub>GS</sub> = 1.8 V; I <sub>D</sub> = 1.3 A; T <sub>j</sub> = 25 °C	-	40	70	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; I <sub>D</sub> = 4.7 A; T <sub>j</sub> = 25 °C	-	30	-	S
R <sub>G</sub>	gate resistance	f = 1 MHz	-	7	-	Ω
Dynamic ch	naracteristics					
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = 10 V; I <sub>D</sub> = 4.7 A; V <sub>GS</sub> = 4.5 V;	-	6.2	10	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.5	-	nC
Q <sub>GD</sub>	gate-drain charge		-	1.4	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 10 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	490	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	86	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	70	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 10 V; I <sub>D</sub> = 4.7 A; V <sub>GS</sub> = 4.5 V;	-	8	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	35	-	ns
t <sub>d(off)</sub>	turn-off delay time	1	-	39	-	ns
t <sub>f</sub>	fall time		-	14	-	ns
Source-drai	in diode		1	1		
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 1.3 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	0.7	1.2	V

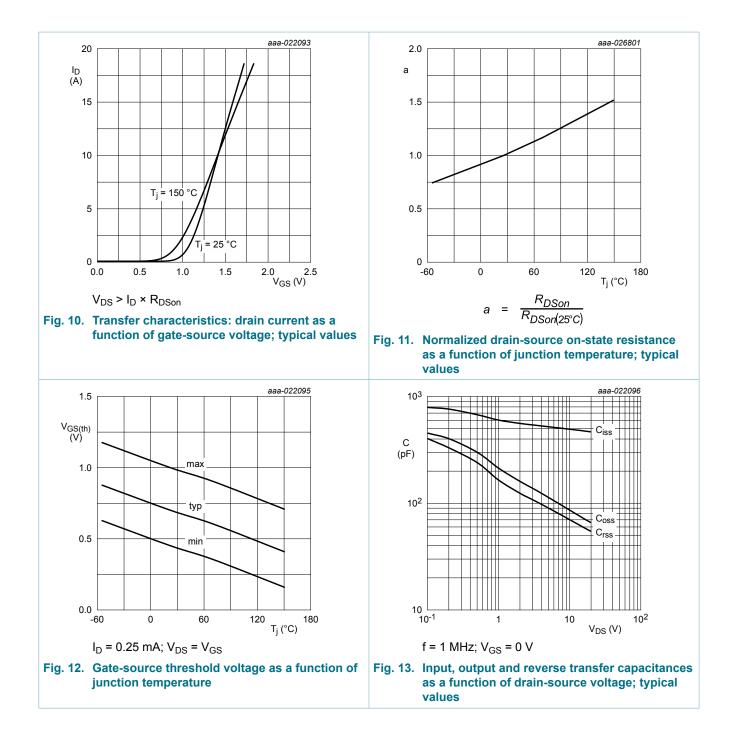
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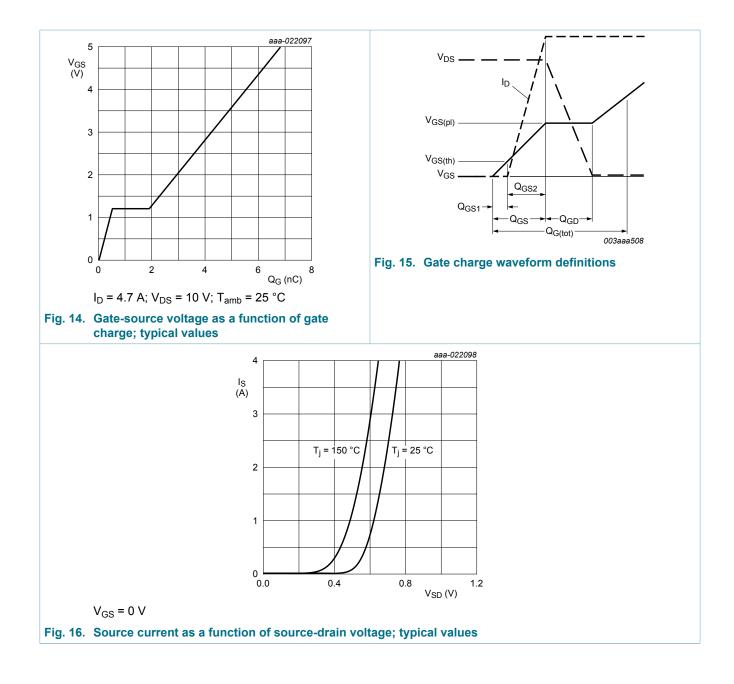
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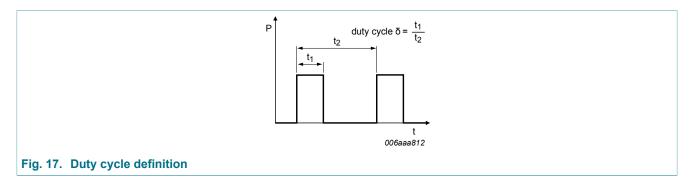


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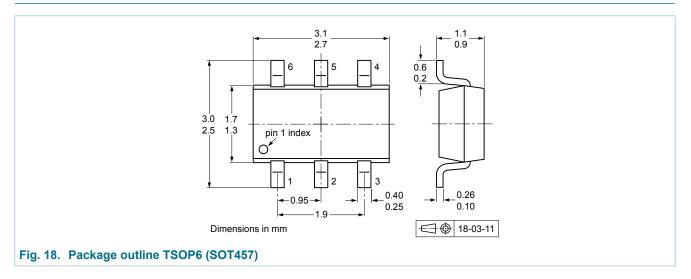
#### 20 V, N-channel Trench MOSFET



## **11. Test information**

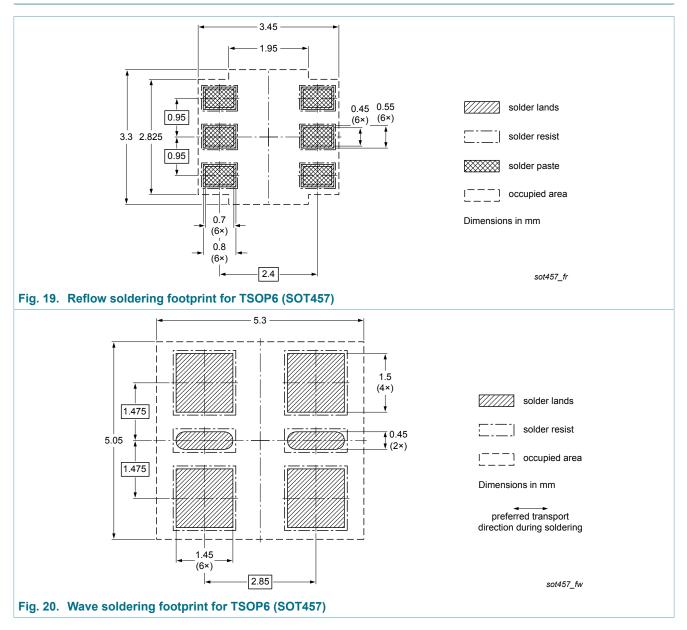


## 12. Package outline



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#### 13. Soldering



## 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMN28UNE v.1	20180416	Product data sheet	-	-		

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## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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