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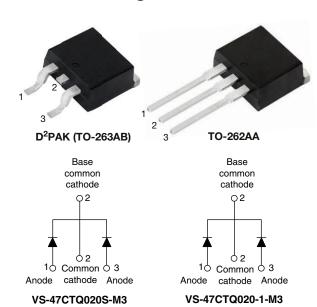
Vishay Semiconductors

COMPLIANT

HALOGEN

**FREE** 

## High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 20 A				
$V_{R}$	20 V				
V <sub>F</sub> at I <sub>F</sub>	0.34 V				
I <sub>RM</sub> max.	310 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
E <sub>AS</sub>	18 mJ				
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA				
Circuit configuration	Common cathode				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Center tap configuration
- Optimized for 3.3 V application
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION**

This center tap Schottky rectifier module has been optimized for ultralow forward voltage drop specifically for 3.3 V output power supplies. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	40	A			
V <sub>RRM</sub>		20	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1000	Α			
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.34	V			
T <sub>J</sub>		-55 to +150	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VS-47CTQ020S-M3 VS-47CTQ020-1-M3	UNITS		
Maximum DC rayaraa yaltaga	V/_	125 °C	20	V		
Maximum DC reverse voltage	V <sub>R</sub>	150 °C	10	V		



# VS-47CTQ020S-M3, VS-47CTQ020-1-M3

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ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS		
Maximum average per leg forward current per device			50 % duty avalo at T = 125 °C	rootongular wayafarm	20		
		I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 135 °C, rectangular waveform		40		
Maximum poak and avala			5 μs sine or 3 μs rect. pulse	Following any rated load	1000	Α	
Maximum peak one cycle non-repetitive surge current per leg		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	250		
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 3  \text{A},  L = 3  \text{mH}$		18	mJ	
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 µs  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		3	Α	

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		20 A	T <sub>.1</sub> = 25 °C	0.45		
		40 A	11=25 0	0.51		
Maximum famuard valtage drap par lag	V <sub>FM</sub> <sup>(1)</sup>	20 A	T <sub>.1</sub> = 125 °C	0.34	V	
Maximum forward voltage drop per leg	VFM (1)	40 A	1   1   125 C	0.44	V	
		20 A	T <sub>.1</sub> = 150 °C	0.31		
		40 A	1 J = 150 C	0.42		
	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = 5 V	60		
Market and a second section of			V <sub>R</sub> = 3.3 V	45		
Maximum reverse leakage current per leg		T <sub>J</sub> = 150 °C	V <sub>R</sub> = 10 V	306	mA	
current per leg		T <sub>J</sub> = 25 °C	V Dated V	3		
		T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	310		
Threshold voltage	V <sub>F(TO)</sub>	$T_J = T_J$ maximum		0.188	V	
Forward slope resistance	r <sub>t</sub>			5.9	mΩ	
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz), 25 °C		3000	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.5			nΗ	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>				

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHAN	THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storag temperature range	е	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C		
Maximum thermal resistance, junction to case per leg		$R_{th,JC}$	DC operation	1.5			
Maximum thermal resistance, junction to case per package		□thJC	DC operation	0.75	°C/W		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50			
Approximate weight				2	g		
Approximate weight				0.07	oz.		
Mounting torque minimum				6 (5)	kgf · cm		
Mounting torque -	maximum			12 (10)	(lbf · in)		
Marking daying			Case style D <sup>2</sup> PAK (TO-263AB)	47CTQ0	)20S		
Marking device			Case style TO-262AA	47CTQ0	20-1		



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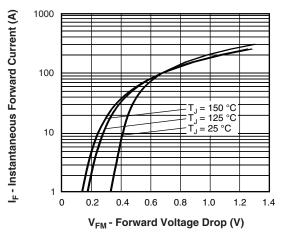


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

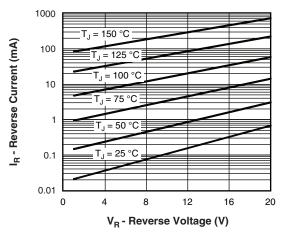


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

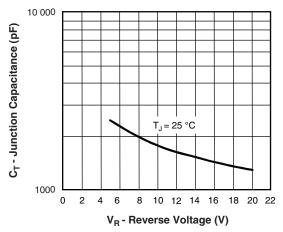


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

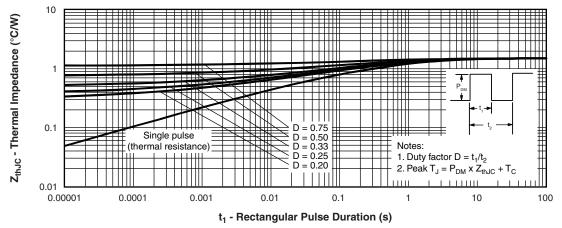


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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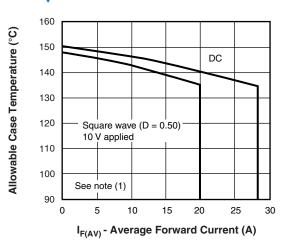


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

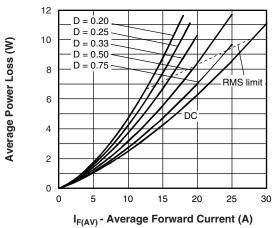


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

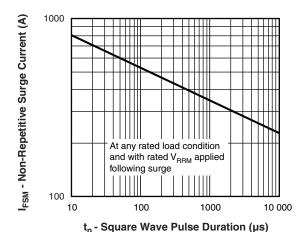


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

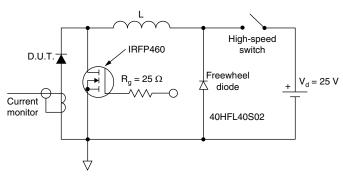


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

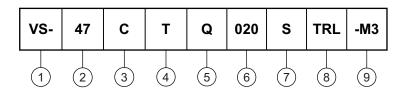
(1) Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 10 V



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### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 A)

Gircuit configuration: C = common cathode

**4** - T = TO-220

5 - Schottky "Q" series

6 - Voltage rating (020 = 20 V)

7 - • S =  $D^2PAK$  (TO-263AB)

• -1 = TO-262AA

None = tube

• TRL = tape and reel (left oriented - for D<sup>2</sup>PAK (TO-263AB) only)

• TRR = tape and reel (right oriented - for D<sup>2</sup>PAK (TO-263AB) only)

9 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-47CTQ020S-M3	50	1000	Antistatic plastic tubes			
VS-47CTQ020STRR-M3	800	800	13" diameter reel			
VS-47CTQ020STRL-M3	800	800	13" diameter reel			
VS-47CTQ020-1-M3	50	1000	Antistatic plastic tubes			

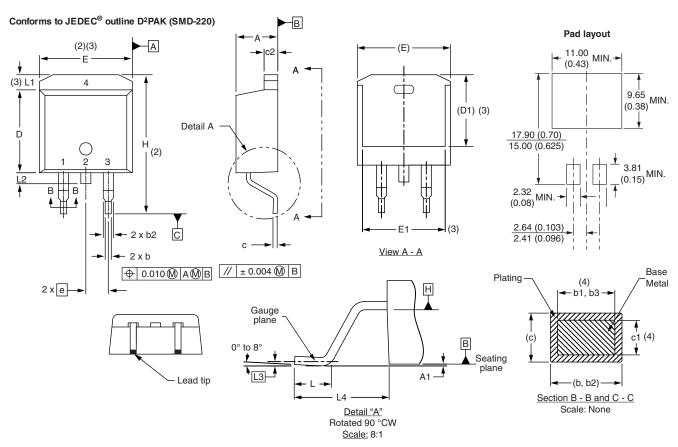
LINKS TO RELATED DOCUMENTS				
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164		
Dimensions	TO-262AA	www.vishay.com/doc?96165		
Dart marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444		
Part marking information	TO-262AA	www.vishay.com/doc?95443		
Packaging information		www.vishay.com/doc?96424		



## Vishay Semiconductors

## D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190		D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010		Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039		E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4	е	2.54	BSC	0.100	) BSC	
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4	L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029		L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4	L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065		L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2	L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

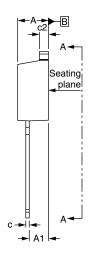


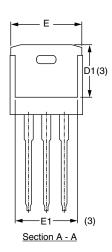
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## **TO-262**

#### **DIMENSIONS** in millimeters and inches

# Modified JEDEC outline TO-262 (Datum A) (2) (3) (3) L1 D D D C C C C A (2) A (2) A (3) L2 B B B B C C C A (2)



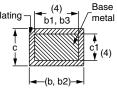


**⊕** 0.010**⋒**|A**⋒**|B

#### Lead assignments



<u>Diodes</u>
1. - Anode (two die)/open (one die)
2., 4. - Cathode
3. - Anode



Section B - B and C - C Scale: None

CVMPOL	MILLIN	METERS	INCH	INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.100	BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

#### Notes

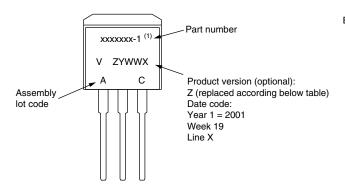
- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$  Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



# **Part Marking Information**

## Vishay Semiconductors

## **TO-262**



Example: This is a xxxxxxx-1 <sup>(1)</sup> with assembly lot code AC, assembled on WW 19, 2001

in the assembly line "X"

#### Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

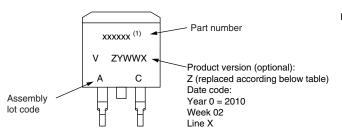
ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION			
A	Termination lead (Pb)-free			
В	Totally lead (Pb)-free			
E	RoHS-compliant and termination lead (Pb)-free			
F	RoHS-compliant and totally lead (Pb)-free			
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free			
N	Halogen-free, RoHS-compliant and totally lead (Pb)-free			
G	G Green			



# **Part Marking Information**

Vishay Semiconductors

## D<sup>2</sup>PAK



Example: This is a xxxxxx <sup>(1)</sup> with assembly lot code AC, assembled on WW 02, 2010

#### Note

(1) If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION		
А	Termination lead (Pb)-free		
В	Totally lead (Pb)-free		
E	RoHS-compliant and termination lead (Pb)-free		
F RoHS-compliant and totally lead (Pb)-free			
M	Halogen-free, RoHS-compliant, and termination lead (Pb)-free		
N	Halogen-free, RoHS-compliant, and totally lead (Pb)-free		
G	Green		



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