



<b>Title of Change:</b>	NCP1244 Data sheet Change.
<b>Proposed first ship date:</b>	12 October 2018
<b>Contact information:</b>	Contact your local ON Semiconductor Sales Office or < <a href="mailto:marty.paul@onsemi.com">marty.paul@onsemi.com</a> >
<b>Samples:</b>	Contact your local ON Semiconductor Sales Office or < <a href="mailto:PCN.samples@onsemi.com">PCN.samples@onsemi.com</a> > Sample requests are to be submitted no later than 30 days from the date of first notification, Initial PCN or Final PCN, for this change.
<b>Additional Reliability Data:</b>	Contact your local ON Semiconductor Sales Office or < <a href="mailto:tomas.vajter@onsemi.com">tomas.vajter@onsemi.com</a> >.
<b>Type of notification:</b>	This is a Final Product/Process Change Notification (FPCN) sent to customers. FPCNs are issued 90 days prior to implementation of the change. ON Semiconductor will consider this change accepted, unless an inquiry is made in writing within 30 days of delivery of this notice. To do so, contact < <a href="mailto:PCN.Support@onsemi.com">PCN.Support@onsemi.com</a> >
<b>Change Part Identification:</b>	Product marked with date code 1841 or later shall be tested to new Datasheet limits. The trace code marking on Line 2 is of the form ALYW where A = Assembly Location, L = Wafer Lot ID and YW is a 2-digit date code. The 2-digit date code is an Alpha code so the change will occur after date code "JO".
<b>Change Category:</b>	<input type="checkbox"/> Wafer Fab Change <input type="checkbox"/> Assembly Change <input checked="" type="checkbox"/> Test Change <input type="checkbox"/> Other _____

**Change Sub-Category(s):**

<input type="checkbox"/> Manufacturing Site Addition	<input type="checkbox"/> Material Change	<input checked="" type="checkbox"/> Datasheet/Product Doc change
<input type="checkbox"/> Manufacturing Site Transfer	<input type="checkbox"/> Product specific change	<input type="checkbox"/> Shipping/Packaging/Marking
<input type="checkbox"/> Manufacturing Process Change		<input type="checkbox"/> Other: _____

<b>Sites Affected:</b>	ON Semiconductor Sites: ON Carmona, Philippines	External Foundry/Subcon Sites: None
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**Description and Purpose:**

This notification announces that ON Semiconductor is making a correction to the limits of the TGTOM and Tonmax parameters. Datasheet limits do not reflect long term variability of the process over temperature. Limits will now include all devices within the normal distribution. Test limits will be adjusted to reflect the datasheet change. No design, silicon, package, process, or material changes have been made. This change will affect function of the product. There is no product marking change as a result of this change.

Turn up current in on mode	$V_{CC} < V_{CC(off)}$	t <sub>OFF</sub>	500	600	<del>700</del>	ms
Go To Off mode timer	$V_{CC} > V_{CC(off)}$	t <sub>GTOM</sub>	500	600	<del>700</del>	ms

770

Maximum on time for T <sub>J</sub> = 25°C to +125°C only	f <sub>OSC</sub> = 65 kHz	t <sub>ONmax(65kHz)</sub>	11.5 <sup>27</sup>	12.3	13.1 <sup>66</sup>	μs
	f <sub>OSC</sub> = 100 kHz	t <sub>ONmax(100kHz)</sub>	7.5 <sup>27</sup>	8.0	<del>8.5</del> 9.25	
Maximum on time	f <sub>OSC</sub> = 65 kHz	t <sub>ONmax(65kHz)</sub>	11.3 <sup>0</sup>	12.3	13.1 <sup>66</sup>	μs
	f <sub>OSC</sub> = 100 kHz	t <sub>ONmax(100kHz)</sub>	7.4 <sup>0</sup>	8.0	<del>8.5</del> 9.25	



**List of Affected Parts:**

NCP1244AD065R2G  
NCP1244AD100R2G  
NCP1244BD065R2G  
NCP1244BD100R2G