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Product Change Notification - SYST-15TGGB698
(Convert To PDF)

Date:

16 Aug 2018

Product Category:

32-bit Microcontrollers

Affected CPNs:

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Notification subject:

ERRATA - SAM D5x/E5x Family Errata and Data Sheet Clarification

Notification text:

SYST-15TGGB698

Microchip has released a new DeviceDoc for the SAM D5x/E5x Family Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at SAM D5x/E5x Family Errata and Data Sheet Clarification.

Notification Status: Final

Description of Change:

1) The current device data sheet revision letter was updated.

2) The following silicon issues were added:

a) Configurable Custom Logic (CCL):

– Enable Protected Registers

– Sequential Logic Reference

b) Device:

– Reverse Current in VDDIOB Domain

c) SERCOM:

– Repeated Start in Master Write Operation in 10-bit Addressing Mode

– Repeated Start in High-Speed Master Write Operation

– Repeated Start in High-Speed Master Read Operation

– STATUS.CLKHOLD Bit in Master and Slave Modes

d) Supply Controller (SUPC):

– Buck Converter Mode

e) TCC:
– TCC with EVSYS in SYNC/RESYNC Mode

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 16 Aug 2018

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):
SAM D5x/E5x Family Errata and Data Sheet Clarification

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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