



Cypress Semiconductor Corporation, 198 Champion Court, San Jose, CA 95134. Tel: (408) 943-2600

## ADVANCE PRODUCT CHANGE NOTIFICATION

**APCN:** APCN181801A

**Date:** October 14, 2018

**Subject:** Addendum to APCN181801 - Planned Qualification of Tera Probe, Inc. Japan as an Additional Wafer Test (Sort) Site for GL-S Product Family

**To:** GENERAL INBOX  
PREMIER FARNELL  
ProductChangeNotices@premierfarnell.com

### **Description of Change:**

The purpose of this addendum is to communicate the new schedule for the planned qualification of Tera Probe, Inc. as an additional wafer test sort site for GL-S products.

Cypress announced the planned qualification of Tera Probe, Inc. Japan as an additional wafer-level test (Sort) site for the GL-S products. These products are currently being tested at Test 25 in Austin, Texas. Tera Probe, Inc. uses the next generation Advantest 5830 for testing. There will be no changes to the test program coverage or test methodologies.

This qualification will allow Cypress to leverage Tera Probe's manufacturing expertise and quality focus. This in turn also provides the means for Cypress to continue to meet its customers' IC testing needs as well as delivery commitments in dynamic, changing market conditions. Tera Probe, Inc. is certified on several international quality standards: ISO / TS16949, ISO 9001, ISO 14001 and OHSAS 18001. Tera Probe's certificates can be viewed on their corporate web site: <http://www.teraprobe.com/english/corporate/iso.html>

Cypress qualification is in compliance with JEDEC and AEC standards. To help expedite the implementation of this site, please share any additional customer specific qualification and test requirements at the earliest.

### **Benefit of Change:**

Cypress will have the added capability to meet varying market demand, and to ensure consistent and reliable delivery performance to customers.

### **Part Numbers Affected:** 391

See the attached 'Affected Parts List' file for a list of all part numbers affected by this change.

**Qualification Status:**

Estimated completion dates for internal qualification and sample availability dates are provided in the table below.

Estimated Cypress Qualification	Estimated PCN Publication Date	Estimated Implementation Date
March 2019	March 2019	June 2019

**Approximate Implementation Date:**

The estimated implementation date is provided in table above.

**Anticipated Impact:**

No impact is expected to form, fit, function, datasheet parameters, package composition or package pin-out.

**Method of Identification:**

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the date code marked on the package.

**Response Required:**

Please share any additional qualification and test requirements if needed.

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at [pcn\\_adm@cypress.com](mailto:pcn_adm@cypress.com).

Sincerely,

Cypress PCN Administration