## **Product / Process Change Notification**



#### N° 2016-072-A

Dear Customer,

Please find attached our INFINEON Technologies PCN:

# Introduction of an Additional Wafer Test Location at Infineon Technologies (Kulim) Sdn. Bhd, Kulim, Malaysia for Dedicated SFET4 100V Products

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before 15<sup>th</sup> November 2018.
- Infineon aligns with the widely-recognized JEDEC STANDARD "JESD46", which stipulates: "Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change."

Your prompt reply will help Infineon Technologies to assure a smooth and well executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Infineon Technologies AG Postal Address Headquarters: Am Campeon 1-15, D-85579 Neubiberg, Phone +49 (0)89 234-0 Chairman of the Supervisory Board: Dr. Eckart Sünner Management Board: Dr. Reinhard Ploss (CEO), Dominik Asam, Dr. Helmut Gassel, Jochen Hanebeck Registered Office: Neubiberg Commercial Register Amtsgericht München HRB 126492

2018-10-04

# **Product / Process Change Notification**



#### 2016-072-A N°

► Products affected: Please refer to attached affected product list 1\_cip16072\_A

### Detailed Change Information:

Subject:	Introduction of an additional wafer test in Kulim for dedicated SFET4 100V products.	
Reason:	Expansion of wafer test to assure continuity of supply and enable flexible testing.	
Description:	<u>Old</u>	New
Wafer Test	<ul> <li>Infineon Technologies AG, Regensburg, Germany</li> </ul>	<ul> <li>Infineon Technologies (Kulim) Sdn. Bhd, Kulim, Malaysia</li> <li>or</li> <li>Infineon Technologies AG, Regensburg, Germany</li> </ul>
Product Identification:	Traceability is ensured by lot number.	
Impact of Change:	No change in form fit or function and no impact on quality and reliability of the final product. The wafer test location verification is performed via the Advanced Measurement System Analysis (AMSA) methodology.	
Attachments:	Affected product list 1_cip16072_A	
Time Schedule:		
Final qualification report:	Available on request	
<ul> <li>First samples available:</li> </ul>	No samples planned due to no change of product.	
Intended start of delivery:	01-Februar-2019 or earlier after customer approval	

If you have any questions, please do not hesitate to contact your local Sales office.