

Product Change Notification - SYST-01SYLQ197 [\(Convert To PDF\)](#)

Date: 03 Oct 2018

Product Category: 32-bit Microcontrollers

Affected CPNs: 

Notification subject: ERRATA - SAM D21 Family Silicon Errata and Data Sheet Clarification

Notification text:

SYST-01SYLQ197

Microchip has released a new DeviceDoc for the SAM D21 Family Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [SAM D21 Family Silicon Errata and Data Sheet Clarification](#).

ERRATA - SAM D21 Family Silicon Errata and Data Sheet Clarification

Notification Status: Final

Description of Change: 1) The following Errata Issues were added: a) 1.13.5 Write-Protect. b) 1.15.5 NACK and Repeated Start in I2C Master Mode. c) 1.15.6 SERCOM-USART: Collision Detection. d) 1.15.7 SERCOM-USART: USART in Debug Mode. e) 1.15.8 SERCOM-I2C: Slave Mode with DMA. f) 1.15.9 SERCOM-I2C: I2C Mode in 10-bit Address. g) 1.15.10 SERCOM-SPI: Data Preload. h) 1.15.11 CLKHOLD Bit Status in I2C. i) 1.15.12 Quick Command I2C. j) 1.15.13 Repeated Start I2C k) 1.15.14 Slave Mode I2C. l) 20 PATTB. n) 1.16.21 PERBUF. o) 1.16.22 TCC with EVSYS in SYNC/RESYNC Mode p) 1.17.2 TC with EVSYS in SYNC/RESYNC Mode q) 1.20.1 XOSC r) 1.20.2 BOD33. 2) Updated Silicon Errata Summary. 3) The following Data Sheet Clarifications were added: a) ADC Differential Mode at 85°C b) Maximum Peripheral Clock Frequencies at 105°C. c) ADC Differential Mode at 105°C d) ADC Differential Mode at 125°C

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 02 Oct 2018

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s): [SAM D21 Family Silicon Errata and Data Sheet Clarification](#)

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