



Customer Information Notification

201811013I

Issue Date: 07-Dec-2018
Effective Date: 08-Dec-2018

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)



Change Category

- | | | | | |
|----------------------------------------------|---------------------------------------------|----------------------------------------------------|-----------------------------------------|--------------------------------------------------------------------|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input checked="" type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware | <input type="checkbox"/> Other | | | |

i.MXRT1050
Reference Manual
Rev 2 Updates

Description

NXP Semiconductors announces a reference manual update for the i.MXRT1050 to revision 2. The revision history included in the updated documents provides a detailed description of the changes.

Reference Manual Changes, please refer to the attachment in detail.

The i.MXRT1050 reference manual revision 2 can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-based-processors-and-mcus/i.mx-applications-processors/i.mx-rt-series/i.mx-rt1050-crossover-processor-with-arm-cortex-m7-core:i.MX-RT1050?tab=Documentation_Tab

Reason

The reference manual has been updated to correct errors and / or provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Additional information

Affected products and sales history information: see attached file

Additional documents: [view online](#)



Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name Kevin Chang
Position Product Engineer
e-mail address le.chang@nxp.com

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

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