

MDT5000C	IDT5000C 800 x 480 24-Bit RGB Interface			
			Specification	
Ver	sion: 1		Date: 14/05/2018	
			Revision	
	1	24/03/2015 F	First issue.	

Display F	eatures		
Display Size	5.0"		
Resolution	800 x 480		
VGA Size	WVGA		
Orientation	Landscape		
Appearance	RGB		ompliant
Logic Voltage	3.3V		
Interface	24-Bit RGB		moliant
Brightness	550 cd/m ²		Jinpliant
Touchscreen	CTP		1.54
Module Size	120.70 x 76.30 x 5.00 mm		
Operating Temperature	-20°C ~ +70°C	Box Quantity	Weight / Display
Pinout operand	40 - Way FFC		
ut sign	manufactur	c sup	July

* - For full design functionality, please use this specification in conjunction with the ILI5960 specification. (Provided Separately)

Disp	lay Accessories
Part Number	Description
MPBV6	40 Way FFC to cable and wires. Driven by any driver board that can be wired to a 1mm pitch SHDR-40V-S-B receptacle.

Optional Varian	ts
Appearances	Voltage
Resistive Touch Panel No Touch Panel	

General Specifications

	Feature	Spec
	LCD Size	5inch
	Display Format	800 (RGB) × 480
	Interface	24-bit RGB
	Color Depth	16.7M
Characteristics	Technology type	a-Si
	Display Spec.	0.045 x 0.135
	Display Mode	Normally White
	Driver IC	ILI5960+ILI6122
	Viewing Direction	12 O'clock
	Gray Viewing Direction	6 O'clock
	LCM (W x H x D) (mm)	120.7*76.3*5
	Activ <mark>e</mark> Area(mm)	108 x 64.8
Mechanical	With /Without TSP	With CTP
	Weig <mark>h</mark> t (g)	TBD
	LED Numbers	18 LEDs

Note 1: Viewing direction is following the data which measured by optics equipment.

Note 2: Requirements on Environmental Protection: RoHS

Note 3: LCM weight tolerance: +/- 5% anufacture • Supply

Input/Output Terminals

LCD PIN

No.	Symbol	Description
1	VBL-	Backlight LED Cathode
2	VBL+	Backlight LED Anode.
3	GND	System Ground
4	VCC	Power supply for logic operation
5~12	R0~R7	Data bus
13~20	G0~G7	Data bus
21~28	B0~B7	Data bus
29	GND	System Ground
30	CLK	Pixel clock signal
31	DISP	Display on/off control
32	HSYNC	Horizontal Sync signal
33	VSYNC	Vertical Sy <mark>n</mark> c signal
34	DEN	Data Enable
35	NC	No connect
36	GND	System Ground
37	XR(NC)	The right side signal of TP
38	YD(NC)	The down side signal of TP
39	XL(NC)	The left side signal of TPICCLUIC SUDDLY
40	YU(NC)	The up side signal of TP

CTP PIN

Pin	Signal	Description
1	VSS	Ground
2	VDD	Power supply 3.2V
3	SCL	I2C clock input
4	VSS	Ground
5	SDA	I2C data input and output
6	VSS	Ground
7	/RST	Reset Pin for CTP
8	WAKE	Wakeup request from the host
9	/INT	Interrupt request to the host
10	VSS	Ground

Absolute Maximum Ratings

		(N	ote 1)		
		Val	ues		
Item	Symbol	Min.	Max.	Unit	Remark
	DVDD	-0.3	5	V	
	AVDD	-0.5	13.5	V	
	VGH	-0.3	42	V	
	VGL	-20	0.3	V	
Power voltage	V _{GH} -V _{GL}		40	V	
Operation Temperature	TOP	-20	70	°C	
Storage Temperature	Тат	-30	80	°C	
LED Reverse Voltage	VR	•	3.3	v	Each LED Note 2
LED Forward Current	le		20	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed.

Note 2: VR Conditions: Zener Diode 20mA TUPE - SUPPLY

Electrical Characteristics

Typical operation conditions

Item	Sumbol		Values			Remark
item	Symbol	Min.	Тур.	Max.	Unit	
	DVDD	3.0	3.3	3.6	V	Note 2
Deverseltere	AVDD	10.2	10.4	10.6	V	
Power voltage	V _{GH}	15.3	16.0	16.7	V	
	V _{GL}	-6.7	-6.0	-5.3	V	
Input signal voltage	Vcow	3.09	4.09	5.09	V	Note 4
Input logic high voltage	ViH	0.7 DVpp	-0	DVpp	V	Nets 2
Input logic low voltage	VIL	0		0.3 DV _{DD}	V	Note 3

(Note 1)

Note 1: Be sure to apply DVDD and VGL to the LCD first, and then apply VGH-

Note 2: DV_{DD} setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

Note 4: Typical V_{COM} is only a reference value. It must be optimized according to each LCM. Please use VR and base on below application circuit.

Current Consumption

	Question		Values			-
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Current for Driver	I _{GH}	385	(0.50)	1	mA	V _{GH} =16.0V
	IGL		(0.54)	1	mA	V _{GL} = -6.0V
	IDV _{DD}	0.00	(4.2)	10	mA	DV _{DD} =3.3V
	IAVDD		(19)	50	mA	AV _{DD} =10.4V

Driving Backlight

|--|

Forward Current	l _F	-	60		mA
Forward Voltage	VF		19.2	21	V
Backlight Power consumption	W _{BL}	-			W
LED Lifetime		-	25000	-	Hrs

Note 1 : There are 1 Groups LED

Note 2 : Ta = 25℃

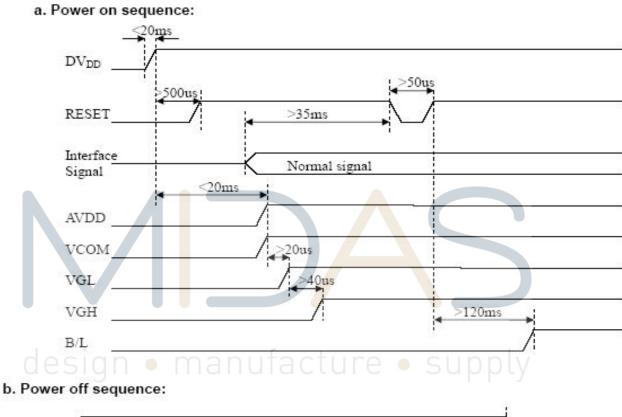
Note 3 : Brightess to be decreased to 50% of the initial value

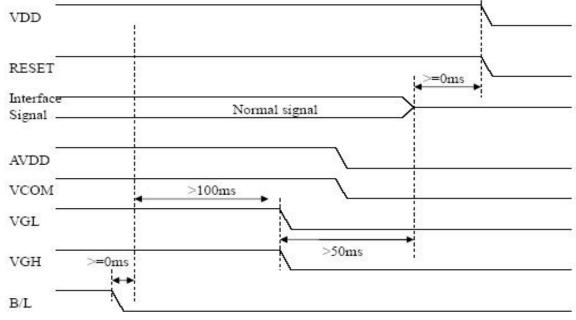


Interface Timing

Power Sequence

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.

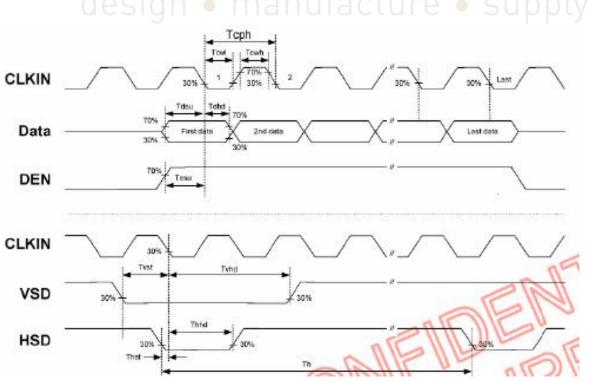




Timing Conditions

Iteaus	Sumbel		Values		Unit	Description
Item	Symbol	Min.	Тур.	Max.		Remark
HS setup time	Thst	8	1629	2	ns	1
HS hold time	Thhd	8	1029	1 1	ns	
VS setup time	Tvst	8	020	20	ns	
VS hold time	Tvhd	8			ns	
Data setup time	Tdsu	8	1053	-	ns	
Data hole time	Tdhd	8	81 7 83	-	ns	
DE setup time	Tesu	8	81 - 91		ns	
DE hole time	Tehd	8	12-6.0	-	ns	
DV _{DD} Power On Slew rate	TPOR			20	ms	From 0 to 90% DV _{DD}
RESET pulse width	TRst	1	0 <u>4</u> 0	-	ms	
DCLK cycle time	Tcoh	20	2		ns	
DCLK pulse duty	Towh	40	50	60	%	

Timing Diagram



Timing

Harris	Combal		Values		D	
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Horizontal Display Area	thd	2	800	2	DCLK	
DCLK Frequency	fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS pulse width	thpw	1	÷	40	DCLK	
HS Blanking	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

14-14-	Symbol	Values			11-14	
ltem desian ● n		f Min. t	Тур.	Max.	Unit	Remark
Vertical Display Area	tvd	-	480		тн)
VS period time	tv	510	525	650	TH	
VS pulse width	tvpw	1	82	20	ΤН	
VS Blanking	tvb	23	23	23	тн	
VS Front Porch	t∨fp	7	22	147	тн	

Data Input Format

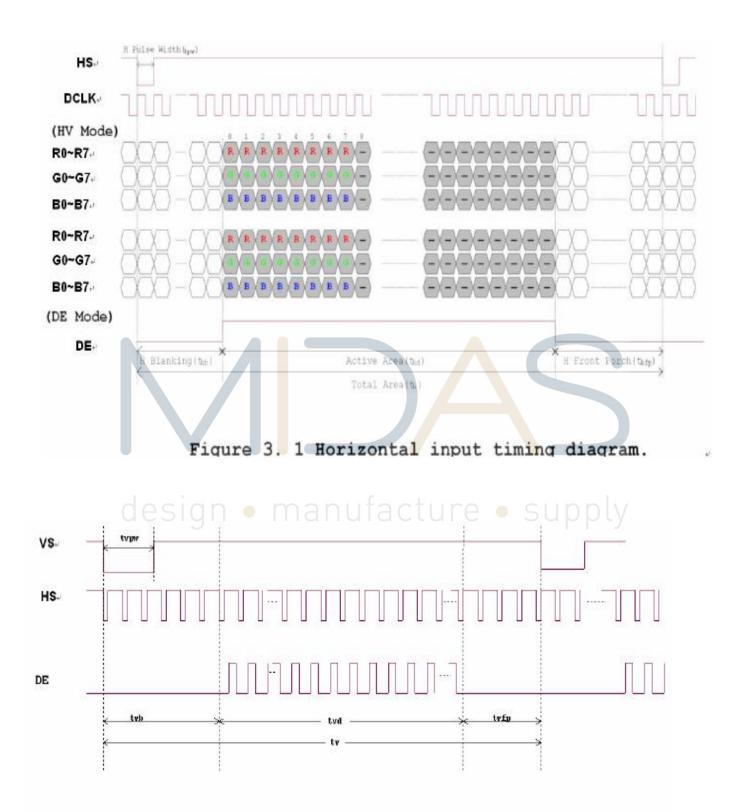


Figure 3. 2 Vertical input timing diagram.

*

Optical Characteristics

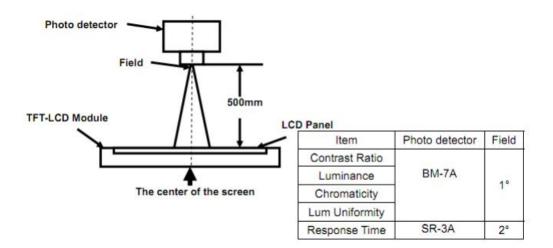
Items		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing angles		θτ		40	50	-	Degree	
		θΒ	Center	60	70	-		Note2
	JIES	θι	CR≥10	60	70	-	Degree.	NOLEZ
		θ _R		60	70	-		
Contrast Ratio		CR	Θ =0	500	600	-	-	Note1, Note3
Posponso T	Response Time		25°C	-	20	30	ma	Note1,
Response i		TOFF	200	-	20	30	ms	Note4
	White	Xw		0.324	0.326	0.328	-	
	Red	Yw		0.364	0.366	0.368	-	
		X _R		0.611	0.613	0.615		
Chromaticity		Y _R	Backlight	0.333	0.335	0.337	-	Note1,
Chromaticity	Gree	X _G	is on	0.305	0.307	0.309	-	Note5
	n	Y _G		0.558	0.560	0.562	-	
	Blue	X _B		0.1 3 3	<mark>0.1</mark> 35	0.137	-	
	Diue	YB		0.158	0.160	0.162	-	
Uniformit	y	U		80	-	-	%	Note1, Note6
NTSC					50		%	Note5
Luminance		• m	anufa	500	550	• 9	supp	Note1, Note7

Test Conditions:

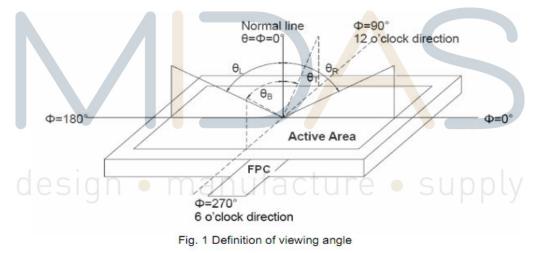
- 1. IF= 20mA(one channel),the ambient temperature is 25°C.
- 2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical Properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



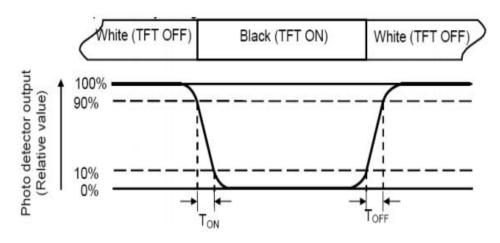
Note 2: Definition of viewing angle range and measurement system. Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80).



Note 3: Definition of contrast ratio

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval Between "White" state and "Black" state. Rise time (TON) is the time between Photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is The time between photo detector output intensity changed from 10% to 90%



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the Center of each measuring area

Luminance Uniformity (U) = Lmin/ Lmax X100%

L-----Active area length W----- Active area width

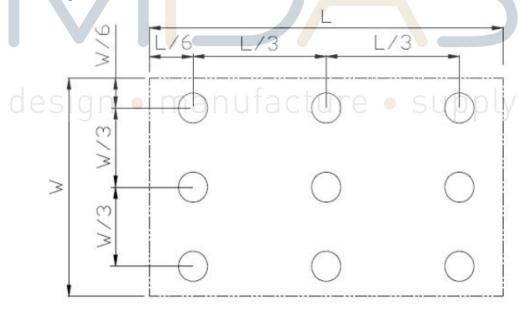


Fig. 2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

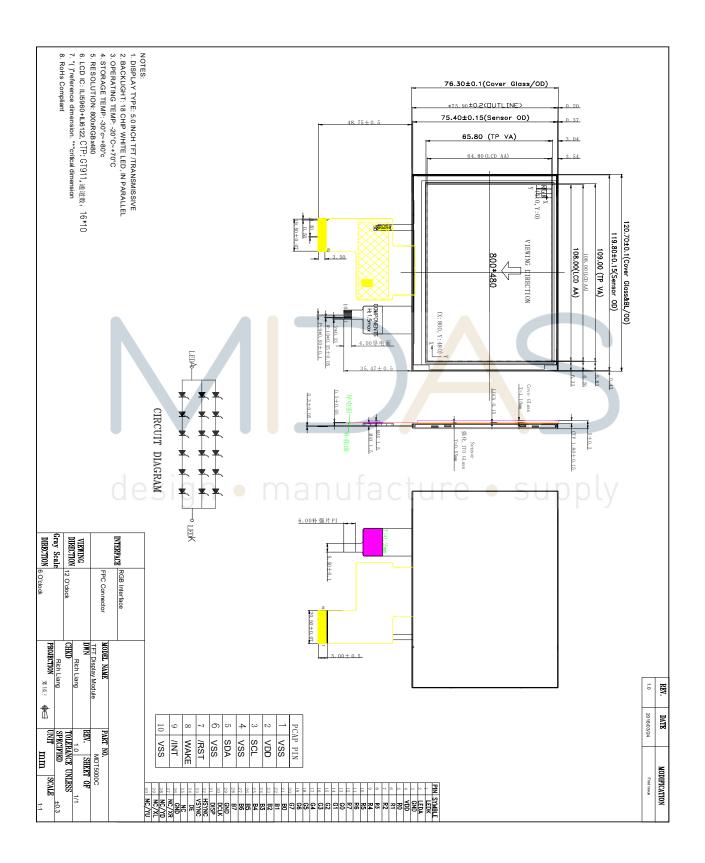
Environmental / Reliability Tests

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts= +70°C, 240hrs	Note 1 IEC60068-2-2, GB2423. 2-89
2	Low Temperature Operation	Ta= -20°C, 240hrs	Note 2 IEC60068-2-1 GB2423.1-89
3	High Temperature Storage	Ta= +80°C, 240hrs	IEC60068-2-2 GB2423. 2-89
4	Low Temperature Storage	Ta= -30°C, 240hrs	IEC60068-2-1 GB/T2423.1-89
5	High Temperature & Humidity Storage	Ta= +60°C, 90% RH max, 160 hours	IEC60068-2-3 GB/T2423.3-2006
6	Thermal Shock (Non-operation)	-30°C 30 min ~ +80°C 30 min Change time: 5min, 30 Cycle	Start with cold temperature, end with high temperature IEC60068-2-14, GB2423.22-87
7	Electro Static Discharge (Operation)	C=150pF, R=330 Ω, 5 points/panel Air:±8KV, 5 times; Contact: ±4KV, 5 times; (Environment: 15°C ~ 35°C, 30% ~ 60%, 86Kpa ~ 106Kpa)	IEC61000-4-2 GB/T17626.2-1998
8	Vibration (Non-operation)	Frequency range: 10~55Hz, Stroke: 1.mm Sweep: 10Hz~55Hz~10Hz 2 hours for each direction of X .Y. Z. (package condition)	IEC60068-2-6 GB/T2423.5-1995
9	Shock (Non-operation)	60G 6ms, ± X, ±Y , ± Z 3 times for each direction	IEC60068-2-27 GB/T2423.5-1995
10	Package Drop Test	Height: 60 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8-1995

Note: 1. T_{S} is the temperature of panel's surface.

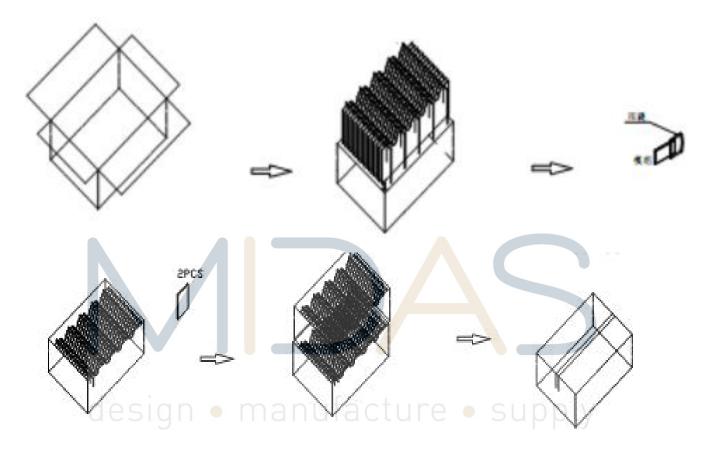
2. Ta is the ambient temperature of sample.

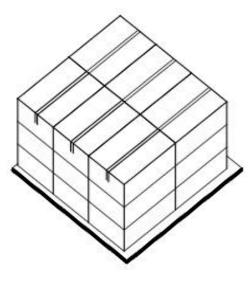
Mechanical Drawing



Packing

Packing Method





Precautions for Use of LCD modules

Handling Precautions

1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water ; Ketene ; Aromatic solvents
- 1.6. Do not attempt to disassemble the LCD Module.
- 1.7. If the logic circuit power is off, do not apply the input signals.
- 1.8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- 1.8.1. Be sure to ground the body when handling the LCD Modules.
- 1.8.2. Tools required for assembly, such as soldering irons, must be properly ground.
- 1.8.3. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- 1.8.4. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

Storage Precautions

2.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

2.2. The LCD modules should be stored under the storage temperature range If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0° C $\sim 40^{\circ}$ C Relatively humidity: $\leq 80^{\circ}$

2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.