



Customer Information Notification

2019020171

Issue Date: 27-Mar-2019

Effective Date: 28-Mar-2019

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

This notice is NXP Company Proprietary.



Change Category

- | | | | | |
|--|---|--|---|--|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input checked="" type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware | <input type="checkbox"/> Other | | | |

i.MX 7Dual/Solo Data Sheet Update to Rev6

Description

NXP Semiconductors announces a data sheet update for the i.MX 7Dual/Solo to revision 6. The revision history included in the updated documents provides a detailed description of the changes. Changes are summarized below.

i.MX 7Solo data sheet Changes:

Updated maximum value for DRAM_VREF from 30 to 1 in Table 12, "Maximum supply currents".

i.MX7 Dual data sheet Changes:

1. Added information related to new part number, MCIMX7D5EVK10SD in Table 1, "Orderable parts".

2. Updated maximum value for DRAM_VREF from 30 to 1 in Table 12, "Maximum supply currents".

3. In Table 22, "PCIe PHY reference clock timing requirements"

-Updated "Min." column in "Absolute maximum input voltage" from 33 to -- and "Absolute minimum input voltage" from 400 to -0.3

-Updated "Max." column in "Absolute crossing point voltage" from 1550 to 550 and "Absolute minimum input voltage" from -0.3 to --

The i.MX 7Solo data sheet revision 6 is attached to this notice, and can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-based-processors-and-mcus/i.mx-applications-processors/i.mx-7-processors/i.mx-7solo-processors-heterogeneous-processing-with-arm-cortex-a7-and-cortex-m4-cores:i.MX7S?tab=Documentation_Tab

The i.MX 7Dual data sheet revision 6 is attached to this notice, and can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-based-processors-and-mcus/i.mx-applications-processors/i.mx-7-processors/i.mx-7dual-processors-heterogeneous-processing-with-dual-arm-cortex-a7-cores-and-cortex-m4-core:i.MX7D?tab=Documentation_Tab

Reason

The datasheet has been updated to correct errors and / or provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Additional information

Affected products and sales history information: see attached file

Additional documents: [view online](#)



Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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Position Product Marketing Manager
e-mail address paulo.knirsch@nxp.com

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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