

Keywords: linear regulator, LDO, MAX38902A, MAX38902B, MAX38902, power supply noise, power supply rejection ratio, PSRR

APPLICATION NOTE 6650

# HOW TO MEASURE THE POWER SUPPLY REJECTION RATIO FOR THE MAX38902A/MAX38902B

*Abstract: Noise on power supplies typically propagates to the output and is generally unwanted, especially in sensitive applications such as precision measurement and RF communication. Linear regulators (LDOs) reject AC noise based on their power-supply rejection ratio (PSRR). PSRR is a function of noise frequency and input-to-output voltage difference. Furthermore, PSRR values are not consistently presented in manufacturers' data sheets. This application note shows the detailed measurement steps for determining the PSRR of the MAX38902A/B using Bode 100 from OMICRON, thereby equipping readers to perform their own PSRR measurements under the appropriate conditions for their application.*

## Introduction

The power-supply rejection ratio (PSRR) describes the ability of an amplifier to maintain its output voltage as its DC power-supply voltage is varied. The ratio can be expressed as follows:  $PSRR = (\text{change in } V_{IN})/(\text{change in } V_{OUT})$ . **Figure 1** shows the concept of the PSRR formula, where  $v_{in}$  and  $v_{out}$  represent the change in input and output, respectively.

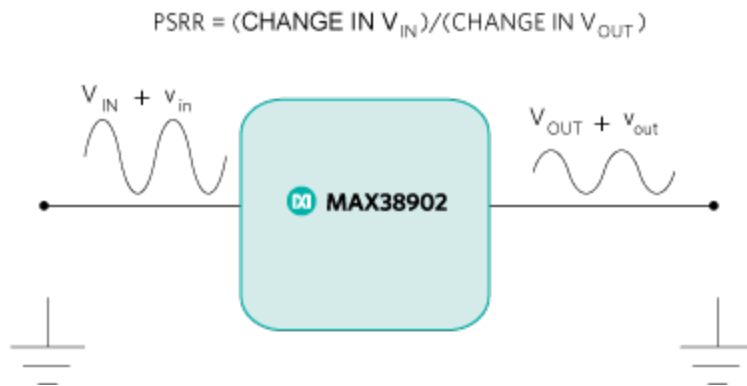


Figure 1. MAX38902 PSRR block diagram.

In practice, noise on an input supply also propagates to the output. In op amps, positive and negative supplies result in PSRR+ and PSRR-. In low-dropout regulators (LDOs), however, one PSRR value is the measure of how well the regulator rejects an AC signal riding on a nominal DC voltage. In general, PSRR is expressed in decibels and is defined over a wide frequency range (10Hz to 1MHz is common) with the following equation:

$$PSRR = -20\text{Log} (\text{Ripple Input})/(\text{Ripple Output})$$

where Ripple Input is the RMS value of the change in  $V_{IN}$  (applied power supply) at input and Ripple Output is the RMS value of the change in the voltage at output.

Note that PSRR is a negative value, but for simplicity we sometimes drop the sign and mention the absolute value.

PSRR performance greatly depends on the difference in input-to-output voltage. As the input voltage falls and the LDO approaches dropout, PSRR performance degrades significantly. This happens because when input voltage is reduced, the power pMOS pass device approaches the boundary of saturation operation and requires a significant gate-voltage swing to maintain regulation. The specific input voltage where the PSRR degradation occurs depends on the LDO's dropout specification.

As a general rule, LDOs have significant degradation in their ability to reject input power-supply ripple when input is within three times the dropout voltage specification. For example, the MAX38902A/B guarantee dropout voltage of 100mV and, therefore, should not be run with less than 300mV of drop if a reasonable PSRR (~40dB) performance is required. Increasing the input voltage can lead to PSRR improvement. At the same time, a higher input-to-output voltage increases the LDO's power dissipation and adversely affects efficiency. This additional power loss is generally undesirable. An LDO with a better dropout performance potentially offers the best PSRR with the smallest input-to-output voltage, yielding greater efficiency.

**Figure 2** shows the MAX38902A/B's PSRR at two different  $V_{IN} - V_{OUT}$  values. The blue curve shows PSRR at  $V_{IN} - V_{OUT} = 100\text{mV}$ , where the PSRR drops between -50dB and -60dB; the green curve shows PSRR at  $V_{IN} - V_{OUT} = 400\text{mV}$ , where the PSRR reaches -80dB. LDOs usually exhibit poorer PSRR at higher frequencies. However, the MAX38902A/B have PSRR ~-60dB at 5MHz and at 10kHz, PSRR is -80dB.

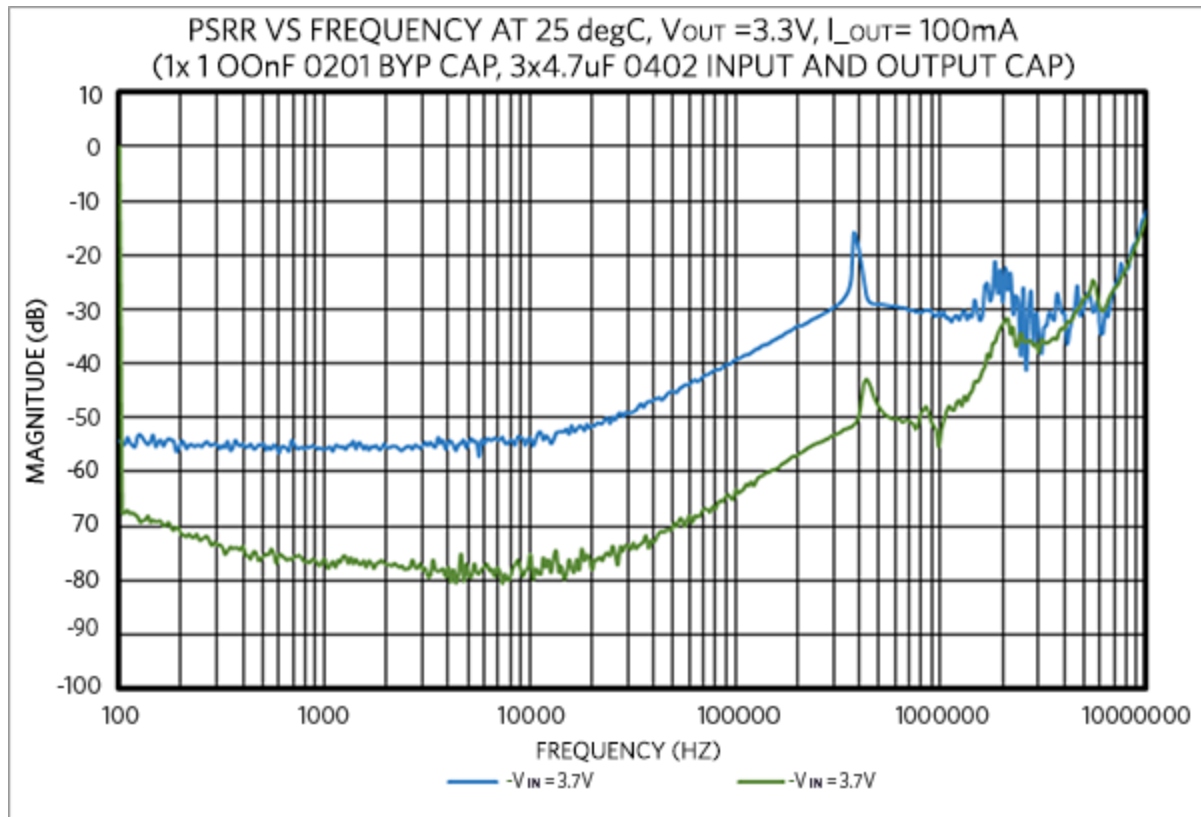


Figure 2. MAX38902A/B PSRR at  $V_{IN} - V_{OUT} = 400mV$  and  $V_{IN} - V_{OUT} = 100mV$ . Because PSRR can be measured under various conditions, manufacturers do not include all the conditions in their product data sheets. In this application note, we show the detailed measurement steps for determining the PSRR of the MAX38902A/B using the Bode 100 from OMICRON®, allowing readers to perform their own PSRR measurements under the appropriate conditions for their application. **Figure 3** shows the required setup for the measurements.

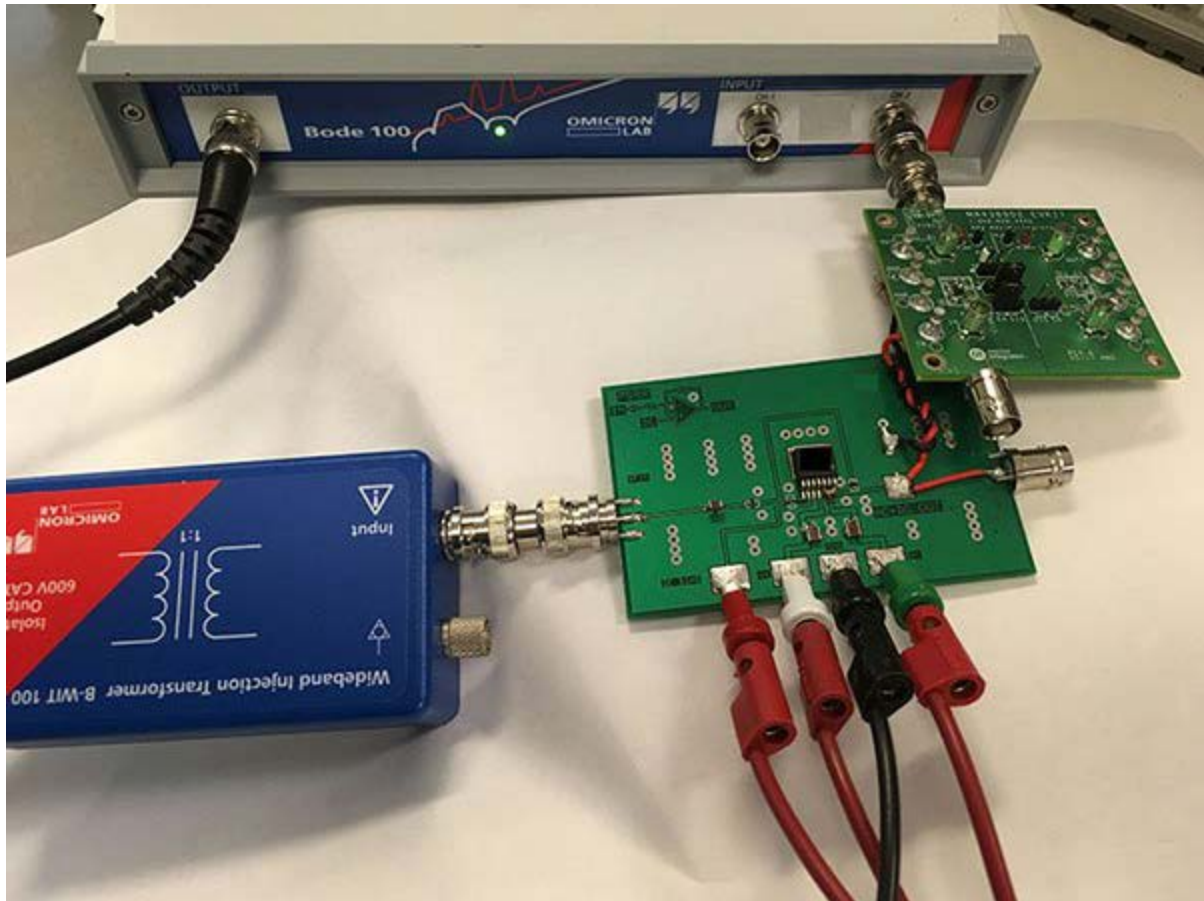


Figure 3. Required setup for PSRR measurement, using the Bode 100 with injection transformer and the MAX3892 LDO.

## Measurement

### Required Equipment

The following equipment is required to conduct measurements:

- [MAX3892 EV Kit](#)
- Amplifier capable of carrying 2A load
- OMICRON Lab Bode 100
- OMICRON Lab Wideband Injection Transformer

### Setup

When measuring the PSRR using the Bode 100, ensure that the MAX3892 EV kit's input and output connectors have direct connections to the Bode 100 channel, as shown in **Figure 4**. This reduces additional noise in the system. **Figure 5** is a simplified block diagram of the entire measurement setup.



Figure 4. Direct connections between the MAX3892 EV kit and Bode 100.

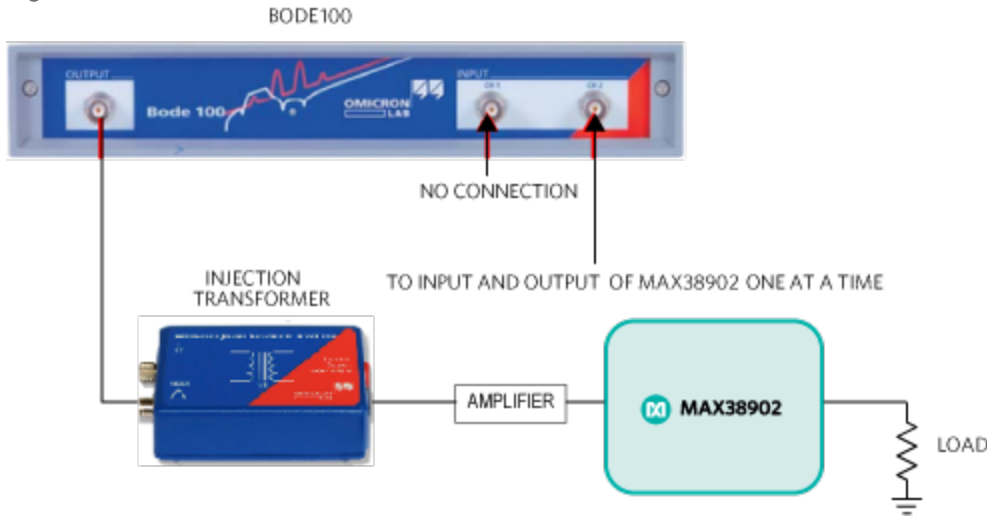


Figure 5. Measurement setup block diagram.

### MAX38902 EV Kit Setup

1. Connect the BNC adapters with 5W load at both the  $V_{IN}$  and  $V_{OUT}$  of the MAX38902A/B.
2. Connect the injection transformer to the input of the amplifier and from the output of the amplifier to the  $V_{IN}$  of the MAX38902 EV kit.
3. Apply the desired input voltage at  $V_{IN}$  of the MAX38902 EV Kit.
4. Set  $V_{OUT}$  of the MAX38902A/B and apply a resistive load at which the measurement is desired.

**Note:** Do the PSRR measurement at  $V_{IN}$  and  $V_{OUT}$  of the MAX38902 EV kit one at a time. First, measure the PSRR of  $V_{IN}$  and then measure the PSRR of  $V_{OUT}$ ; then, at the end, subtract the noise at  $V_{OUT}$  and  $V_{IN}$  to get the PSRR. Measure the  $V_{IN}$  PSRR to ensure there is no signal at input and it should show a flat line when measuring the PSRR at  $V_{IN}$ .

### Bode 100 Setup

To ensure a proper connection of the Bode 100 to the MAX38902 EV kit:

1. Open the Bode Analyzer Suite (BAS) (latest version available at [www.omicron-lab.com](http://www.omicron-lab.com)).
2. The Bode 100 is set up as follows (see **Figure 6**):
  - o Measurement Mode: Frequency Sweep Mode
  - o Start frequency: 100Hz
  - o Stop frequency: 10MHz
  - o Sweep: Logarithmic
  - o Number of points: 401 (or more)

- o Receiver bandwidth: 30Hz
- o Attenuator Receiver 1: 20dB
- o Attenuator Receiver 2: 20dB
- o Source level: -10dBm

**Note:** Adjust the source level accordingly by monitoring the output signal of the amplifier injected to the input of MAX38902EVKIT. Usually, a source level of 50mV to 100mV peak-to-peak is desirable. Monitor this with a scope probe on the oscilloscope.

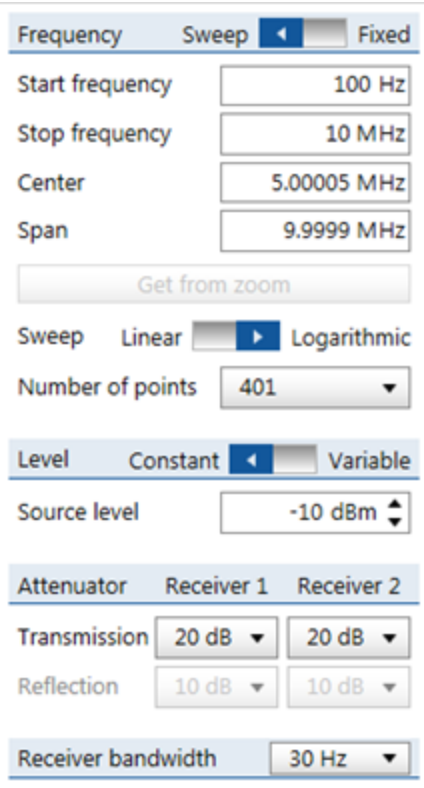


Figure 6. Bode 100 setup.

3. To configure the **Hardware Setup**, go to **Home | Impedance/Reflection** and only turn the switch of CH2 to the ON position; only use this channel for  $V_{IN}$  and  $V_{OUT}$  of the MAX38902A/B, one at a time. Note that either CH1 or CH2 can be used for this measurement, even though we use CH2 here (see **Figure7**).

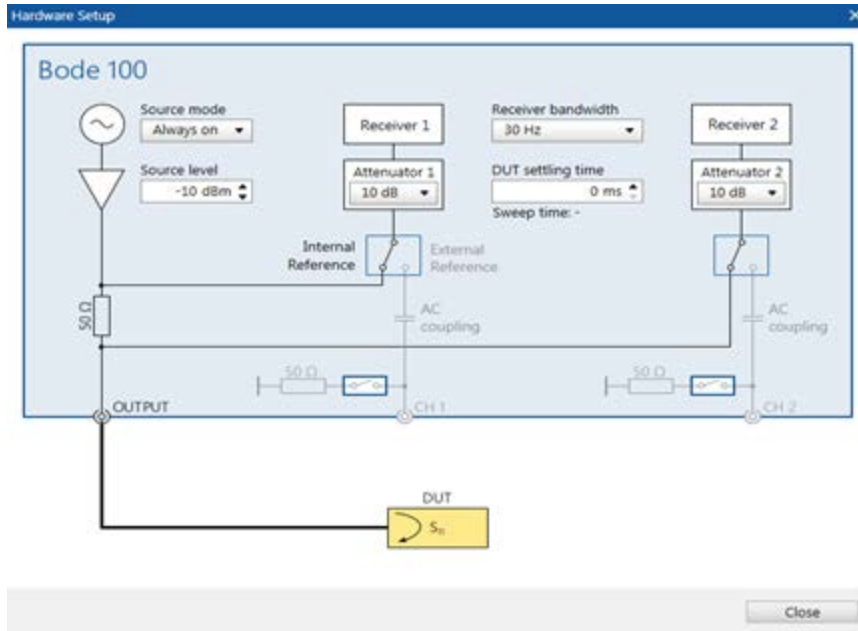


Figure 7. Hardware setup in Bode 100.

- Now start the measurement by sweeping the frequency using the single-sweep mode. First sweep for  $V_{IN}$  by connecting the MAX38902 BNC connector to the CH2 of the Bode 100. When done, save the result in a Microsoft® Excel® spreadsheet by going to **Report | save as excel sheet**. Repeat this step for  $V_{OUT}$  and save the measurement. Note: When measuring  $V_{IN}$  noise, the result should be flat at 0 level. At higher frequencies, the noise is not flat, due to the effect of the input capacitor.

## Results

Open the measurements saved in Excel. Find the  $V_{IN}$  and  $V_{OUT}$  noise, which are in dB, then subtract  $V_{OUT} - V_{IN}$  and find out the PSRR versus frequency. The result can be plotted similar to Figure 2.

## References

MAX38902EVKIT

OMICRON Lab Bode Analyzer Suite

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## Related Parts

<a href="#">MAX38902A</a>	12 $\mu$ V <sub>RMS</sub> Low Noise 500mA LDO Linear Regulator	<a href="#">Free Samples</a>
<a href="#">MAX38902B</a>	12 $\mu$ V <sub>RMS</sub> Low Noise 500mA LDO Linear Regulator	<a href="#">Free Samples</a>

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**More Information**

For Technical Support: <https://www.maximintegrated.com/en/support>

For Samples: <https://www.maximintegrated.com/en/samples>

Other Questions and Comments: <https://www.maximintegrated.com/en/contact>

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Application Note 6650: <https://www.maximintegrated.com/en/an6650>

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