

IRF7862PbF

HEXFET® Power MOSFET

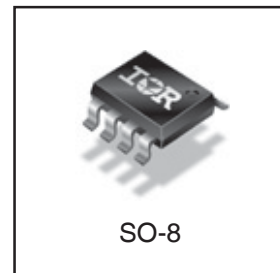
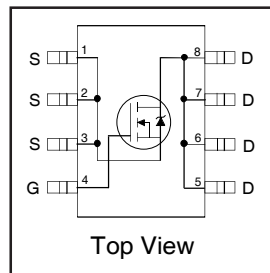
Applications

- Synchronous MOSFET for Notebook Processor Power
- Synchronous Rectifier MOSFET for Isolated DC-DC Converters

V_{DSS}	R_{DS(on)} max	Q_g
30V	3.7mΩ@V_{GS} = 10V	30nC

Benefits

- Very Low R_{DS(on)} at 4.5V V_{GS}
- Ultra-Low Gate Impedance
- Fully Characterized Avalanche Voltage and Current
- 20V V_{GS} Max. Gate Rating
- 100% tested for R_g
- Lead-Free



Absolute Maximum Ratings

	Parameter	Max.	Units
V _{DS}	Drain-to-Source Voltage	30	V
V _{GS}	Gate-to-Source Voltage	± 20	
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V	21	A
I _D @ T _A = 70°C	Continuous Drain Current, V _{GS} @ 10V	17	
I _{DM}	Pulsed Drain Current ①	170	
P _D @ T _A = 25°C	Power Dissipation	2.5	W
P _D @ T _A = 70°C	Power Dissipation	1.6	
	Linear Derating Factor	0.02	W/°C
T _J	Operating Junction and	-55 to + 150	°C
T _{STG}	Storage Temperature Range		

Thermal Resistance

	Parameter	Typ.	Max.	Units
R _{θJL}	Junction-to-Drain Lead ⑤	—	20	°C/W
R _{θJA}	Junction-to-Ambient ④⑤	—	50	

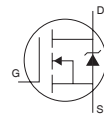
Notes ① through ⑤ are on page 9

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Static @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-to-Source Breakdown Voltage	30	—	—	V
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.023	—	V/°C



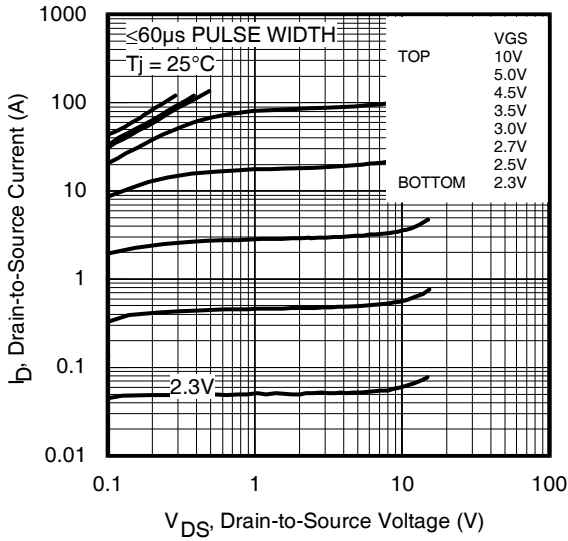


Fig 1. Typical Output Characteristics

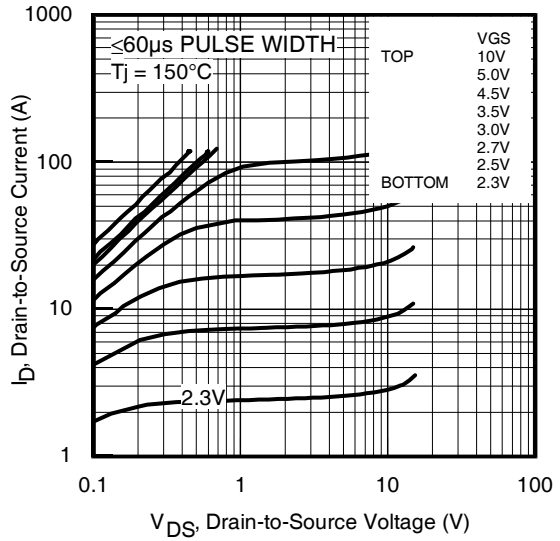


Fig 2. Typical Output Characteristics

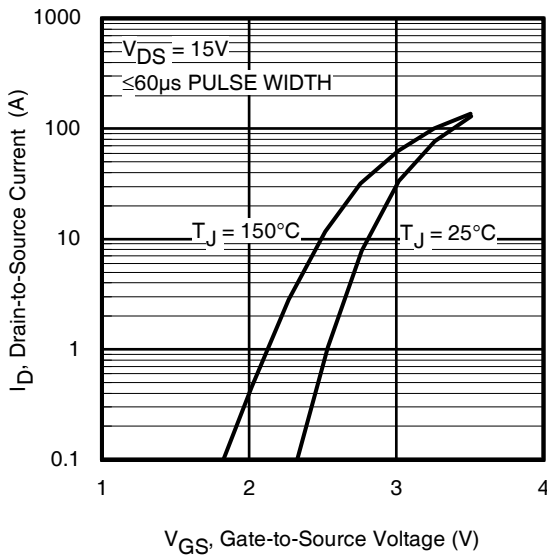


Fig 3. Typical Transfer Characteristics

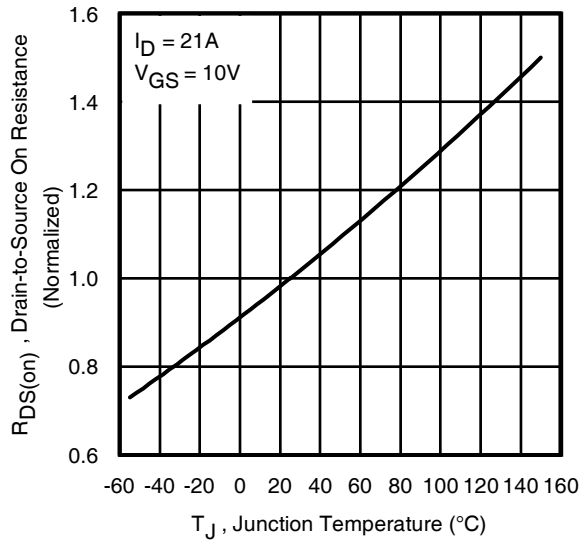


Fig 4. Normalized On-Resistance vs. Temperature

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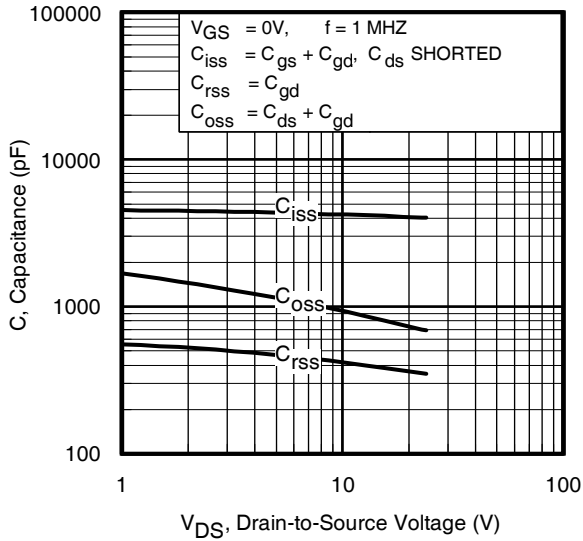


Fig 5. Typical Capacitance vs. Drain-to-Source Voltage

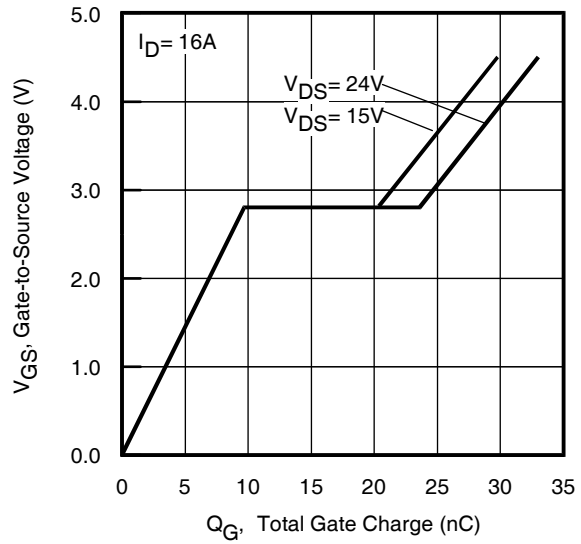


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

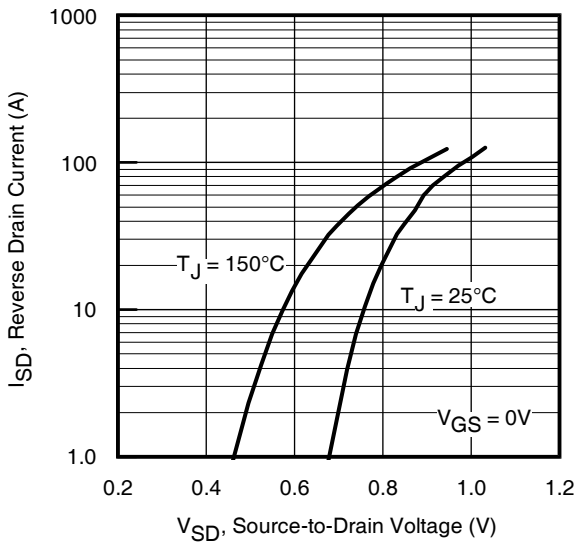


Fig 7. Typical Source-Drain Diode Forward Voltage

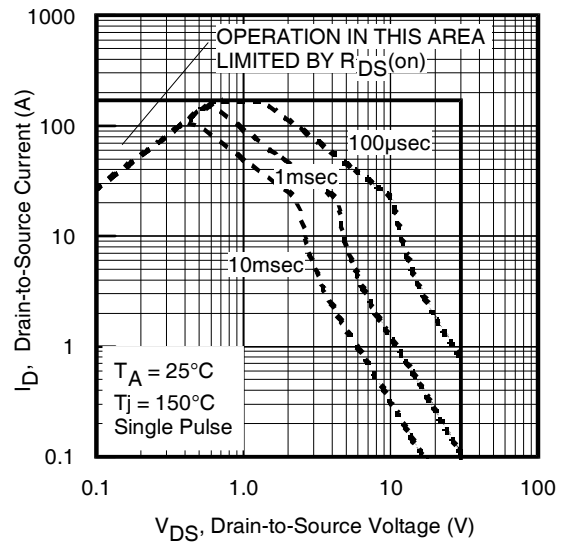


Fig 8. Maximum Safe Operating Area

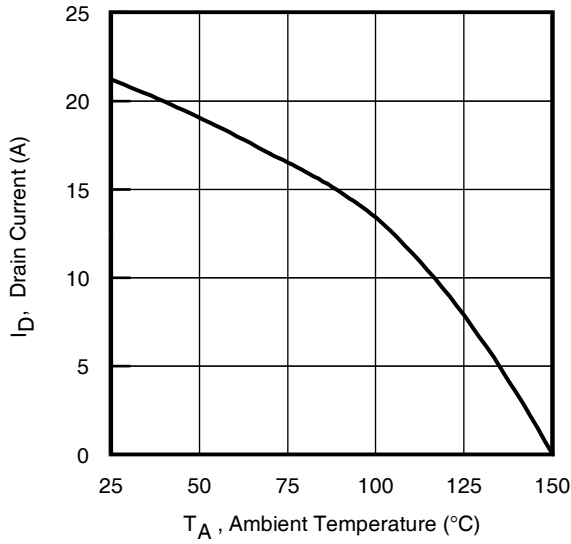


Fig 9. Maximum Drain Current vs. Ambient Temperature

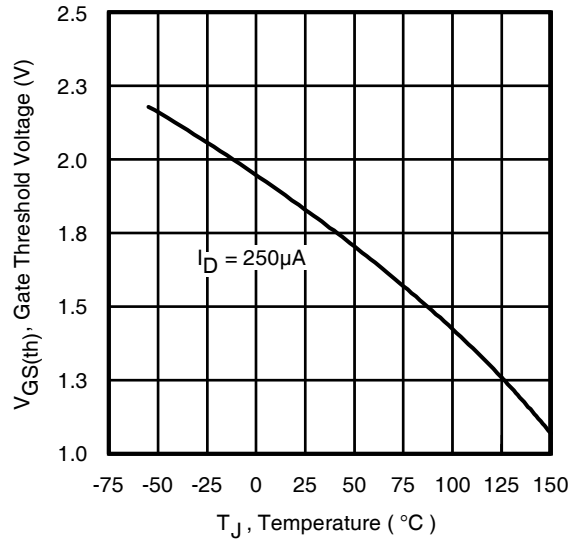


Fig 10. Threshold Voltage vs. Temperature

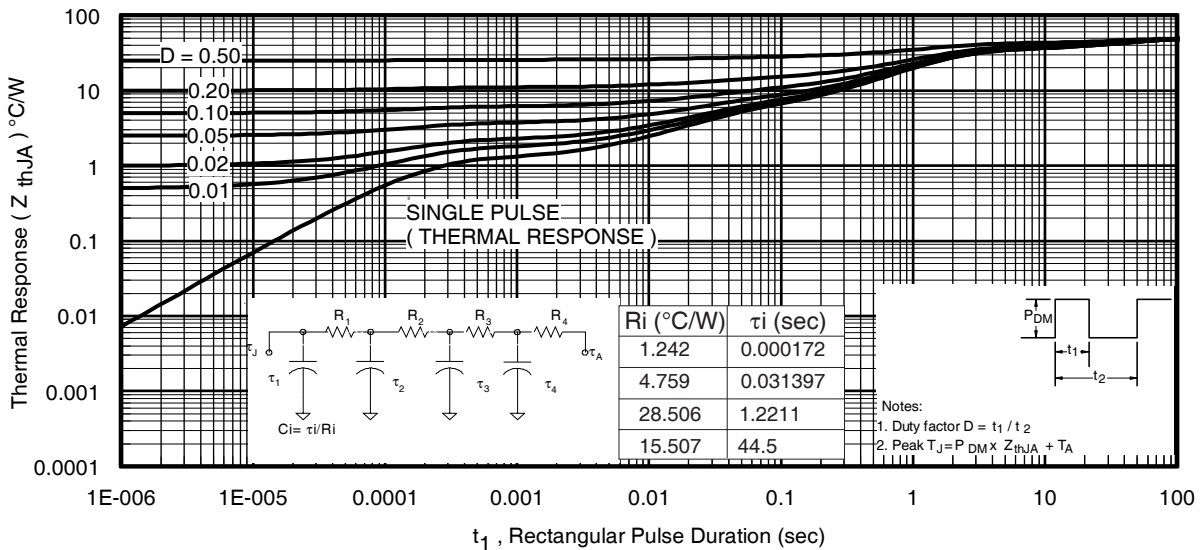


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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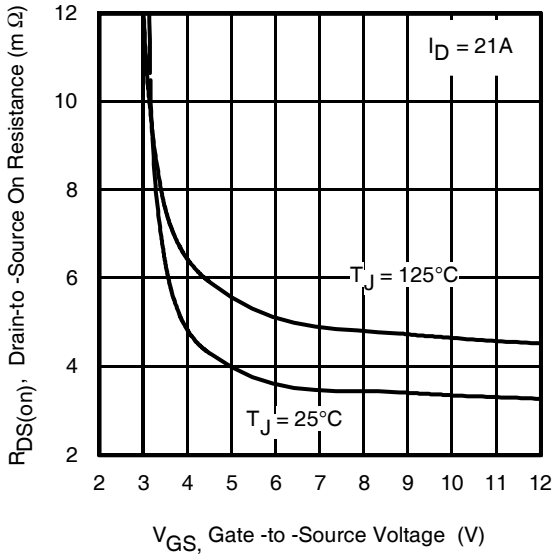


Fig 12. On-Resistance vs. Gate Voltage

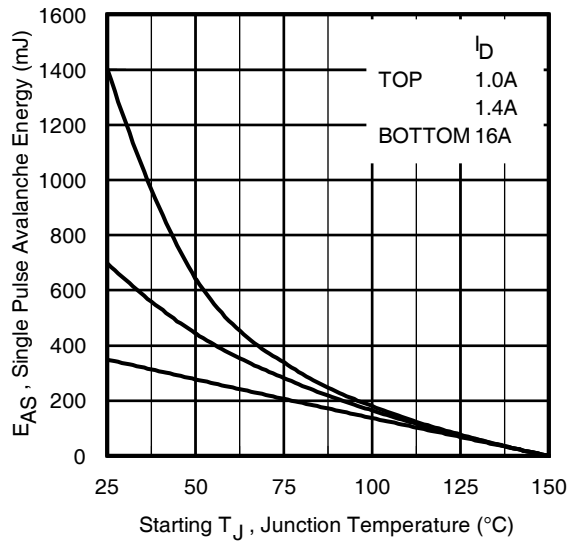


Fig 13. Maximum Avalanche Energy vs. Drain Current



Fig 14. Unclamped Inductive Test Circuit and Waveform



Fig 15. Gate Charge Test Circuit



Fig 16. Gate Charge Waveform

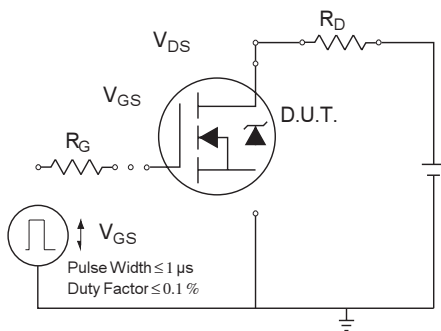
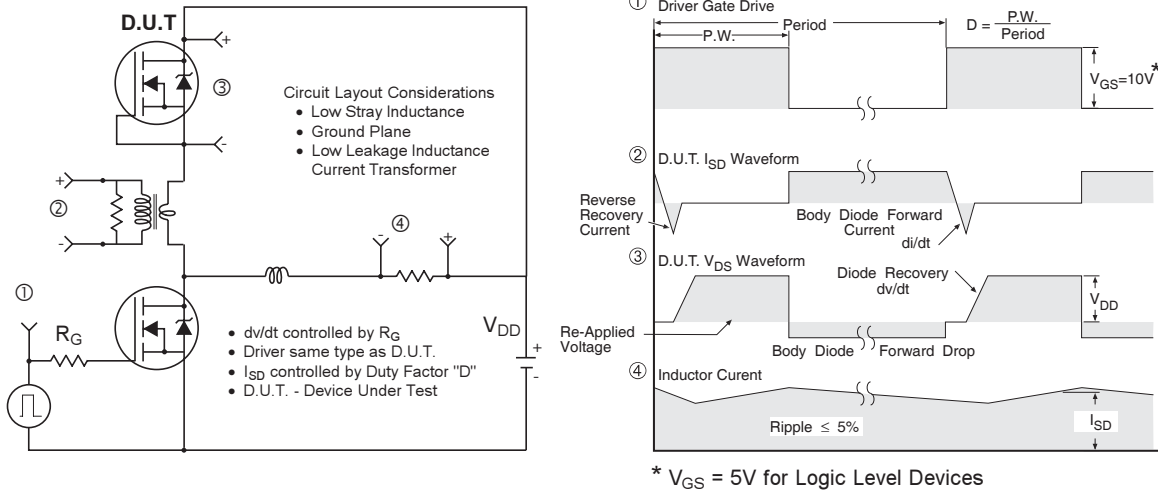


Fig 18a. Switching Time Test Circuit

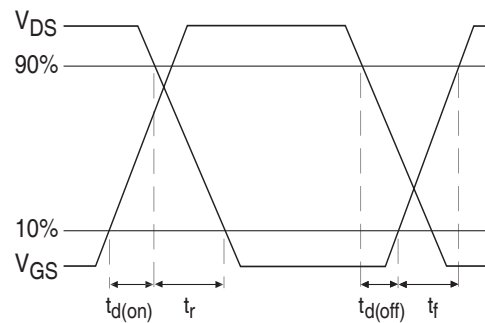
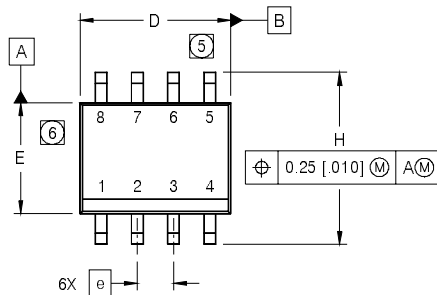


Fig 18b. Switching Time Waveforms

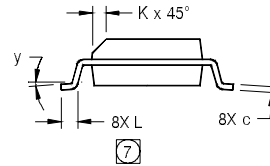
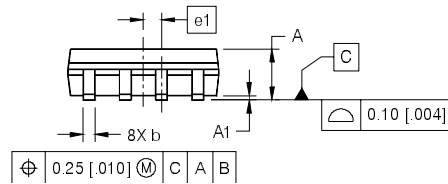
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SO-8 Package Outline (Dimensions are shown in millimeters (inches))



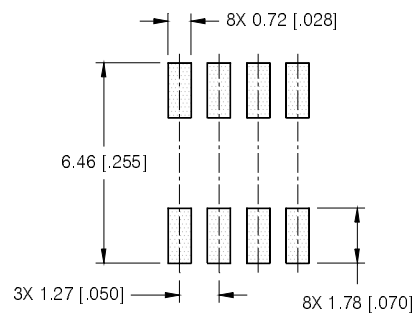
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°



NOTES:

- DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
- CONTROLLING DIMENSION: MILLIMETER
- DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- ⑤** DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 [0.006].
- ⑥** DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 [0.010].
- ⑦** DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.

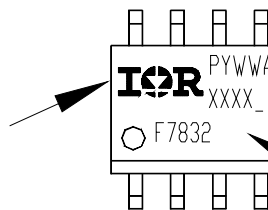
FOOTPRINT



SO-8 Part Marking

EXAMPLE: THIS IS AN IRF7832U (MOSFET)

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RECTIFIER
LOGO

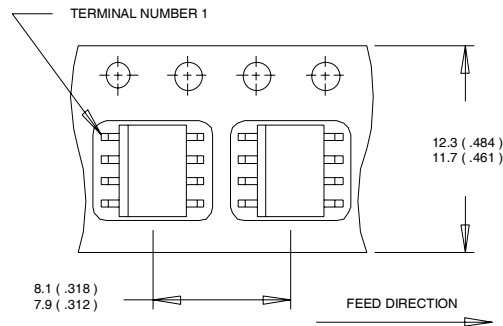


DATE CODE (YWW)
P = DESIGNATES LEAD - FREE PRODUCT (OPTIONAL)
Y = LAST DIGIT OF THE YEAR
WW = WEEK
A = ASSEMBLY SITE CODE
LOT CODE
PART NUMBER

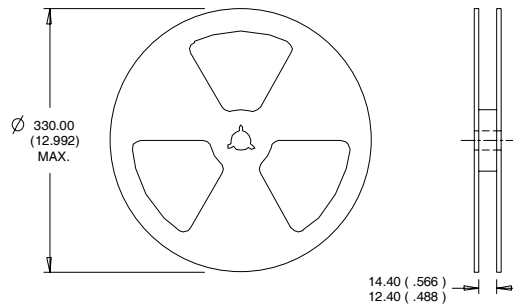
Note: For the most current drawing please refer to IR website at <http://www.irf.com/package>

SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^\circ\text{C}$, $L = 2.7\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 16\text{A}$.
- ③ Pulse width $\leq 400\mu\text{s}$; duty cycle $\leq 2\%$.
- ④ When mounted on 1 inch square copper board.
- ⑤ R_θ is measured at T_J of approximately 90°C .

Note: For the most current drawing please refer to IR website at <http://www.irf.com/package>

Data and specifications subject to change without notice.
 This product has been designed and qualified for the Consumer market.
 Qualification Standards can be found on IR's Web site.