



Customer Information Notification

201906008I

Issue Date: 06-Jul-2019

Effective Date: 07-Jul-2019

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

This notice is NXP Company Proprietary.



Change Category

- | | | | | |
|--|---|--|---|--|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input checked="" type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware | <input type="checkbox"/> Other | | | |

i.MX8Mscale850D Data Sheet Rev 1.1 Update

Description

NXP Semiconductors announces a data sheet update for the i.MX8Mscale850D to revision 1.1. The revision history included in the updated documents provides a detailed description of the changes. Changes are summarized below:

Data Sheet Changes:

1. Updated the package type information in cover sheet
2. Updated the package type information in the Figure 2, "Part number nomenclature- i.MX 8M Dual/8M QuadLite / 8M Quad processors"
3. Updated eCSPI description in the Table 3, "i.MX 8M Dual / 8M QuadLite / 8M Quad modules list"
4. Added the core voltage, analog domain voltage, PLL 1.8 V voltage, 25 MHz crystal voltage, 27MHz crystal voltage, DDR I/O voltage, HDMI voltage, MIPI voltage, PCIe voltage, temperature sensor voltage, and fuse power in the Table 5, "Absolute maximum ratings"

5. Updated the Table 6, "Thermal resistance data"

6. Updated the RUN mode unit in the Table 10, "Chip power in different LP mode"

The i.MX8Mscale850D data sheet revision 1.1 is attached to this notice, and can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/arm-based-processors-and-mcus/i.mx-applications-processors/i.mx-8-processors/i.mx-8m-family-armcortex-a53-cortex-m4-audio-voice-video:i.MX8M?tab=Documentation_Tab

Reason

The data sheet has been updated to correct errors and / or provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Additional information

Affected products and sales history information: see attached file

Additional documents: [view online](#)



Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name Christina Shao

Position Product Engineer

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At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

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