



MPLAB Code Coverage User's Guide

Notice to Customers



Important:

All documentation becomes dated and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our website (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a "DS" number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is "DSXXXXXA," where "XXXXX" is the document number and "A" is the alphabetic revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Help Content to open a list of available online help files.



Table of Contents

Notice to Customers.....	1
1. MPLAB Code Coverage Overview.....	3
2. MPLAB Code Coverage Details.....	4
2.1. Compiler Operation	4
2.2. MPLAB X IDE Operation.....	4
3. Get the Software.....	5
4. Enable/Disable Code Coverage.....	6
5. View Code Coverage Output.....	7
5.1. Color Meanings.....	8
5.2. Address Units.....	8
5.3. Code Coverage Tab Buttons.....	9
6. Create a Code Coverage HTML Report	10
7. Tips and Tricks.....	11
8. Increase Code Coverage.....	12
The Microchip Website.....	13
Product Change Notification Service.....	13
Customer Support.....	13
Microchip Devices Code Protection Feature.....	13
Legal Notice.....	13
Trademarks.....	14
Quality Management System.....	14
Worldwide Sales and Service.....	15

1. MPLAB Code Coverage Overview

MPLAB Code Coverage (SW006026-COV) is an MPLAB® XC C compiler add-on license that provides visibility as to what portions of your code are being executed. In MPLAB X IDE, execute your test cases to completion to see:

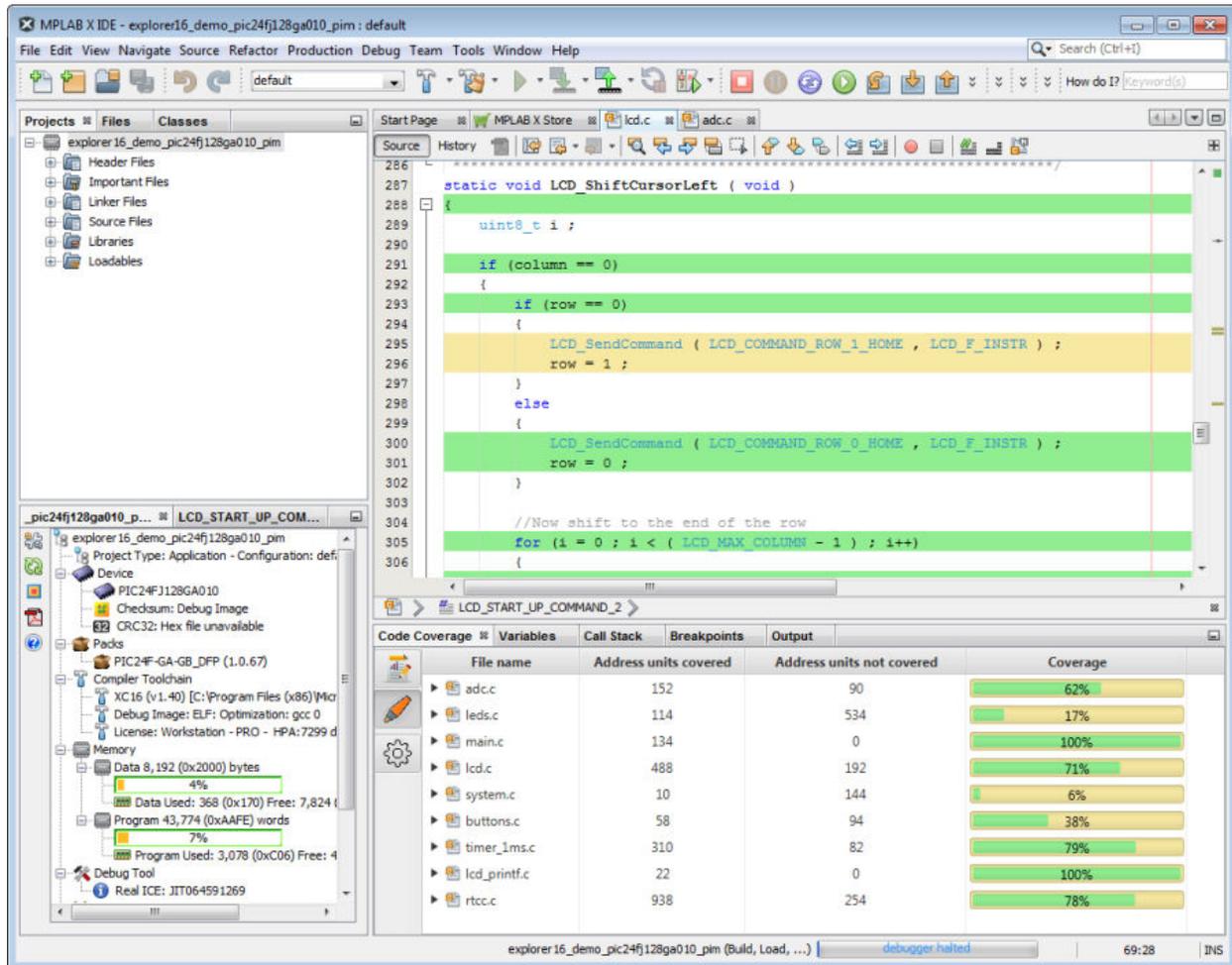
- Editor text highlighted by colors representing the coverage.
- A Code Coverage tab with a report displaying percentages of code covered.

The information displayed in the IDE may be written to an HTML Report for later viewing.

Compared to trace, code coverage differs in the following ways:

- Trace indicates when and how many times each line of code was executed.
- Code coverage indicates which portions of code have or have not been executed.

Figure 1-1. Code Coverage in MPLAB X IDE



2. MPLAB Code Coverage Details

To view code coverage output, an MPLAB XC C compiler that supports code coverage must be used with MPLAB X IDE.

2.1 Compiler Operation

Code coverage is supported by MPLAB XC C compiler instrumentation. The compiler adds a small amount of code in program memory to update flags in RAM to indicate coverage points.

Note: If your code fills most of program memory, code coverage may not have enough memory to work.

Note: Optimized code can affect how coverage operates. For more information, see section 7. [Tips and Tricks](#).

Compiler Operation - No Add-On License

Without the license, the compilers provide a simple ratio of executed/all coverage points. The compilers don't provide address ranges for the coverage points (bits). So address range overlap, due to multiple paths through the code (e.g., if statements), cannot be used to consider coverage.

Compiler Operation with MPLAB Code Coverage

With a license, each coverage point corresponds to several address ranges. So for multiple code paths, ranges covered in at least one coverage point can be considered covered.

2.2 MPLAB X IDE Operation

MPLAB X IDE Operation - No Add-On License

MPLAB XC C compilers that **do not** have the add-on license will display the compiler simple ratio.

MPLAB X IDE Operation with MPLAB Code Coverage

MPLAB XC C compilers that **do** have the add-on license will display highlighted covered code and percentages of covered code for project files. A report of the coverage may be generated also.

3. Get the Software

To use code coverage, you will need an MPLAB XC C compiler that supports code coverage and an MPLAB X IDE version that supports code coverage display. For full code coverage features, you will need the MPLAB Code Coverage add-on license.

Acquiring MPLAB XC C Compilers

The MPLAB Code Coverage add-on license may be used with MPLAB XC C compilers that support code coverage, beginning with the following versions:

- MPLAB XC8 v2.10
- MPLAB XC16 v1.40
- MPLAB XC32 v2.30

MPLAB XC C compilers may be downloaded for free at <https://www.microchip.com/mplab/compilers>.

Acquiring MPLAB X IDE

MPLAB X IDE support for viewing code coverage output begins with version 5.25. The IDE may be downloaded for free at <https://www.microchip.com/mplab/mplab-x-ide>.

Acquiring MPLAB Code Coverage

An MPLAB Code Coverage add-on license may be purchased and activated like other compiler licenses. For more information, see “*Installing and Licensing MPLAB[®] XC C Compilers*” (DS50002059) for details. This license is for Workstations only (not Networks). This license may be used with FREE or PRO compilers. See also <http://www.microchip.com/mplab/codecoverage>.

4. Enable/Disable Code Coverage

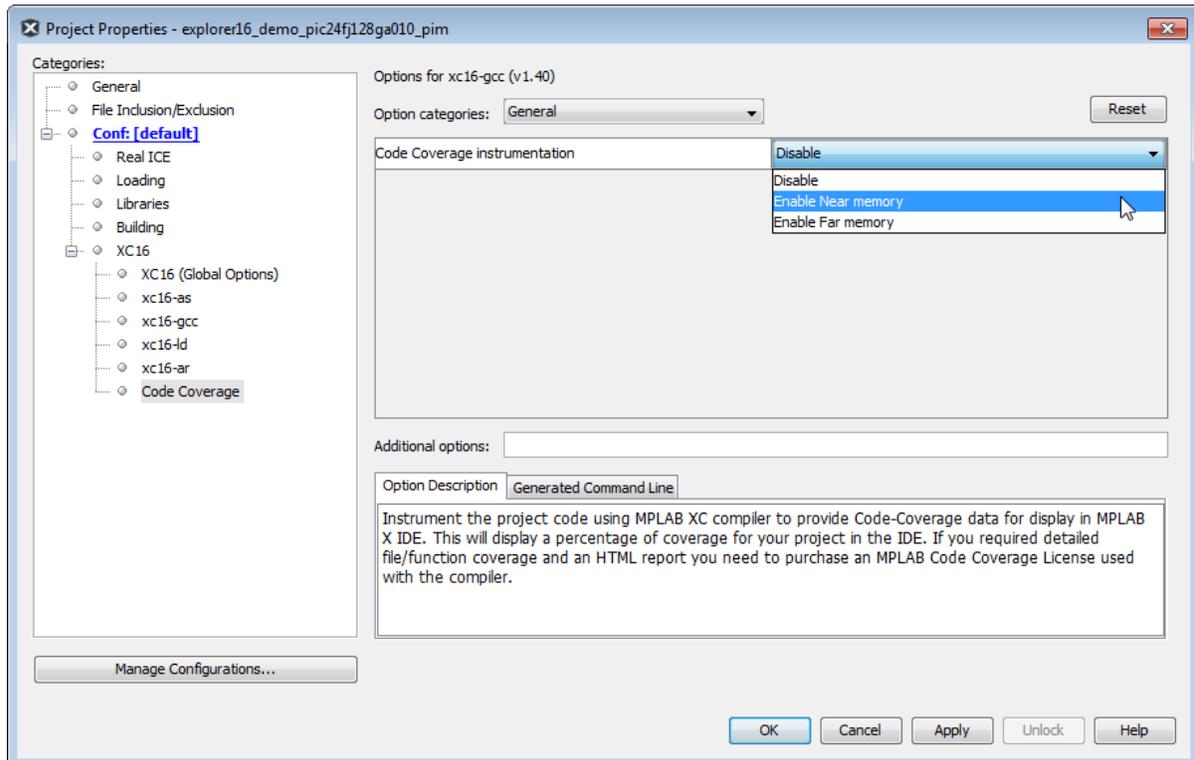
To enable or disable code coverage, complete the following steps:

1. Right click on the name of your project in the Projects window and select “Properties.”
2. Under “Categories,” find the project compiler, i.e., *XCnn*, where *nn* is either 8, 16 or 32.
3. Click on the “Code Coverage” under the compiler (see figure).
4. Select “Code Coverage Instrumentation.” Selections differ for each MPLAB XC compiler. See the table below.

Table 4-1. Enable Code Coverage Options by Compiler

MPLAB XC C Compiler	Enable Options	Description
XC8	Disable	Disable code coverage.
	Enable	Enable code coverage.
XC16	Disable	Disable code coverage.
	Enable Near memory	Use Near RAM space for code coverage instrumentation. This will result in instrumentation code that is less complex and faster. It is recommended that you select Near first, even if you use Far for your program code. If code coverage does not work, then select Far.
	Enable Far memory	Use Far RAM space for code coverage instrumentation. This will result in instrumentation code that is more complex and slower.
XC32	Disable	Disable code coverage.
	Enable	Enable code coverage.

Figure 4-1. Code Coverage Options - MPLAB XC16 Example



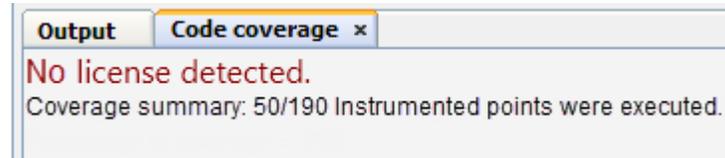
5. View Code Coverage Output

After you have enabled code coverage, debug your code and execute all test cases to completion, e.g., finish at an empty `while` loop or idle state for a state machine. Then pause/halt execution automatically (`__builtin_software_breakpoint`) or manually.

Note: If the code is still running, you can't be sure if all of the coverage points have been executed.

To view code coverage, select *Window>Debugging>Code Coverage*.

No Add-On License



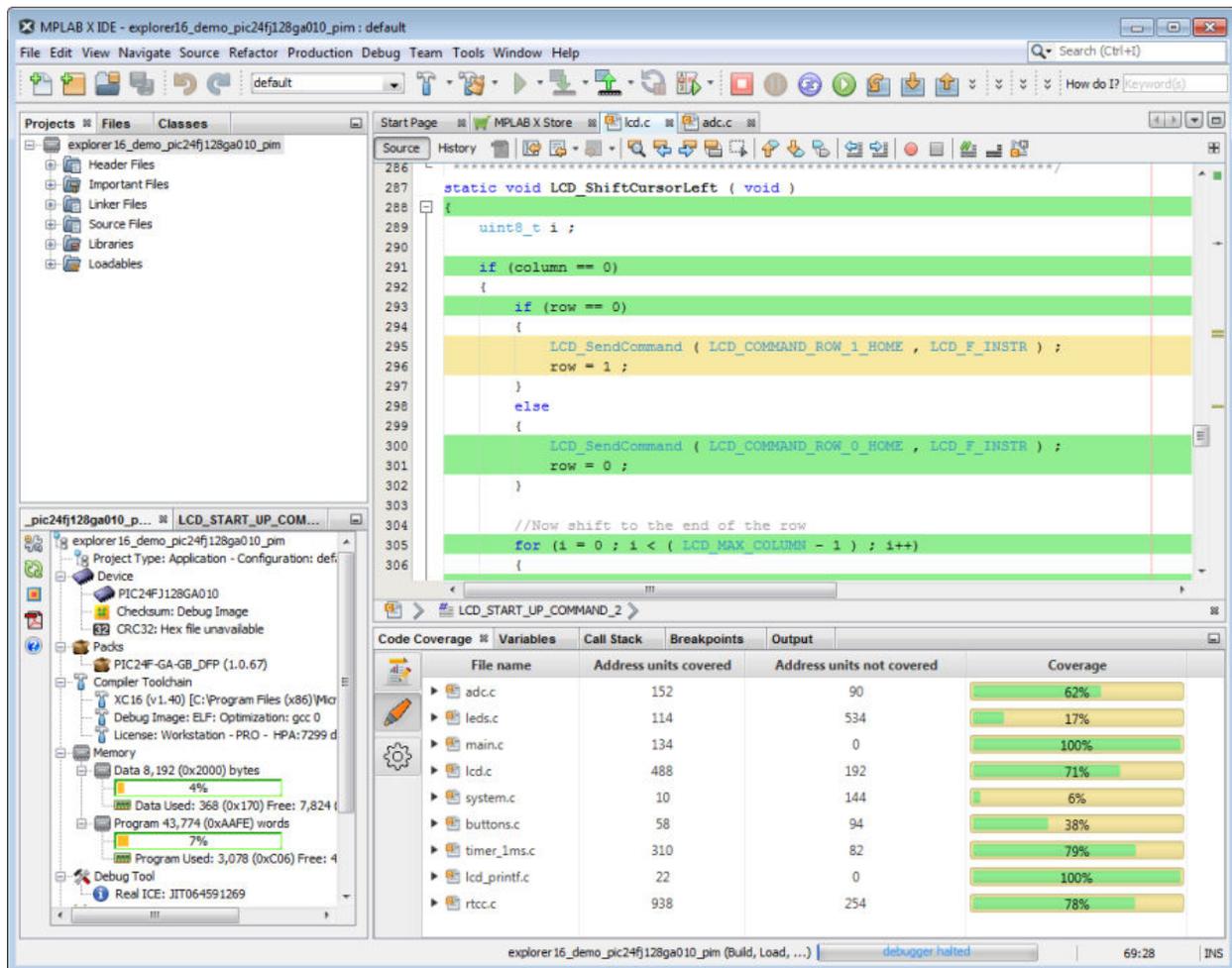
MPLAB Code Coverage Add-On License

- Lines of covered code will be highlighted in the editor.
- A summary report will be shown in the Code Coverage tab.

Summary Report Display	Description
File name	Names of all files in the project.
Address units covered	Address units covered and executed.
Address units not covered	Address units covered but not executed.
Coverage	The Coverage percentage represents $x/(x+y)$ where x = address units covered and executed and y = address units covered but not executed. x vs. y is shown visually in the bar.

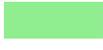
For more information on Address Units, see the [Address Units](#) section.

Figure 5-1. Code Coverage in MPLAB X IDE



5.1 Color Meanings

The meaning of colors in the Editor and on the Code Coverage tab are shown in the table below.

Color	Name	Meaning
	Green	Covered and Executed
	Yellow	Covered but not Executed
	White/None	Not Covered (for lines of code)

5.2 Address Units

An address unit (AU) can represent one or more program memory addresses. An instruction can consume a single AU, multiple AUs or a partial AU. For example, AVR and dsPIC instructions can consume multiple AUs, whereas MIPS and ARM instructions can consume partial AUs.

The AU coverage percentage will be greater than instruction coverage percentage if an instruction can consume multiple AUs. The AU coverage percentage will be less than instruction coverage percentage if an instruction can consume partial AUs.

5.3 Code Coverage Tab Buttons

Click on buttons in the gutter of this tab for the following functions.

	Generate HTML Report. This report will show the same data presented in the Code Coverage window.
	Toggle (enable/disable) color highlighting in editor window.
	Open Project Properties to enable/disable code coverage.

6. Create a Code Coverage HTML Report

Code coverage information may be saved into a file by clicking on the **Generate HTML Report** button on the **Code Coverage** tab.

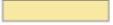
For information on the meaning of colors, see section 5. [View Code Coverage Output](#) .

Figure 6-1. Report Code Coverage

Source files
All
adc.c
leds.c
main.c
lcd.c
system.c
buttons.c
timer_1ms.c
lcd_printf.c
rtcc.c

All Source files
adc.c (62%)
leds.c (17%)
main.c (100%)
lcd.c (71%)
system.c (6%)
buttons.c (38%)
timer_1ms.c (79%)
lcd_printf.c (100%)
rtcc.c (78%)

Figure 6-2. Report Code Coverage Summary

Coverage Report - All Source files			
Package	Address units covered	Address units not covered	Coverage
Source files	2226	1390	61% 
adc.c	152	90	
buttons.c	58	94	
lcd.c	488	192	
lcd_printf.c	22	0	
leds.c	114	534	
main.c	134	0	
rtcc.c	938	254	
system.c	10	144	
timer_1ms.c	310	82	
Functions in these source file(s)		Coverage	
ADC_ChannelEnable		66%	
ADC_Read10bit		82%	
ADC_ReadPercentage		0%	
ADC_SetConfiguration		94%	
BSP_RTCC_Initialize		0%	

7. Tips and Tricks

Consider the following tips and tricks to get the code coverage you expect.

Simple Code

When performing code coverage on a simple program in `main()`, do not let the program simply end. Depending on the device, the code could reset or end up in an undefined state. This may interfere with code coverage calculation.

It is suggested that you place something like a `while(1)` loop at the end of `main()`. Then to see code coverage, either:

- Place a breakpoint at the last brace of the `while(1)` loop. When you hit the breakpoint, step once to ensure you have completed the loop to see the coverage output.
- Let the code run for a while before hitting **Pause**. This will ensure that the `while(1)` loop was run (and covered) at least once. View coverage output.

Optimized Code

Different optimizations (`-On`) may result in different coverage of code. Code may be optimized so that it becomes smaller, faster, or more efficient in its operation. Code that is optimized for size will produce different coverage results from code that is optimized for speed.

8. Increase Code Coverage

Note: Code coverage percentage is determined by Code Covered and Executed/Code Covered (Executed and Not Executed). By increasing the code covered and executed or decreasing total code covered, the code coverage percentage is increased.

The following tips may be useful in helping you increase coverage of your code.

Review Test Cases

Ensure the tests you are running mirror how the code is to be used. Considering all cases can lead to more tests, but also more coverage.

Create tests for each use case, as well as corner cases. Avoid repeated testing of code already tested.

Automate Tests

Use utilities to automate the testing process. Investigate tools that can read your source code, identify inputs & outputs, and program flow to develop relevant tests.

Remove Dead or Redundant Code

Getting rid of code that does not need to be tested will decrease total code and increase coverage.

To find dead code, inspect the sections of code that are not covered after a couple of testing runs and determine if this code is really needed.

To find redundant code, you may need a tool to search for repeating blocks. Then determine if the code blocks can be consolidated.

The Microchip Website

Microchip provides online support via our website at <http://www.microchip.com/>. This website is used to make files and information easily available to customers. Some of the content available includes:

- **Product Support** – Data sheets and errata, application notes and sample programs, design resources, user's guides and hardware support documents, latest software releases and archived software
- **General Technical Support** – Frequently Asked Questions (FAQs), technical support requests, online discussion groups, Microchip design partner program member listing
- **Business of Microchip** – Product selector and ordering guides, latest Microchip press releases, listing of seminars and events, listings of Microchip sales offices, distributors and factory representatives

Product Change Notification Service

Microchip's product change notification service helps keep customers current on Microchip products. Subscribers will receive email notification whenever there are changes, updates, revisions or errata related to a specified product family or development tool of interest.

To register, go to <http://www.microchip.com/pcn> and follow the registration instructions.

Customer Support

Users of Microchip products can receive assistance through several channels:

- Distributor or Representative
- Local Sales Office
- Embedded Solutions Engineer (ESE)
- Technical Support

Customers should contact their distributor, representative or ESE for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in this document.

Technical support is available through the website at: <http://www.microchip.com/support>

Microchip Devices Code Protection Feature

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the Microchip products in a manner outside the operating specifications contained in Microchip's Data Sheets. Most likely, the person doing so is engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as "unbreakable."

Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip's code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.

Legal Notice

Information contained in this publication regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your responsibility to ensure that your application meets with

your specifications. MICROCHIP MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION, INCLUDING BUT NOT LIMITED TO ITS CONDITION, QUALITY, PERFORMANCE, MERCHANTABILITY OR FITNESS FOR PURPOSE. Microchip disclaims all liability arising from this information and its use. Use of Microchip devices in life support and/or safety applications is entirely at the buyer's risk, and the buyer agrees to defend, indemnify and hold harmless Microchip from any and all damages, claims, suits, or expenses resulting from such use. No licenses are conveyed, implicitly or otherwise, under any Microchip intellectual property rights unless otherwise stated.

Trademarks

The Microchip name and logo, the Microchip logo, Adaptec, AnyRate, AVR, AVR logo, AVR Freaks, BesTime, BitCloud, chipKIT, chipKIT logo, CryptoMemory, CryptoRF, dsPIC, FlashFlex, flexPWR, HELDO, IGLOO, JukeBlox, KeeLoq, Kleer, LANCheck, LinkMD, maXStylus, maXTouch, MediaLB, megaAVR, Microsemi, Microsemi logo, MOST, MOST logo, MPLAB, OptoLyzer, PackeTime, PIC, picoPower, PICSTART, PIC32 logo, PolarFire, Prochip Designer, QTouch, SAM-BA, SenGenuity, SpyNIC, SST, SST Logo, SuperFlash, Symmetricom, SyncServer, Tachyon, TempTrackr, TimeSource, tinyAVR, UNI/O, Vectron, and XMEGA are registered trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

APT, ClockWorks, The Embedded Control Solutions Company, EtherSynch, FlashTec, Hyper Speed Control, HyperLight Load, IntelliMOS, Libero, motorBench, mTouch, Powermite 3, Precision Edge, ProASIC, ProASIC Plus, ProASIC Plus logo, Quiet-Wire, SmartFusion, SyncWorld, Temux, TimeCesium, TimeHub, TimePictra, TimeProvider, Vite, WinPath, and ZL are registered trademarks of Microchip Technology Incorporated in the U.S.A.

Adjacent Key Suppression, AKS, Analog-for-the-Digital Age, Any Capacitor, AnyIn, AnyOut, BlueSky, BodyCom, CodeGuard, CryptoAuthentication, CryptoAutomotive, CryptoCompanion, CryptoController, dsPICDEM, dsPICDEM.net, Dynamic Average Matching, DAM, ECAN, EtherGREEN, In-Circuit Serial Programming, ICSP, INICnet, Inter-Chip Connectivity, JitterBlocker, KleerNet, KleerNet logo, memBrain, Mindi, MiWi, MPASM, MPF, MPLAB Certified logo, MPLIB, MPLINK, MultiTRAK, NetDetach, Omniscient Code Generation, PICDEM, PICDEM.net, PICkit, PICtail, PowerSmart, PureSilicon, QMatrix, REAL ICE, Ripple Blocker, SAM-ICE, Serial Quad I/O, SMART-I.S., SQI, SuperSwitcher, SuperSwitcher II, Total Endurance, TSHARC, USBCheck, VariSense, ViewSpan, WiperLock, Wireless DNA, and ZENA are trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

SQTP is a service mark of Microchip Technology Incorporated in the U.S.A.

The Adaptec logo, Frequency on Demand, Silicon Storage Technology, and Symmcom are registered trademarks of Microchip Technology Inc. in other countries.

GestIC is a registered trademark of Microchip Technology Germany II GmbH & Co. KG, a subsidiary of Microchip Technology Inc., in other countries.

All other trademarks mentioned herein are property of their respective companies.

© 2019, Microchip Technology Incorporated, Printed in the U.S.A., All Rights Reserved.

ISBN: 978-1-5224-4968-3

Quality Management System

For information regarding Microchip's Quality Management Systems, please visit <http://www.microchip.com/quality>.

Worldwide Sales and Service

AMERICAS	ASIA/PACIFIC	ASIA/PACIFIC	EUROPE
<p>Corporate Office 2355 West Chandler Blvd. Chandler, AZ 85224-6199 Tel: 480-792-7200 Tel: 480-792-7277 Technical Support: http://www.microchip.com/support Web Address: http://www.microchip.com</p> <p>Atlanta Duluth, GA Tel: 678-957-9614 Fax: 678-957-1455</p> <p>Austin, TX Tel: 512-257-3370</p> <p>Boston Westborough, MA Tel: 774-760-0087 Fax: 774-760-0088</p> <p>Chicago Itasca, IL Tel: 630-285-0071 Fax: 630-285-0075</p> <p>Dallas Addison, TX Tel: 972-818-7423 Fax: 972-818-2924</p> <p>Detroit Novi, MI Tel: 248-848-4000</p> <p>Houston, TX Tel: 281-894-5983</p> <p>Indianapolis Noblesville, IN Tel: 317-773-8323 Fax: 317-773-5453 Tel: 317-536-2380</p> <p>Los Angeles Mission Viejo, CA Tel: 949-462-9523 Fax: 949-462-9608 Tel: 951-273-7800</p> <p>Raleigh, NC Tel: 919-844-7510</p> <p>New York, NY Tel: 631-435-6000</p> <p>San Jose, CA Tel: 408-735-9110 Tel: 408-436-4270</p> <p>Canada - Toronto Tel: 905-695-1980 Fax: 905-695-2078</p>	<p>Australia - Sydney Tel: 61-2-9868-6733</p> <p>China - Beijing Tel: 86-10-8569-7000</p> <p>China - Chengdu Tel: 86-28-8665-5511</p> <p>China - Chongqing Tel: 86-23-8980-9588</p> <p>China - Dongguan Tel: 86-769-8702-9880</p> <p>China - Guangzhou Tel: 86-20-8755-8029</p> <p>China - Hangzhou Tel: 86-571-8792-8115</p> <p>China - Hong Kong SAR Tel: 852-2943-5100</p> <p>China - Nanjing Tel: 86-25-8473-2460</p> <p>China - Qingdao Tel: 86-532-8502-7355</p> <p>China - Shanghai Tel: 86-21-3326-8000</p> <p>China - Shenyang Tel: 86-24-2334-2829</p> <p>China - Shenzhen Tel: 86-755-8864-2200</p> <p>China - Suzhou Tel: 86-186-6233-1526</p> <p>China - Wuhan Tel: 86-27-5980-5300</p> <p>China - Xian Tel: 86-29-8833-7252</p> <p>China - Xiamen Tel: 86-592-2388138</p> <p>China - Zhuhai Tel: 86-756-3210040</p>	<p>India - Bangalore Tel: 91-80-3090-4444</p> <p>India - New Delhi Tel: 91-11-4160-8631</p> <p>India - Pune Tel: 91-20-4121-0141</p> <p>Japan - Osaka Tel: 81-6-6152-7160</p> <p>Japan - Tokyo Tel: 81-3-6880-3770</p> <p>Korea - Daegu Tel: 82-53-744-4301</p> <p>Korea - Seoul Tel: 82-2-554-7200</p> <p>Malaysia - Kuala Lumpur Tel: 60-3-7651-7906</p> <p>Malaysia - Penang Tel: 60-4-227-8870</p> <p>Philippines - Manila Tel: 63-2-634-9065</p> <p>Singapore Tel: 65-6334-8870</p> <p>Taiwan - Hsin Chu Tel: 886-3-577-8366</p> <p>Taiwan - Kaohsiung Tel: 886-7-213-7830</p> <p>Taiwan - Taipei Tel: 886-2-2508-8600</p> <p>Thailand - Bangkok Tel: 66-2-694-1351</p> <p>Vietnam - Ho Chi Minh Tel: 84-28-5448-2100</p>	<p>Austria - Wels Tel: 43-7242-2244-39 Fax: 43-7242-2244-393</p> <p>Denmark - Copenhagen Tel: 45-4450-2828 Fax: 45-4485-2829</p> <p>Finland - Espoo Tel: 358-9-4520-820</p> <p>France - Paris Tel: 33-1-69-53-63-20 Fax: 33-1-69-30-90-79</p> <p>Germany - Garching Tel: 49-8931-9700</p> <p>Germany - Haan Tel: 49-2129-3766400</p> <p>Germany - Heilbronn Tel: 49-7131-72400</p> <p>Germany - Karlsruhe Tel: 49-721-625370</p> <p>Germany - Munich Tel: 49-89-627-144-0 Fax: 49-89-627-144-44</p> <p>Germany - Rosenheim Tel: 49-8031-354-560</p> <p>Israel - Ra'anana Tel: 972-9-744-7705</p> <p>Italy - Milan Tel: 39-0331-742611 Fax: 39-0331-466781</p> <p>Italy - Padova Tel: 39-049-7625286</p> <p>Netherlands - Drunen Tel: 31-416-690399 Fax: 31-416-690340</p> <p>Norway - Trondheim Tel: 47-72884388</p> <p>Poland - Warsaw Tel: 48-22-3325737</p> <p>Romania - Bucharest Tel: 40-21-407-87-50</p> <p>Spain - Madrid Tel: 34-91-708-08-90 Fax: 34-91-708-08-91</p> <p>Sweden - Gothenberg Tel: 46-31-704-60-40</p> <p>Sweden - Stockholm Tel: 46-8-5090-4654</p> <p>UK - Wokingham Tel: 44-118-921-5800 Fax: 44-118-921-5820</p>