



Product Change Notification - LIAL-05RINN651

Date:

16 Sep 2019

Product Category:

8-bit Microcontrollers

Affected CPNs:**Notification subject:**

CCB 3836 Initial Notice: Qualification of MPHL as a new final test site for selected Atmel ATTINY16xx device families available in 20L SOIC (.300in) and 14L SOIC (.150in) packages.

Notification text:**PCN Status:**

Initial notification.

PCN Type:

Manufacturing Change

Microchip Parts Affected:

Please open one of the icons found in the Affected CPNs section above.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change:

Qualification of MPHL as a new final test site for selected Atmel ATTINY16xx device families available in 20L SOIC (.300in) and 14L SOIC (.150in) packages.

Pre Change:

Tested at ASCL final test site.

Post Change:

Tested at MPHL final test site

Pre and Post Change Summary

		Pre Change	Post Change
Final Test Site		ASE Group Chung-Li (ASCL)	Microchip Technology Operations (Philippines) Corporation (MPHL)
Base Quantity Multiple (BQM)	14L	3000	3000
	20L	1500	1500
Pin Orientation	14L	Quadrant 1	Quadrant 1
	20L	Quadrant 1	Quadrant 1
Packing Media		Tape and Reel	Tape and Reel



Carrier Tape Dimension	Minor Dimensional Changes. See attached Pre and Post comparison
Cover Tape Dimension	Minor Dimensional Changes. See attached Pre and Post comparison
Plastic Reel	Minor Dimensional Changes. See attached Pre and Post comparison
Desiccant	See attached Pre and Post comparison
Moisture Barrier Bag	Minor Dimensional Changes. See attached Pre and Post comparison
Carton Box	Minor Dimensional Changes. See attached Pre and Post comparison

Impacts to Data Sheet:

None.

Change Impact:

None.

Reason for Change:

To improve on-time delivery performance by qualifying MPHL as a new final test site.

Change Implementation Status:

In Progress

Estimated Qualification Completion Date:

October 2019

Note: Please be advised the qualification completion times may be extended because of unforeseen business conditions however implementation will not occur until after qualification has completed and a final PCN has been issued. The final PCN will include the qualification report and estimated first ship date. Also note that after the estimated first ship date guided in the final PCN customers may receive pre and post change parts.

Time Table Summary:

	September 2019					October 2019			
Workweek	36	37	38	39	40	41	42	43	44
Initial PCN Issue Date			X						
Qual Report Availability							X		
Final PCN Issue Date							X		

Method to Identify Change:

Traceability code

Qualification Plan:

Please open the attachments included with this PCN labeled as PCN_#_Qual Plan.

Revision History:



September 16, 2019: Issued initial notification.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s):

[PCN_LIAL-05RINN651_QUAL PLAN.pdf](#)

[PCN_LIAL-05RINN651_PRE AND POST CHANGE SUMMARY.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

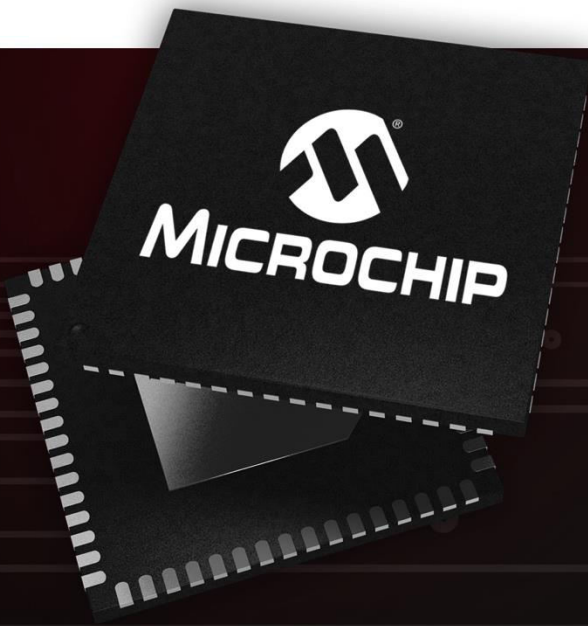
Affected Catalog Part Numbers (CPN)

ATTINY1614-SSFR

ATTINY1614-SSNR

ATTINY1616-SFR

ATTINY1616-SNR



A Leading Provider of Microcontroller, Security,
Mixed-Signal, Analog & Flash-IP Solutions



PCN#: LIAL-05RINN65
CCB 3836 Pre and Post Change Summary
20L and 14L SOIC Package





Carrier tape Dimension

Material	Package	PRE CHANGE					POST CHANGE				
		ASCL					MPHL				
		Width (mm)	Pitch (mm)	A0 (mm)	B0 (mm)	K0 (mm)	Width (mm)	Pitch (mm)	A0 (mm)	B0 (mm)	K0 (mm)
Carrier Tape	14L SOIC	16.4 ±0.3 mm	8.0 ±0.1 mm	8.0 ±0.1 mm	9.00 ±0.1 mm	2.30 ±0.1 mm	16 ±0.3 mm	8.0 ±0.1 mm	6.50 ±0.1 mm	9.50 ±0.1 mm	2.10 ±0.1 mm
	20L SOIC	24 +0.3/-0.0	12.0 ±0.1 mm	10.90 ±0.1 mm	13.30 ±0.1 mm	3.20 ±0.1 mm	24 ±0.3 mm	12.0 ±0.1 mm	10.90 ±0.1 mm	13.30 ±0.1 mm	3.00 ±0.1 mm

Cover tape Dimension





Material	Package	PRE CHANGE			POST CHANGE		
		ASCL			MPHL		
Cover Tape		Width	Thickness	Method	Width	Thickness	Method
	14L SOIC	13.1, 13.3, 13.5 \pm 0.1 mm	0.050mm	Heat Activated Adhesive	13.3 \pm 0.1 mm	0.05 \pm 0.005 mm	Pressure
	20L SOIC	21.0, 21.3 \pm 0.1 mm	0.05 \pm 0.005 mm	Heat Activated Adhesive	21.3 \pm 0.1 mm	0.05 \pm 0.005 mm	Pressure

Reel





Package	PRE CHANGE					POST CHANGE				
	ASCL					MPHL				
	Color	Diameter (mm)	Hub Diameter (mm)	W1 (mm)	W2(mm)	Color	Diameter (mm)	Hub Diameter (mm)	W1 (mm)	W2(mm)
	White	330 ± 3.0 mm	100.0mm	16.4 ±2 mm	22.4 mm	White	330 ±4 mm	101.5 ±1.5mm	16.4 ±2 mm	22.4 mm
14L SOIC										
20L SOIC	Color	Diameter (mm)	Hub Diameter (mm)	W1 (mm)	W2(mm)	Color	Diameter (mm)	Hub Diameter (mm)	W1 (mm)	W2(mm)
	White	330 ± 3.0 mm	100 ± 2.0 mm	24.4 +2/-0 mm	29.4 ± 1.0 mm	White	330 +0/-4 mm	101.5 ±1.5mm	24.4 +2/-0 mm	30.4 mm
										

Desiccant





Package	PRE CHANGE	POST CHANGE
	ASCL	MPHL
14L SOIC		
20L SOIC		

Package	PRE CHANGE	POST CHANGE
	ASCL	MPHL
14L SOIC		
20L SOIC		

Moisture Barrier Bag

Package	PRE CHANGE		POST CHANGE	
	ASCL		MPHL	
	Length (mm)	Width (mm)	Length (mm)	Width (mm)
	455mm	425mm	457.2 mm	406.4 mm
14L SOIC				
20L SOIC	Length (mm)	Width (mm)	Length (mm)	Width (mm)
	455mm	425mm	457.2 mm	406.4 mm
				

Carton Box

Package	PRE CHANGE			POST CHANGE		
	ASCL			ASCL		
	Length (mm)	Width (mm)	Height	Length (mm)	Width (mm)	Height
	352 mm	351 mm	53 mm	354mm	352 mm	53 mm
14L SOIC						
20L SOIC	Length (mm)	Width (mm)	Height	Length (mm)	Width (mm)	Height
	352 mm	351 mm	53 mm	354mm	352 mm	53 mm
						



MICROCHIP

QUALIFICATION PLAN SUMMARY

PCN #: LIAL-05RINN651

Date
May 15, 2019

**Qualification of MPHL as a new final test site for selected
Atmel ATTINY16xx device families available in 20L SOIC
(.300in) and 14L SOIC (.150in) packages.**

Purpose: Qualification of MPHL as a new final test site for selected Atmel ATTINY16xx device families available in 20L SOIC (.300in) and 14L SOIC (.150in) packages.

CCB No.: 3836

Test / Evaluation	Test Conditions / Parameters
Datalog / Bin Comparison	<ul style="list-style-type: none">• 3,000-unit yield sample for bin comparison
Yield correlation	<ul style="list-style-type: none">• Lot Validation, Good vs. rejects comparison.• 3,000 units matching to 0.1% yield
Parametric Correlation	<ul style="list-style-type: none">• 33 units of parametric data comparison