

Product Change Notification - LIAL-03HHRI927

Data	•	
Date	=	

30 Sep 2019

Product Category:

8-bit Microcontrollers

Affected CPNs:



Notification subject:

CCB 3873 Final Notice: Implement top marking changes for selected ATMEGA480x, ATMEGA320x, ATMEGA160x, ATMEGA80x Atmel device families manufactured with 59.91k process technology.

Notification text:

PCN Status:

Final notification.

PCN Type:

Manufacturing Change

Microchip Parts Affected:

Please open one of the icons found in the Affected CPNs section above.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change:

Implement top marking changes for selected ATMEGA480x, ATMEGA320x, ATMEGA160x, ATMEGA80x Atmel device families manufactured with 59.91k process technology.

Pre Change:

With package code, temperature grade, packing media and die rev in the top marking

Post Change:

Without package code, temperature grade, packing media and die rev in the top marking

Pre and Post Change Summary:

	9	<i>)</i> ·			
Change item		Pre Change	Post Change		
Top mark		See attached pre an	d post marking change		

Impacts to Data Sheet:

None

Change Impact:

None

Reason for Change:

To improve manufacturability and on time delivery by standardizing top marking format for ATMEGA480x, ATMEGA320x, ATMEGA160x, ATMEGA80x device families.

Change Implementation Status:



In Progress

Estimated First Ship Date:

October 30, 2019 (date code: 1944)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Time Table Summary:

	September 2019					October 2019			
Workweek	36	37	38	39 40		41	42	43	44
Final PCN Issue Date					X				
Estimated Implementation									V
Date									^

Method to Identify Change:

Traceability code, top marking

Qualification Report

Not applicable

Revision History:

September 30, 2019: Issued final notification. Provided estimated first ship date to be October 30, 2019

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s):

PCN LIAL-03HHRI927 Top marking Pre and Post Change.pdf

Please contact your local <u>Microchip sales office</u> with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to <u>receive Microchip PCNs via email</u> please register for our PCN email service at our <u>PCN home page</u> select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the <u>PCN FAQ</u> section.

If you wish to <u>change your PCN profile</u>, <u>including opt out</u>, please go to the <u>PCN home page</u> select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

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Affected Catalog Part Numbers (CPN)

ATMEGA1608-AF

ATMEGA1608-AFR

ATMEGA1608-MF

ATMEGA1608-MFR

ATMEGA1608-XF

ATMEGA1608-XFR

ATMEGA1609-AF

ATMEGA1609-AFR

ATMEGA3208-AF

ATMEGA3208-AFR

ATMEGA3208-MF

ATMEGA3208-MFR

ATMEGA3208-XF

ATMEGA3208-XFR

ATMEGA3209-AF

ATMEGA3209-AFR

ATMEGA3209-MF

ATMEGA3209-MFR

ATMEGA4808-AF

ATMEGA4808-AFR

ATMEGA4808-MF

ATMEGA4808-MFR

ATMEGA4808-XF

ATMEGA4808-XFR

ATMEGA4809-AF

ATMEGA4809-AFR

ATMEGA4809-MF

ATMEGA4809-MFR

ATMEGA4809-PF

ATMEGA808-AF

ATMEGA808-AFR

ATMEGA808-MF

ATMEGA808-MFR

ATMEGA808-XF

ATMEGA808-XFR

ATMEGA809-AF

ATMEGA809-AFR



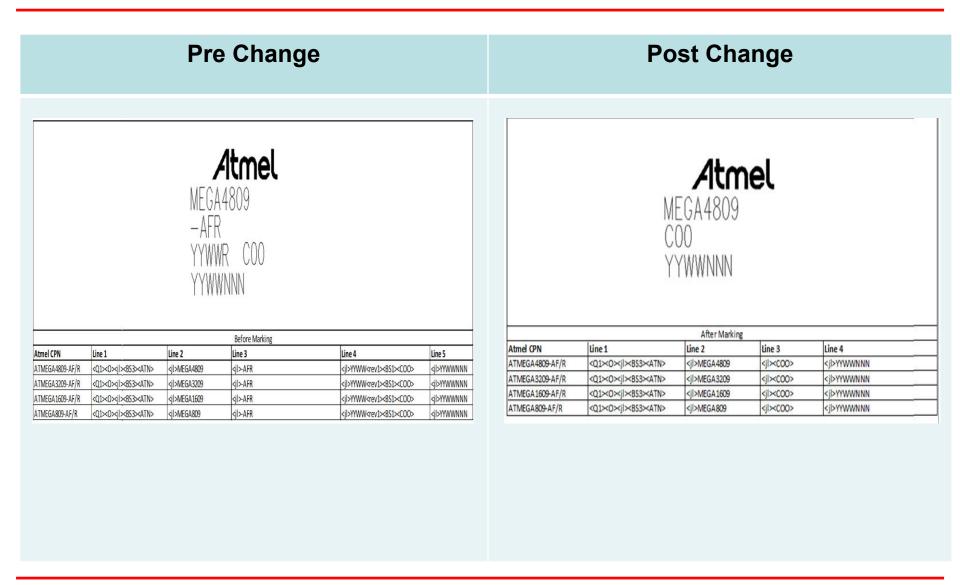
PCN#: LIAL-03HHRI927

CCB#: 3873

Implement top marking changes for selected ATMEGA480x, ATMEGA320x, ATMEGA160x, ATMEGA80x Atmel device families manufactured with 59.91k process technology.

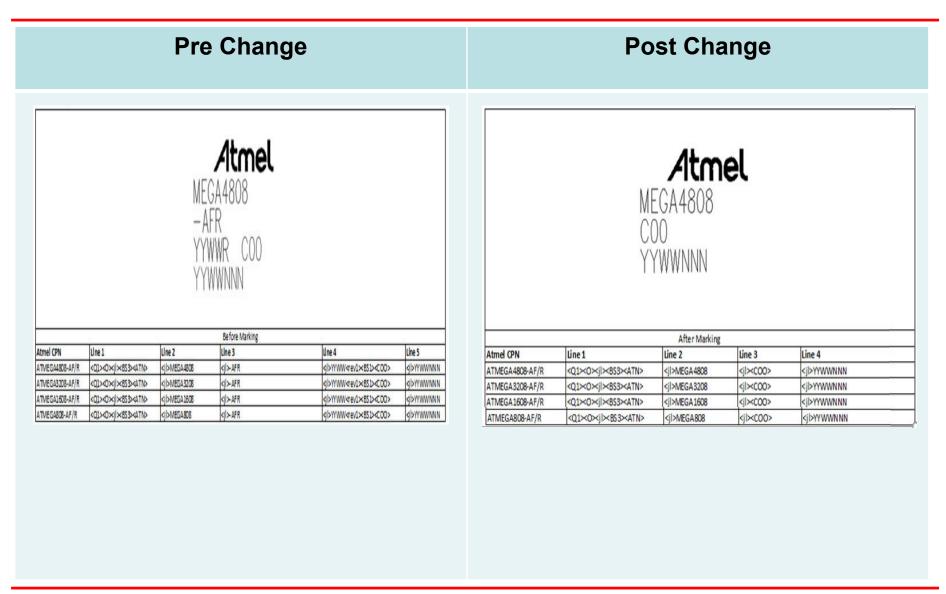


Top Marking Pre and Post Change 48L TQFP



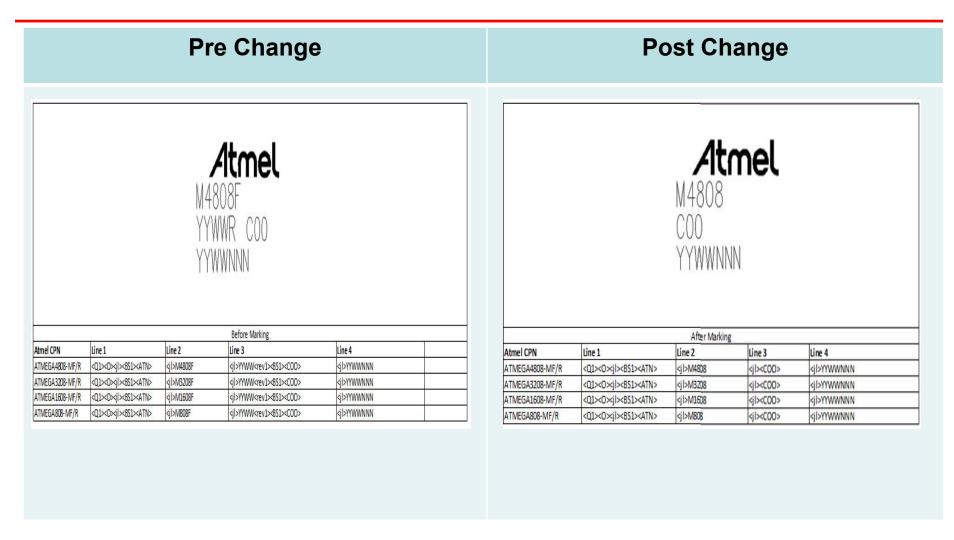


Top Marking Pre and Post Change 32L TQFP



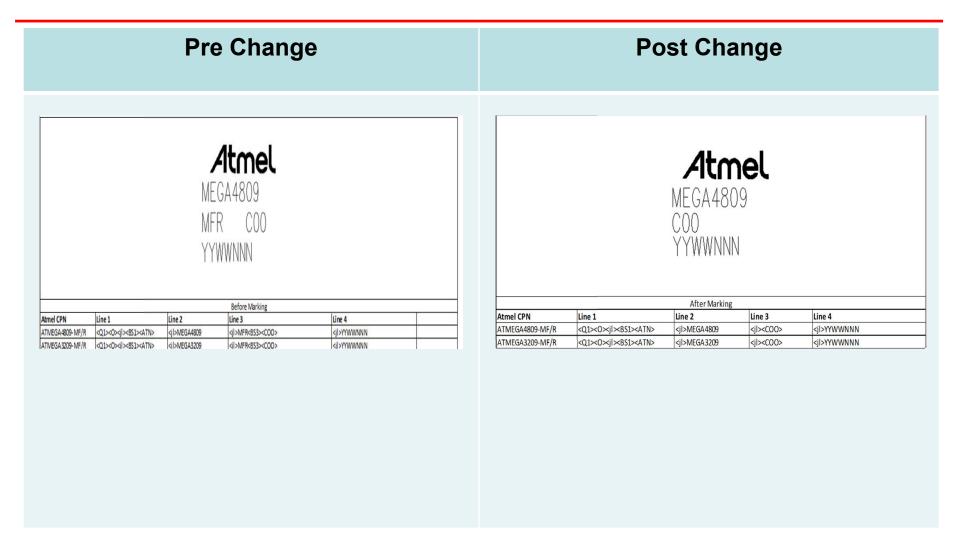


Top Marking Pre and Post Change 32L VFQN





Top Marking Pre and Post Change 48L UQFN





Top marking Pre and Post Change 28L SSOP

Pre Change						P	ost Ch	ange		
	Atmel MEGA4808-XFR YYWWR COO YYWWNNN				Atmel MEGA4808 COO YYWWNNN					
	-		Before Marking		After Marking					
Atmel CPN	Line 1	Line 2	Line 3	Line 4	Atmel CPN	Line 1	Line 2	Line 3	Line 4	
ATMEGA4808-XF/R	<jl><bs2><atn></atn></bs2></jl>	<jl>MEGA4808-XFR</jl>	<jl><bs2>YYWW<rev1><bs1>C00></bs1></rev1></bs2></jl>	<q3><o><bs2><jl>YYWWNNN</jl></bs2></o></q3>	ATMEGA4808-XF/R	<jl><bs2><atn></atn></bs2></jl>	<jl>MEGA4808</jl>	<jl><000></jl>	<q3><0><bs2><jl>YYWWNNN</jl></bs2></q3>	
ATMEGA3208-XF/R	<jl><bs2><atn></atn></bs2></jl>	<jl>MEGA3208-XFR</jl>	<jl><bs2>YYWW<rev1>BS1>COO></rev1></bs2></jl>	<q3×o×bs2×jl>YYWWNNN</q3×o×bs2×jl>	ATMEGA3208-XF/R	<jl><bs2><atn></atn></bs2></jl>	<jl>MEGA3208</jl>	<jl><000></jl>	<q3><o><bs2><jl>YYWWNNN</jl></bs2></o></q3>	
ATMEGA1608-XF/R	<jl><bs2>ATN></bs2></jl>	<jl>MEGA1608-XFR</jl>	<jl><pl><pl><pl><pre><pre><pre><pre><pre><pre><pre><pre< td=""><td><q3×o×bs2×jl>YYWWNNN</q3×o×bs2×jl></td><td>ATMEGA1608-XF/R</td><td><jl>BS2><atn></atn></jl></td><td><jl>MEGA1608</jl></td><td><jl><000></jl></td><td><q3><o><bs2><jl>YYWWNNN</jl></bs2></o></q3></td></pre<></pre></pre></pre></pre></pre></pre></pre></pl></pl></pl></jl>	<q3×o×bs2×jl>YYWWNNN</q3×o×bs2×jl>	ATMEGA1608-XF/R	<jl>BS2><atn></atn></jl>	<jl>MEGA1608</jl>	<jl><000></jl>	<q3><o><bs2><jl>YYWWNNN</jl></bs2></o></q3>	
ATMEGA808-XF/R	<jl><bs2><atn></atn></bs2></jl>	<jl>MEGA808-XFR</jl>	<jl><852>YYWW<rev1>851>C00></rev1></jl>	<q3><0><bs2><jl>YYWWNNN</jl></bs2></q3>	ATMEGA808-XF/R	<jl>BS2><atn></atn></jl>	<jl>MEGA808</jl>	<jl><c00></c00></jl>	<q3><0><bs2><jl>YYWWNNN</jl></bs2></q3>	



Top Marking Pre and Post Change 40L PDIP

Pre Change					Post Change							
										,		
	ATMEGA4809-PF XXXXXXXX Atmel					ATMEGA4809 XXXXXXXX Atmel						
			n for Markov					A fe A A L t				
Atmel CPN	Line 1	Line 2	Before Marking Line 3	Line 4	_	After Marking Atmel CPN Line 1 Line 2 Line 3 Line 4						
ATMEGA4809-PF/R	<jc×bl></jc×bl>	<ic>ATMEGA4809-PF</ic>	<joyywwnnn< th=""><th><q3><o><jc><atn></atn></jc></o></q3></th><th></th><th>ATMEGA4809-PF/R</th><th><jc><bl></bl></jc></th><th><ic>ATMEGA4809</ic></th><th><jc>YYWWNNN</jc></th><th><q3><o><jc><atn></atn></jc></o></q3></th></joyywwnnn<>	<q3><o><jc><atn></atn></jc></o></q3>		ATMEGA4809-PF/R	<jc><bl></bl></jc>	<ic>ATMEGA4809</ic>	<jc>YYWWNNN</jc>	<q3><o><jc><atn></atn></jc></o></q3>		