



Product Change Notification - LIAL-03HHRI927

Date:

30 Sep 2019

Product Category:

8-bit Microcontrollers

Affected CPNs:**Notification subject:**

CCB 3873 Final Notice: Implement top marking changes for selected ATMEGA480x, ATMEGA320x, ATMEGA160x, ATMEGA80x Atmel device families manufactured with 59.91k process technology.

Notification text:**PCN Status:**

Final notification.

PCN Type:

Manufacturing Change

Microchip Parts Affected:

Please open one of the icons found in the Affected CPNs section above.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change:

Implement top marking changes for selected ATMEGA480x, ATMEGA320x, ATMEGA160x, ATMEGA80x Atmel device families manufactured with 59.91k process technology.

Pre Change:

With package code, temperature grade, packing media and die rev in the top marking

Post Change:

Without package code, temperature grade, packing media and die rev in the top marking

Pre and Post Change Summary:

Change item	Pre Change	Post Change
Top mark	See attached pre and post marking change	

Impacts to Data Sheet:

None

Change Impact:

None

Reason for Change:

To improve manufacturability and on time delivery by standardizing top marking format for ATMEGA480x, ATMEGA320x, ATMEGA160x, ATMEGA80x device families.

Change Implementation Status:



In Progress

Estimated First Ship Date:

October 30, 2019 (date code: 1944)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Time Table Summary:

	September 2019					October 2019			
Workweek	36	37	38	39	40	41	42	43	44
Final PCN Issue Date					X				
Estimated Implementation Date									X

Method to Identify Change:

Traceability code, top marking

Qualification Report

Not applicable

Revision History:

September 30, 2019: Issued final notification. Provided estimated first ship date to be October 30, 2019

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s):

[PCN_LIAL-03HHRI927_Top marking Pre and Post Change.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

ATMEGA1608-AF
ATMEGA1608-AFR
ATMEGA1608-MF
ATMEGA1608-MFR
ATMEGA1608-XF
ATMEGA1608-XFR
ATMEGA1609-AF
ATMEGA1609-AFR
ATMEGA3208-AF
ATMEGA3208-AFR
ATMEGA3208-MF
ATMEGA3208-MFR
ATMEGA3208-XF
ATMEGA3208-XFR
ATMEGA3209-AF
ATMEGA3209-AFR
ATMEGA3209-MF
ATMEGA3209-MFR
ATMEGA4808-AF
ATMEGA4808-AFR
ATMEGA4808-MF
ATMEGA4808-MFR
ATMEGA4808-XF
ATMEGA4808-XFR
ATMEGA4809-AF
ATMEGA4809-AFR
ATMEGA4809-MF
ATMEGA4809-MFR
ATMEGA4809-PF
ATMEGA808-AF
ATMEGA808-AFR
ATMEGA808-MF
ATMEGA808-MFR
ATMEGA808-XF
ATMEGA808-XFR
ATMEGA809-AF
ATMEGA809-AFR



PCN#: LIAL-03HHRI927

CCB#: 3873

**Implement top marking changes for selected
ATMEGA480x, ATMEGA320x, ATMEGA160x,
ATMEGA80x Atmel device families manufactured with
59.91k process technology.**

Top Marking Pre and Post Change 48L TQFP

Pre Change

Atmel
MEGA4809
-AFR
YYWWR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4	Line 5
ATMEGA4809-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA4809	<j>>AFR	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN
ATMEGA3209-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA3209	<j>>AFR	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN
ATMEGA1609-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA1609	<j>>AFR	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN
ATMEGA809-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA809	<j>>AFR	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN

Post Change

Atmel
MEGA4809
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA4809	<j>>COO	<j>>YYWWNNN
ATMEGA3209-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA3209	<j>>COO	<j>>YYWWNNN
ATMEGA1609-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA1609	<j>>COO	<j>>YYWWNNN
ATMEGA809-AF/R	<Q1><O><j><BS3><ATN>	<j>>MEGA809	<j>>COO	<j>>YYWWNNN

Top Marking Pre and Post Change 32L TQFP

Pre Change

Atmel
MEGA4808
-AFR
YYWWR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4	Line 5
ATMEGA4808-AF/R	<Q1><O><J><BS3><ATN>	<J>>MEGA4808	<J>>AFR	<J>>YYWW<rev1><BS3><COO>	<J>>YYWWNNN
ATMEGA3208-AF/R	<Q1><O><J><BS3><ATN>	<J>>MEGA3208	<J>>AFR	<J>>YYWW<rev1><BS3><COO>	<J>>YYWWNNN
ATMEGA1608-AF/R	<Q1><O><J><BS3><ATN>	<J>>MEGA1608	<J>>AFR	<J>>YYWW<rev1><BS3><COO>	<J>>YYWWNNN
ATMEGA808-AF/R	<Q1><O><J><BS3><ATN>	<J>>MEGA808	<J>>AFR	<J>>YYWW<rev1><BS3><COO>	<J>>YYWWNNN

Post Change

Atmel
MEGA4808
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-AF/R	<Q1><O><J><BS3><ATN>	<J>>MEGA4808	<J>>COO	<J>>YYWWNNN
ATMEGA3208-AF/R	<Q1><O><J><BS3><ATN>	<J>>MEGA3208	<J>>COO	<J>>YYWWNNN
ATMEGA1608-AF/R	<Q1><O><J><BS3><ATN>	<J>>MEGA1608	<J>>COO	<J>>YYWWNNN
ATMEGA808-AF/R	<Q1><O><J><BS3><ATN>	<J>>MEGA808	<J>>COO	<J>>YYWWNNN

Top Marking Pre and Post Change 32L VFQN

Pre Change

Atmel
M4808F
YYWWR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-MF/R	<Q1><D><j><BS1><ATN>	<j>>M4808F	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN
ATMEGA3208-MF/R	<Q1><D><j><BS1><ATN>	<j>>M3208F	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN
ATMEGA1608-MF/R	<Q1><D><j><BS1><ATN>	<j>>M1608F	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN
ATMEGA808-MF/R	<Q1><D><j><BS1><ATN>	<j>>M808F	<j>>YYWW<rev1><BS1><COO>	<j>>YYWWNNN

Post Change

Atmel
M4808
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-MF/R	<Q1><D><j><BS1><ATN>	<j>>M4808	<j>><COO>	<j>>YYWWNNN
ATMEGA3208-MF/R	<Q1><D><j><BS1><ATN>	<j>>M3208	<j>><COO>	<j>>YYWWNNN
ATMEGA1608-MF/R	<Q1><D><j><BS1><ATN>	<j>>M1608	<j>><COO>	<j>>YYWWNNN
ATMEGA808-MF/R	<Q1><D><j><BS1><ATN>	<j>>M808	<j>><COO>	<j>>YYWWNNN

Top Marking Pre and Post Change 48L UQFN

Pre Change

Atmel
MEGA4809
MFR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-MF/R	<Q1><O><j><BS1><ATN>	<j>MEGA4809	<j>MFR<BS3><COO>	<j>YYWWNNN
ATMEGA3209-MF/R	<Q1><O><j><BS1><ATN>	<j>MEGA3209	<j>MFR<BS3><COO>	<j>YYWWNNN

Post Change

Atmel
MEGA4809
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-MF/R	<Q1><O><j><BS1><ATN>	<j>MEGA4809	<j><COO>	<j>YYWWNNN
ATMEGA3209-MF/R	<Q1><O><j><BS1><ATN>	<j>MEGA3209	<j><COO>	<j>YYWWNNN

Top marking Pre and Post Change 28L SSOP

Pre Change

Atmel
MEGA4808-XFR
YYWWR COO
YYWWNNN

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-XF/R	<j><BS2><ATN>	<j>>MEGA4808-XFR	<j><BS2>YYWW<rev1><BS1><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA3208-XF/R	<j><BS2><ATN>	<j>>MEGA3208-XFR	<j><BS2>YYWW<rev1><BS1><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA1608-XF/R	<j><BS2><ATN>	<j>>MEGA1608-XFR	<j><BS2>YYWW<rev1><BS1><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA808-XF/R	<j><BS2><ATN>	<j>>MEGA808-XFR	<j><BS2>YYWW<rev1><BS1><COO>	<Q3><Q><BS2><j>YYWWNNN

Post Change

Atmel
MEGA4808
COO
YYWWNNN

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4808-XF/R	<j>><BS2><ATN>	<j>>MEGA4808	<j>><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA3208-XF/R	<j>><BS2><ATN>	<j>>MEGA3208	<j>><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA1608-XF/R	<j>><BS2><ATN>	<j>>MEGA1608	<j>><COO>	<Q3><Q><BS2><j>YYWWNNN
ATMEGA808-XF/R	<j>><BS2><ATN>	<j>>MEGA808	<j>><COO>	<Q3><Q><BS2><j>YYWWNNN

Top Marking Pre and Post Change 40L PDIP

Pre Change

ATMEGA4809-PF
XXXXXXX
Atmel

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-PF/R	<jc><bl>	<jc>ATMEGA4809-PF	<jc>YYWWNNN	<Q3><O><jc><ATN>

Post Change

ATMEGA4809
XXXXXXX
Atmel

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATMEGA4809-PF/R	<jc><bl>	<jc>ATMEGA4809	<jc>YYWWNNN	<Q3><O><jc><ATN>