

**Final Product Change Notification**

201910001F01

**Issue Date:** 06-Nov-2019  
**Effective Date:** 06-May-2020  
 Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from Nexperia. For detailed information we invite you to [view this notification online](#)



**Management Summary**

Transfer 74HC/T1G mini logic products into released and mature C350 process for automotive products

**Change Category**

<input checked="" type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input checked="" type="checkbox"/> Product Marking	<input type="checkbox"/> Test Location	<input checked="" type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input checked="" type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Process	<input type="checkbox"/> Errata
<input checked="" type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Equipment	<input type="checkbox"/> Electrical spec./Test coverage

Transfer 74HC/T1G mini logic products into released and mature C350 process

**Details of this Change**

Transfer 74HC/T1G mini logic products into released and mature C350 process for automotive products

- Next phase of 74HC/T transfer to C350 mini logic products transferred from C50
- Wafer fab. Location ASMC, Shanghai, China
- Process life cycle management action to redesign from non-preferred to preferred process
- Introduce enhanced LF with improved intrinsic quality
- Introduce enhanced LF to meet post stress AEC-Q006 delamination criteria

Qualification in accordance to the Automotive Electronics Council:

- AEC-Q100-rev. H Stress Test Qualification for Integrated Circuits
- AEC-Q006-rev. A Qualification requirements for Cu wire interconnections

**Why do we Implement this Change**

- Process life cycle management action to redesign from non-preferred to preferred process
- Package quality improvement

## Identification of Affected Products

- Applied wafer fab is indicated on the reel, box label

## Product Availability

### Sample Information

Samples are available upon request

Samples are available upon request from the Logic sample store Nijmegen The Netherlands

### Production

Planned first shipment 06-May-2020

## Impact

- No change in data sheet and test limits
- No change in form, fit, function, quality or reliability
- No change in product identification

### Data Sheet Revision

No impact to existing datasheet

### Disposition of Old Products

Products will continue to be shipped from ICN8 C50 process until depleted

## Related Notifications

Notification	Issue Date	Effective Date	Title
201711004F0120	Jan-2018	20-Apr-2018	Transfer 74HC/T1G mini logic products into released and mature C350 process

## Additional information

Affected products and sales history information: see attached file

Self qualification: [view online](#)

Additional documents: [view online](#)



## Timing and Logistics

Your acknowledgement of this change, conform JEDEC J-STD-046, is expected till 06-Dec-2019. Lack of acknowledgement of the PCN constitutes acceptance of the change.

## Remarks

Wafer Fab Indicators:

"Y"= ASMC, Shanghai, China

"T"= NXP ICN8, Nijmegen, Netherlands

## Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact Nexperia "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local Nexperia Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

**e-mail address** [PCN-Logic@nexperia.com](mailto:PCN-Logic@nexperia.com)

At Nexperia B.V. we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

## About Nexperia B.V.

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