# **Transistor**





### **Features**

- Collector-Emitter Sustaining Voltage Vceo (sus) = 400V (Minimum)
- Collector-Emitter Saturation Voltage Vce (sat) = 2V (Maximum) at Ic = 5A
- Reverse-Base SOA 300V to 400V at 7A

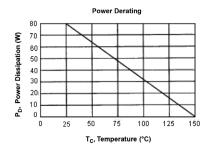
# **Maximum Ratings**

Characteristic	Symbol	Ratings	Unit
Collector - Emitter Voltage	Vceo	400	
Collector - Base Voltage	Vсво	400	V
Emitter - Base Voltage	VEBO	8	
Collector Current - Continuous - Peak	Iс Ісм	7 1	А
Base Current	lв	1.5	
Total Power Dissipation at Tc = 25°C Derate above 25°C	Po	80 0.64	W W/°C
Operating and Storage Junction Temperature Range	ТJ, Tsтg	-65 to +150	°C

# NPN TIP152 7 Amperes Darlington Power Transistor 300V to 400V 80W

### **Thermal Characteristics**

Characteristic	Symbol	Maximum	Unit
Thermal Resistance Junction to case	Rejc	1.56	°C / W



# **Electrical Characteristics (Tc = 25°C unless otherwise specified)**

Characteristic	Symbol	Minimum	Maximum	Unit
OFF Characteristics				
Collector - Emitter Breakdown Voltage (1) (Ic = 10mA, IB = 0)	V (BR) CEO	400	-	V
Collector - Base Breakdown Voltage (1) (Ic = 1mA, IB = 0)	V (BR) CBO	400		
Collector Cutoff Current (VCE = 400V, IB = 0)	ICEO	-	250	μΑ

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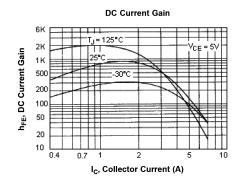


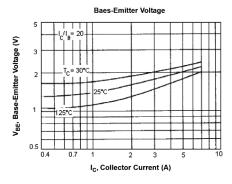
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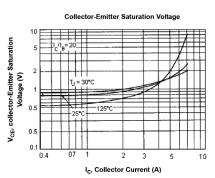


Chara	acteristic	Symbol	Minimum	Maximum	Unit
Emitter Cut off Curre (VEB = 8V, Ic = 0)	ent	Ієво	-	15	mA
ON Characteristics	(1)				
DC Current Gain (Ic= 2.5A, VcE = 5V) (Ic= 5A, VcE = 5V) (Ic= 7A, VcE = 5V)		hfe	150 50 15	-	-
Collector-Emitter Sa (Ic = 1A, IB = 10mA) (Ic = 2A, IB = 100mA (Ic = 5A, IB = 250mA	.)	VCE (sat)	-	1.5 1.5 2	
Base-Emitter Satura (Ic = 2A, I <sub>B</sub> = 100mA (Ic = 5A, I <sub>B</sub> = 250mA	.)	VBE (sat)	-	2.2 2.3	V
Diode Forward Volta (I <sub>F</sub> = 7A)	ge	VF	-	3.5	
Dynamic Character	ristics			•	
Small-Signal Current (Ic = 0.5A, VcE = 5V,		hfe	200	-	-
Output Capacitance (VcB = 10V, IE = 0, f	= 1MHz)	Cob	-	150	pF
Switching Characte	eristics			·	
Delay Time	Vcc = 250V. lc = 5A	ta	30 (Typical)	-	
Rise Time	I <sub>B1</sub> = -I <sub>B2</sub> = 250mA	tr	180 (Typical)	-	] ,,,
Storage Time	tp = 20µs,	ts	3.5 (Typical)	-	μs
Fall Time	Duty cycle ≤2%	tf	1.6 (Typical)	-	

<sup>1.</sup> Pulse Test : Pulse width = 30µs, Duty cycle = 2%





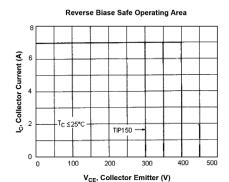


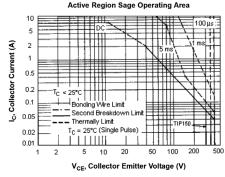
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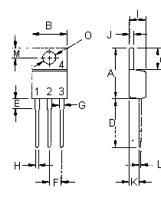
# multicomp PRO





There are two limitations on the power handling ability of a transistor average junction temperature and second breakdown safe operating area curves indicate Ic-Vce: limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than the curves indicate The data of figure - 6 curve is based on TJ (PK) = 150°C; TC is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% provided TJ (PK) <150°C. At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown

## Diagram



Minimum	Maximum
14.68	15.31
9.78	10.42
5.01	8.52
13.06	14.62
3.57	4.07
2.42	3.66
1.12	1.36
	14.68 9.78 5.01 13.06 3.57 2.42

Dimensions	Minimum	Maximum
Н	0.72	0.96
I	4.22	4.98
J	1.14	1.38
К	2.2	2.97
L	0.33	0.55
M	2.48	2.98
0	3.7	3.9

Dimensions : Millimetres

- Pin 1. Base
  - 2. Collector
  - 3. Emitter
  - 4. Collector (Case)

### **Part Number Table**

Description	Part Number
Darlington Transistor, TO-220	TIP152

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