Polar ${ }^{\text {TM }}$ Power MOSFET IXFB44N100P

HiPerFET ${ }^{\text {тм }}$
N-Channel Enhancement Mode
Avalanche Rated
Fast Intrinsic Diode


| Symbol | Test Conditions | Maximum Ratings |  |
| :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {DSs }}$ | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$ | 1000 | V |
| $\mathrm{V}_{\text {DGR }}$ | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}, \mathrm{R}_{\mathrm{GS}}=1 \mathrm{M} \Omega$ | 1000 | V |
| $\mathrm{V}_{\text {Gss }}$ | Continuous | $\pm 30$ | V |
| $\mathrm{V}_{\text {GSM }}$ | Transient | $\pm 40$ | V |
| $\mathrm{I}_{\mathrm{D} 25}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | 44 | A |
| $\mathrm{I}_{\mathrm{DM}}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$, pulse width limited by $\mathrm{T}_{\mathrm{JM}}$ | 110 | A |
| $\mathrm{I}_{\text {AR }}$ | $\mathrm{T}_{\mathrm{c}}=25^{\circ} \mathrm{C}$ | 22 | A |
| $\mathrm{E}_{\text {AS }}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | 2 | J |
| dV/dt | $\mathrm{I}_{S} \leq \mathrm{I}_{\mathrm{DM}}, \mathrm{V}_{\mathrm{DD}} \leq \mathrm{V}_{\mathrm{DSS}}, \mathrm{T}_{\mathrm{J}} \leq 150^{\circ} \mathrm{C}$ | 15 | V/ns |
| $\mathrm{P}_{\mathrm{D}}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | 1250 | W |
| $\mathrm{T}_{\mathrm{J}}$ |  | $-55 \ldots+150$ | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {JM }}$ |  | 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {stg }}$ |  | $-55 \ldots+150$ | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{L}}$ | 1.6 mm (0.062 in.) from case for 10 s | 300 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {SoLD }}$ | Plastic body for 10s | 260 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{F}_{\mathrm{c}}$ | Mounting torque | 30..120/6.7.. 27 | N/b. |
| Weight |  | 10 | g |




Note 1: Pulse test, $\mathrm{t} \leq 300 \mu \mathrm{~s}$; duty cycle, $\mathrm{d} \leq 2 \%$.

## PRELIMINARY TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from data gathered during objective characterizations of preliminary engineering lots; but also may yet contain some information supplied during a pre-production design evaluation. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

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| IXYS MOSFETs and IGBTs are covered | 4,835,592 | 4,931,844 | 5,049,961 | 5,237,481 | 6,162,665 | 6,404,065 B1 | 6,683,344 | 6,727,585 | 7,005,734 B2 | 7,157,338B2 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
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|  | 4,881,106 | 5,034,796 | 5,187,117 | 5,486,715 | 6,306,728 B1 | 6,583,505 | 6,710,463 | 6,771,478 B2 | 7,071,537 |  |

Fig. 1. Output Characteristics
@ $\mathbf{2 5}^{\circ} \mathrm{C}$


Fig. 3. Output Characteristics
@ $125^{\circ} \mathrm{C}$


Fig. 5. $\mathrm{R}_{\mathrm{DS}(\mathrm{on})}$ Normalized to $\mathrm{I}_{\mathrm{D}}=22 \mathrm{~A}$ Value vs. Drain Current


Fig. 2. Extended Output Characteristics @ $\mathbf{2 5}^{\circ} \mathrm{C}$


Fig. 4. $\mathrm{R}_{\mathrm{DS}(\mathrm{on})}$ Normalized to $\mathrm{I}_{\mathrm{D}}=22 \mathrm{~A}$ Value vs. Junction Temperature


Fig. 6. Maximum Drain Current vs. Case Temperature


Fig. 7. Input Admittance


Fig. 9. Forward Voltage Drop of Intrinsic Diode


Fig. 11. Capacitance


Fig. 8. Transconductance


Fig. 10. Gate Charge


Fig. 12. Maximum Transient Thermal Impedance


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