



Product Change Notification - LIAL-16ERHB724

Date:

24 Jan 2020

Product Category:

8-bit Microcontrollers

Affected CPNs:**Notification subject:**

CCB 4105 Final Notice: Implement top marking changes for selected ATTINY817x, ATTINY816x, ATTINY814x, ATTINY417x, ATTINY416x Atmel device families manufactured with 59.91k technology

Notification text:**PCN Status:**

Final notification.

PCN Type:

Manufacturing Change

Microchip Parts Affected:

Please open one of the icons found in the Affected CPNs section above.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls).

Description of Change:

Implement top marking changes for selected ATTINY817x, ATTINY816x, ATTINY814x, ATTINY417x, ATTINY416x Atmel device families manufactured with 59.91k technology.

Pre Change:

With temperature grade and die rev in the top marking

Post Change:

Without temperature grade and die rev in the top marking

Pre and Post Change Summary:

	Pre Change	Post Change
Top marking	See attached pre and post marking comparison	

Impacts to Data Sheet:

Yes

Change Impact:

None

Reason for Change:

To improve manufacturability and on time delivery by standardizing top marking format for ATTINY817x, ATTINY816x, ATTINY814x, ATTINY417x, ATTINY416x device families.



Change Implementation Status:

In Progress

Estimated First Ship Date

February 24, 2020 (date code: 2009)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Time Table Summary:

	January 2020					February 2020				
Workweek	01	02	03	04	05	06	07	08	09	10
Qual Report Availability				X						
Final PCN Issue Date				X						
Estimated Implementation Date									X	

Method to Identify Change:

Traceability code, top marking

Qualification Report:

Not applicable

Revision History:

January 24, 2020: Issued final notification. Provided estimated first ship date to be February 24, 2020.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s):

[PCN_LIAL-16ERHB724_Top Marking pre nd post change.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

ATTINY814-SSF
ATTINY816-SF
ATTINY816-MF
ATTINY417-MF
ATTINY817-MF
ATTINY814-SSN
ATTINY816-SN
ATTINY816-MN
ATTINY417-MN
ATTINY817-MN
ATTINY817-MB-V05
ATTINY817-MB-VAO
ATTINY416-MBT-V01
ATTINY816-MBT-V02
ATTINY416-MBT-VAO
ATTINY816-MBT-VAO
ATTINY814-SSNR
ATTINY816-SNR
ATTINY816-MNR
ATTINY817-MNR
ATTINY417-MNR
ATTINY816-MBT-V06
ATTINY816-MBT-V07
ATTINY817-MBT-V04
ATTINY417-MBT-VAO
ATTINY817-MBT-VAO
ATTINY416-MZT-VAO
ATTINY816-MZT-VAO
ATTINY814-SSFR
ATTINY816-SFR
ATTINY816-MFR
ATTINY817-MFR
ATTINY417-MFR
ATTINY416-MZT-V03
ATTINY417-MZT-VAO
ATTINY817-MZT-VAO







MICROCHIP

PCN#: LIAL-16ERHB724

CCB#: 4105

**Implement top marking changes for selected
ATTINY817x, ATTINY816x, ATTINY814x,
ATTINY417x, ATTINY416x Atmel device families
manufactured with 59.91k technology**

Change Summary

Pkg		Pre Change		Post Change
24L VQFN	1	<Q1><O><jl><ATN><jl>		<Q1><O><jl><ATN>
	2	<jl>AT817F		<jl>AT817
	3	<jl>YWW<rev1><BS1><COO>		<jl>YWW<BS1><COO>
	4	<jl>YYWWNNN		<jl>YYWWNNN
14L SOIC	1	<jl><ATN><BS1>YWW<rev1><BS1><COO>		<jl><ATN><BS1>YWW<BS1><COO>
	2	<jl>TINY814-F		<jl>TINY814
	3	<Q3><O><jl>YYWWNNN		<Q3><O><jl>YYWWNNN
20L SOIC	1	<jl><BS1><ATN>		<jl><BS1><ATN>
	2	<jl>TINY816-F		<jl>TINY816
	3	<jl>YYWW<rev1><BS2><COO>		<jl>YYWW<BS2><COO>
	4	<Q3><O><jl>YYWWNNN		<Q3><O><jl>YYWWNNN
20L VQFN	1	<Q1><O><jl>AT416		<Q1><O><jl>AT416
	2	<jl>-B<BS1><COO>		<jl><COO>
	3	<jl>WWNNN		<jl>WWNNN

Top marking pre and post change 20L VQFN

Pre Change

AT816
-N CC
YWWXX

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATTINY416-MBT-V01	<Q1><O><j>AT416	<j>-B<BS1><COO>	<j>WWNNN	
ATTINY416-MBT-VAO	<Q1><O><j>AT416	<j>-B<BS1><COO>	<j>WWNNN	
ATTINY416-MZT-V03	<Q1><O><j>AT416	<j>-Z<BS1><COO>	<j>WWNNN	
ATTINY416-MZT-VAO	<Q1><O><j>AT416	<j>-Z<BS1><COO>	<j>WWNNN	
ATTINY816-MBT-V02	<Q1><O><j>AT816	<j>-B<BS1><COO>	<j>WWNNN	
ATTINY816-MBT-V06	<Q1><O><j>AT816	<j>-B<BS1><COO>	<j>WWNNN	
ATTINY816-MBT-V07	<Q1><O><j>AT816	<j>-B<BS1><COO>	<j>WWNNN	
ATTINY816-MBT-VAO	<Q1><O><j>AT816	<j>-B<BS1><COO>	<j>WWNNN	
ATTINY816-MF	<Q1><O><j>AT816	<j>-F<BS1><COO>	<j>WWNNN	
ATTINY816-MFR	<Q1><O><j>AT816	<j>-F<BS1><COO>	<j>WWNNN	
ATTINY816-MN	<Q1><O><j>AT816	<j>-N<BS1><COO>	<j>WWNNN	
ATTINY816-MNR	<Q1><O><j>AT816	<j>-N<BS1><COO>	<j>WWNNN	
ATTINY816-MZT-VAO	<Q1><O><j>AT816	<j>-Z<BS1><COO>	<j>WWNNN	

Post Change

AT816
CC
YWWXX

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATTINY416-MBT-V01	<Q1><O><j>AT416	<j><COO>	<j>WWNNN	
ATTINY416-MBT-VAO	<Q1><O><j>AT416	<j><COO>	<j>WWNNN	
ATTINY416-MZT-V03	<Q1><O><j>AT416	<j><COO>	<j>WWNNN	
ATTINY416-MZT-VAO	<Q1><O><j>AT416	<j><COO>	<j>WWNNN	
ATTINY816-MBT-V02	<Q1><O><j>AT816	<j><COO>	<j>WWNNN	
ATTINY816-MBT-V06	<Q1><O><j>AT816	<j><COO>	<j>WWNNN	
ATTINY816-MBT-V07	<Q1><O><j>AT816	<j><COO>	<j>WWNNN	
ATTINY816-MBT-VAO	<Q1><O><j>AT816	<j><COO>	<j>WWNNN	
ATTINY816-MF	<Q1><O><j>AT816	<j><COO>	<j>WWNNN	
ATTINY816-MFR	<Q1><O><j>AT816	<j><COO>	<j>WWNNN	
ATTINY816-MN	<Q1><O><j>AT816	<j><COO>	<j>WWNNN	
ATTINY816-MNR	<Q1><O><j>AT816	<j><COO>	<j>WWNNN	
ATTINY816-MZT-VAO	<Q1><O><j>AT816	<j><COO>	<j>WWNNN	



Top marking pre and post change 20L SOIC

Pre Change

Atmel
TINY816-N
YYWR CC
XXXXXX

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATTINY816-SF	<j><BS1><ATN>	<j>TINY816-F	<j>YYWW<rev1><BS2><COO>	<Q3><O><j>YYWWNNN
ATTINY816-SFR	<j><BS1><ATN>	<j>TINY816-F	<j>YYWW<rev1><BS2><COO>	<Q3><O><j>YYWWNNN
ATTINY816-SN	<j><BS1><ATN>	<j>TINY816-N	<j>YYWW<rev1><BS2><COO>	<Q3><O><j>YYWWNNN
ATTINY816-SNR	<j><BS1><ATN>	<j>TINY816-N	<j>YYWW<rev1><BS2><COO>	<Q3><O><j>YYWWNNN
ATTINY816-SZT-VAO	<j><BS1><ATN>	<j>TINY816-Z	<j>YYWW<rev1><BS2><COO>	<Q3><O><j>YYWWNNN

Post Change

Atmel
TINY816
YYWW CC
XXXXXX

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATTINY816-SF	<j><BS1><ATN>	<j>TINY816	<j>YYWW<BS2><COO>	<Q3><O><j>YYWWNNN
ATTINY816-SFR	<j><BS1><ATN>	<j>TINY816	<j>YYWW<BS2><COO>	<Q3><O><j>YYWWNNN
ATTINY816-SN	<j><BS1><ATN>	<j>TINY816	<j>YYWW<BS2><COO>	<Q3><O><j>YYWWNNN
ATTINY816-SNR	<j><BS1><ATN>	<j>TINY816	<j>YYWW<BS2><COO>	<Q3><O><j>YYWWNNN
ATTINY816-SZT-VAO	<j><BS1><ATN>	<j>TINY816	<j>YYWW<BS2><COO>	<Q3><O><j>YYWWNNN



Top marking pre and post change 14 SOIC

Pre Change

Atmel YWWR CC
TINY814-N
XXXXXX

Before Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATTINY814-SSF	<j><CATN><BS1>YWW<rev1><BS1><COO>	<j>TINY814-F	<Q3><O><j>YYWWNNN	
ATTINY814-SSFR	<j><CATN><BS1>YWW<rev1><BS1><COO>	<j>TINY814-F	<Q3><O><j>YYWWNNN	
ATTINY814-SSN	<j><CATN><BS1>YWW<rev1><BS1><COO>	<j>TINY814-N	<Q3><O><j>YYWWNNN	
ATTINY814-SSNR	<j><CATN><BS1>YWW<rev1><BS1><COO>	<j>TINY814-N	<Q3><O><j>YYWWNNN	

Post Change

Atmel YWW CC
TINY814
XXXXXX

After Marking

Atmel CPN	Line 1	Line 2	Line 3	Line 4
ATTINY814-SSF	<j><CATN><BS1>YWW<BS1><COO>	<j>TINY814	<Q3><O><j>YYWWNNN	
ATTINY814-SSFR	<j><CATN><BS1>YWW<BS1><COO>	<j>TINY814	<Q3><O><j>YYWWNNN	
ATTINY814-SSN	<j><CATN><BS1>YWW<BS1><COO>	<j>TINY814	<Q3><O><j>YYWWNNN	
ATTINY814-SSNR	<j><CATN><BS1>YWW<BS1><COO>	<j>TINY814	<Q3><O><j>YYWWNNN	