

MicroZed 7010/20 SOM



Product Change Notification PCN20002

Products Affected:

AES-Z7MB-7Z010-SOM-G/REV-G
AES-Z7MB-7Z010-SOM-I-G/REV-G
AES-Z7MB-7Z020-SOM-G/REV-G
AES-Z7MB-7Z020-SOM-I-G/REV-G

Subject(s):

1. Ethernet reset timing adjustment at first power up and during release of reset_n signal.

Products Affected: This PCN affects the part numbers listed below for date codes 2004 and later.

The date code is visible in the 2D barcode. The date code is the 4th line of the code and is 4 digits formatted as "YYWW."

Change Description 1:

Decrease R106 from 10K to 4.99K. Increase C224 from 0.47uF to 2.2uF keeping same footprints. No new BOM components added.

Reason for Change: Marvell MAC IC lists minimum reset low duration to be 10ms. Increasing 1 tau to approximately 11ms during all temperature and variance ranges ensures this requirement is met during board power up and reset_n de-assertion.