# **MOSFET** – Power, **N-Channel** 100 V, 201 A, 4.2 mΩ

# **NTB004N10G**

#### **Features**

- Low R<sub>DS(on)</sub>
- High Current Capability
- Wide SOA
- These Devices are Pb-Free and are RoHS Compliant

#### **Applications**

• Hot Swap in 48 V Systems

#### **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C Unless otherwise specified)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			$V_{DSS}$	100	V
Gate-to-Source Voltage - Continuous			V <sub>GS</sub>	±20	V
Continuous Drain	Steady	T <sub>C</sub> = 25°C	I <sub>D</sub>	201	Α
Current R <sub>θJC</sub>	State	T <sub>C</sub> = 100°C		142	
Power Dissipation $R_{\theta JC}$	Steady State	T <sub>C</sub> = 25°C	P <sub>D</sub>	340	W
Pulsed Drain Current	t <sub>p</sub> = 100 μs		I <sub>DM</sub>	3002	Α
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Source Current (Body Diode)			IS	283	Α
Single Pulse Drain-to-Source Avalanche Energy ( $V_{DD}$ = 50 Vdc, $V_{GS}$ = 10 Vdc, $I_{L(pk)}$ = 102 A, L = 0.1 mH, $R_G$ = 25 $\Omega$ )			E <sub>AS</sub>	520	mJ
Lead Temperature for Soldering Purposes, 1/8" from Case for 10 Seconds			TL	260	°C

#### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case (Drain) Steady State	$R_{\theta JC}$	0.44	°C/W
Junction-to-Ambient (Note 1)	$R_{\theta JA}$	62.5	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface mounted on FR4 board using 1 sq in pad size, (Cu Area 1.127 sq in [2 oz] including traces).

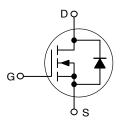


# ON Semiconductor®

#### www.onsemi.com

V <sub>(BR)Ds</sub>	ss R <sub>D</sub>	<sub>S(ON)</sub> MAX	I <sub>D</sub> MAX (Note 1)
100 V	4.2	mΩ @ 10 V	201 A

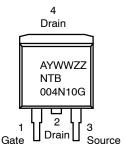
#### N-Channel





D<sup>2</sup>PAK CASE 418AJ STYLE 2

# MARKING DIAGRAM & PIN ASSIGNMENT



A = Assembly Site Code

Y = Year Code

WW = Week Code

ZZ = 2-digit Assembly Lot Code NTB004N10G = Specific Device Code

#### **ORDERING INFORMATION**

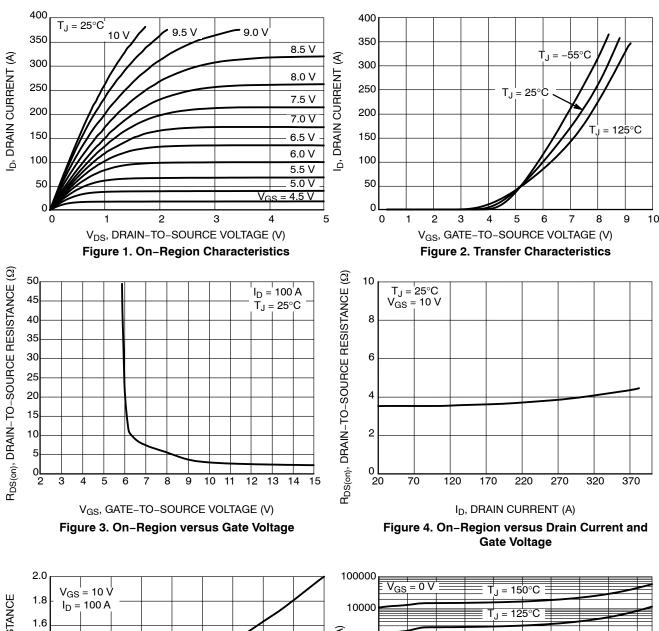
See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

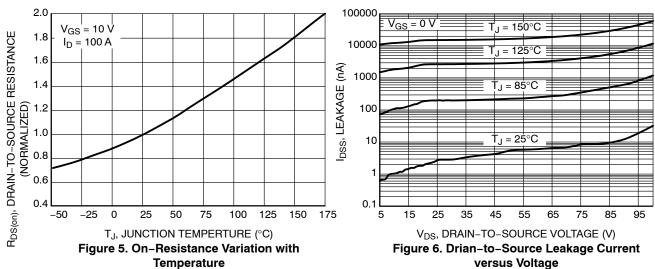
# **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ Unless otherwise specified)

Characteristics	Symbol	Test Co	ondition	Min	Тур	Max	Unit
OFF CHARACTERISTICS	-	-		-	-	-	-
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		100			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>				32.7		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$I_{DSS}$ $V_{GS} = 0 \text{ V}, T_{J} = 25^{\circ}\text{C}$				1.0	μΑ
		V <sub>DS</sub> = 80 V	T <sub>J</sub> = 150°C			100	1
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V	′ <sub>GS</sub> = ±20 V			±100	nA
ON CHARACTERISTICS (Note 2)	•			•	•		•
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{GS} = V_{DS}$	I <sub>D</sub> = 500 μA	2.0	2.8	4.0	V
Negative Threshold Temperature Coefficient	V <sub>GS(th)</sub> /T <sub>J</sub>				-10.5		mV/°C
Drain-to-Source On-Resistance	= 100 A	T <sub>J</sub> = 25°C		3.4	4.2	mΩ	
		T <sub>J</sub> = 175°C		6.82		mΩ	
Forward Transconductance	9FS	V <sub>DS</sub> = 10 V	, I <sub>D</sub> = 100 A		70		S
CHARGES, CAPACITANCES & GATE RESIST	ANCE				1		•
Input Capacitance	C <sub>iss</sub>				11900		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 50 V f = 1	$V_{GS} = 0 V$		1170		1
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1 MHz			147		1
Total Gate Charge	Q <sub>G(TOT)</sub>				175		nC
Threshold Gate Charge	Q <sub>G(TH)</sub>				78.4		-
Gate-to-Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10 V,	V <sub>DS</sub> = 50 V,		67.3		
Gate-to-Drain Charge	$Q_{GD}$	I <sub>D</sub> = 100 A			40.8		1
Plateau Voltage	$V_{GP}$				6.0		V
Gate Resistance	$R_{G}$	V <sub>OSC</sub> = 100 mV, V <sub>GS</sub> = 0 V, f = 1 MHz			0.445		Ω
SWITCHING CHARACTERISTICS, V <sub>GS</sub> = 10 V	(Note 3)	l			1		
Turn-On Delay Time	t <sub>d(on)</sub>				43		ns
Rise Time	t <sub>r</sub>	Voc = 10 V	V <sub>DD</sub> = 50 V		64.5		1
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ = 10 V, $V_{DD}$ = 50 V, $I_{D}$ = 100 A, $R_{G}$ = 4.7 $\Omega$			84.7		1
Fall Time	t <sub>f</sub>				30		1
DRAIN-SOURCE DIODE CHARACTERISTICS							•
Forward Diode Voltage V <sub>SD</sub> I <sub>S</sub> = 100 A	V <sub>SD</sub>		T <sub>J</sub> = 25°C		0.9	1.2	V
	I <sub>S</sub> = 100 A	T <sub>J</sub> = 125°C		0.77		1	
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 100 A, dI <sub>SD</sub> /dt = 100 A/μs			76.6		ns
Charge Time	ta				46.4		1
Discharge Time	t <sub>b</sub>				30.2		1
Reverse Recovery Charge	Q <sub>RR</sub>				157		nC

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

#### **TYPICAL CHARACTERISTICS**





#### TYPICAL CHARACTERISTICS

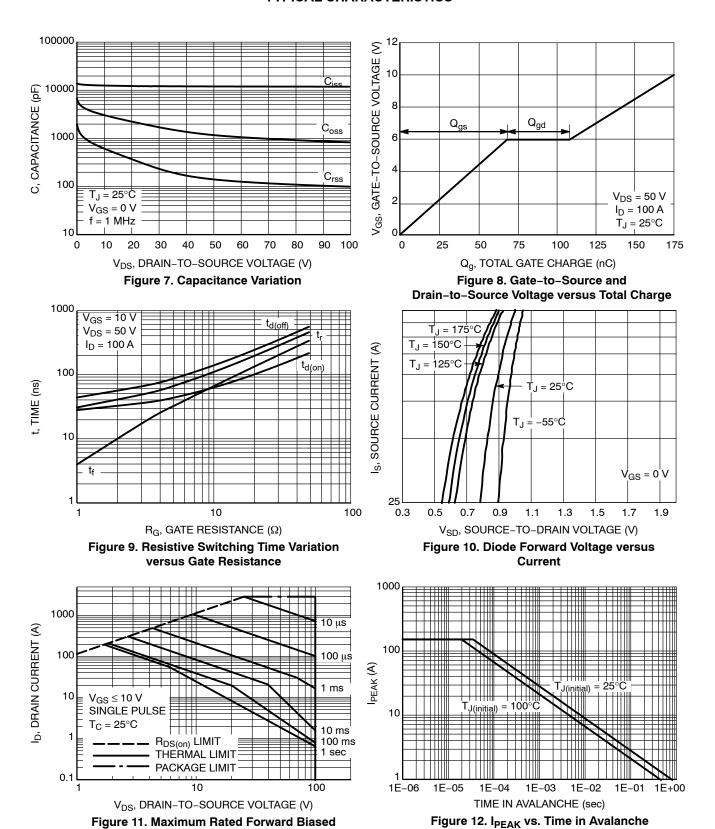


Figure 12.  $I_{\mbox{\scriptsize PEAK}}$  vs. Time in Avalanche

Safe Opeating Area

# **TYPICAL CHARACTERISTICS**

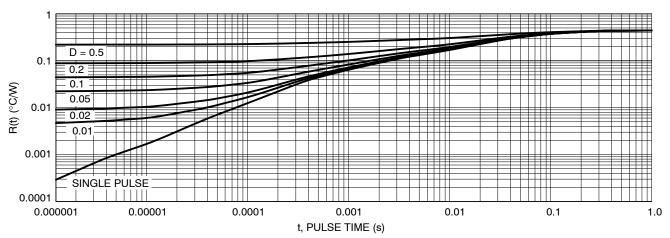


Figure 13. Thermal Response

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTB004N10G	D <sup>2</sup> PAK (Pb-Free)	800 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS

#### D<sup>2</sup>PAK-3 (TO-263, 3-LEAD) CASE 418AJ ISSUE E

#### NUTES

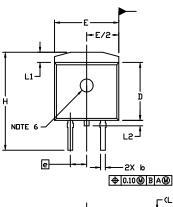
0.366

0.169

0.100 PITCH

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- CONTROLLING DIMENSION: INCHES
- CHAMFER OPTIONAL.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.005 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE DUTERMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
- THERMAL PAD CONTOUR IS OPTIONAL WITHIN DIMENSIONS E, L1, D1, AND E1.
- OPTIONAL MOLD FEATURE.
- ... OPTIONAL CONSTRUCTION FEATURE CALL DUTS.

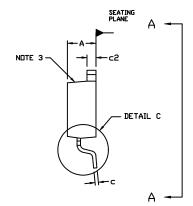
	INCHES		MILLIMETERS		
DIM	MIN.	MAX.	MIN.	MAX.	
Α	0.160	0.190	4.06	4.83	
A1	0.000	0.010	0.00	0.25	
b	0.020	0.039	0.51	0.99	
С	0.012	0.029	0.30	0.74	
c2	0.045	0.065	1.14	1.65	
D	0.330	0.380	8.38	9.65	
D1	0.260		6.60		
E	0.380	0.420	9.65	10.67	
E1	0.245		6.22		
e	0.100 BSC		2.54	2.54 BSC	
Н	0.575	0.625	14.60	15.88	
L	0.070	0.110	1.78	2.79	
L1		0.066		1.68	
L2		0.070		1.78	
L3	0.010 BSC		0.25 BSC		
М	-8*	8.	-8*	8*	

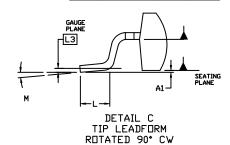


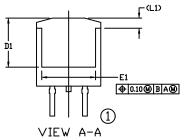
RECOMMENDED MOUNTING FOOTPRINT

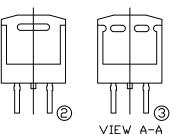
0.653

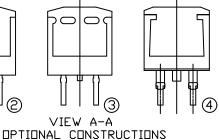
2x 0.063











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