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DM74ALS05A Hex Inverter with Open Collector Outputs

FAIRCHILD

SEMICONDUCTOR TM

DM74ALS05A Hex Inverter with Open Collector Outputs

General Description

This device contains six independent gates, each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$\mathsf{R}_{\mathsf{MAX}} = \frac{\mathsf{V}_{\mathsf{CC}}\left(\mathsf{Min}\right) - \mathsf{V}_{\mathsf{OH}}}{\mathsf{N}_1\left(\mathsf{I}_{\mathsf{OH}}\right) + \mathsf{N}_2\left(\mathsf{I}_{\mathsf{IH}}\right)}$$

$$\mathsf{R}_{\mathsf{MIN}} = \frac{\mathsf{V}_{\mathsf{CC}}\left(\mathsf{Max}\right) - \mathsf{V}_{\mathsf{OL}}}{\mathsf{I}_{\mathsf{OL}} - \mathsf{N}_{\mathsf{3}}\left(\mathsf{I}_{\mathsf{IL}}\right)}$$

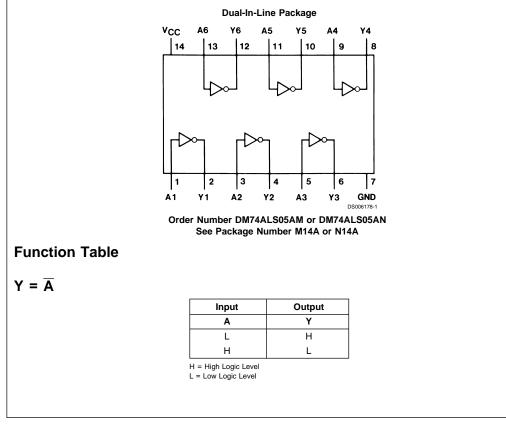
Where: $N_1 (I_{OH}) =$ total maximum output high current for all outputs tied to pull-up resistor $N_2 (I_{H}) =$ total maximum input high current for all inputs tied to pull-up resistor

 N_3 (I_{IL}) = total maximum input low current for all inputs tied to pull-up resistor

Features

- Switching specifications at 50 pF
- Switching specifications guaranteed over full
- temperature and V_{CC} range Advanced oxide-isolated, ion-implanted Schottky TTL
- Process
- Functionally and pin for pin compatible with Schottky and low power Schottky TTL counterpart
- Improved AC performance over Schottky and low power Schottky counterparts

Connection Diagram



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Absolute Maximum Ratings (Note 1)

Supply Voltage Input Voltage High Level Output Voltage	7V 7V 7V
Operating Free Air Temperature	
Range	

DM74ALS Storage Temperature Range	0°C to +70°C −65°C to +150°C
Typical θ _{JA}	
N Package	88.0°C/W
M Package	118.5°C/W

Recommended Operating Conditions

Symbol	mbol Parameter DM74ALS05A			Units	
		Min	Nom	Max]
V _{cc}	Supply Voltage	4.5	5	5.5	V
V _{IH}	High Level Input Voltage	2			V
VIL	Low Level Input Voltage			0.8	V
V _{он}	High Level Output Voltage			5.5	V
I _{OL}	Low Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended operatir	ng free air temperature range	e. All typical values are measur	red at $V_{co} = 5V T_{c} = 25^{\circ}C$
	ig noo an tomporataro range	. The typical values are moused	

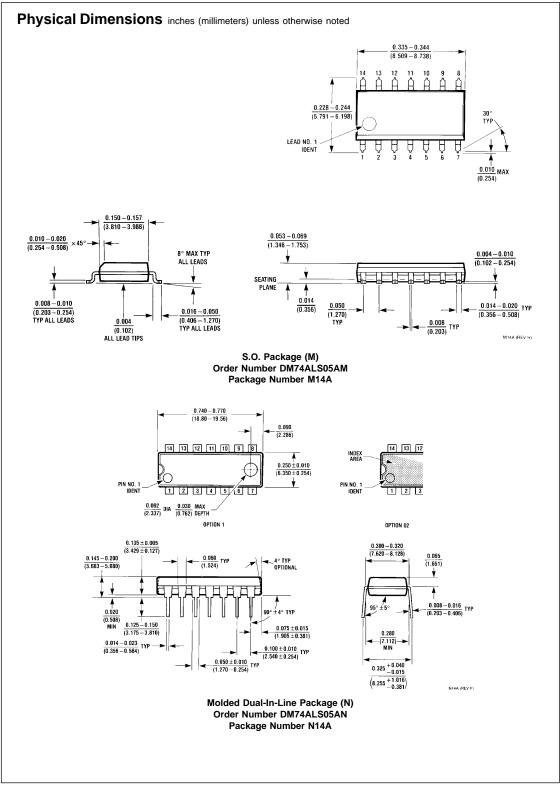
Symbol	Parameter	Con	ditions	Min	Тур	Max	Units
VIK	Input Clamp Voltage	$V_{\rm CC} = 4.5 V, I_{\rm I} = -18 \text{ mA}$				-1.5	V
I _{ОН}	High Level Output Current	V _{CC} = 4.5V, V _{OF}	V _{CC} = 4.5V, V _{OH} = 5.5V			100	μA
V _{OL}	Low Level Output	V _{CC} = 4.5V	I _{OL} =4 mA		0.25	0.4	V
	Voltage		I _{OL} = 8 mA		0.35	0.5	V
l _i	Input Current @ Max	$V_{\rm CC} = 5.5 V, V_{\rm IH} = 7 V$				0.1	mA
	Input Voltage						
I _{IH}	High Level Input Current	V _{CC} = 5.5V, V _{IH}	= 2.7V			20	μA
I _{IL}	Low Level Input Current	$V_{\rm CC} = 5.5 V, V_{\rm IL} = 0.4 V$				-0.1	mA
I _{cc}	Supply Current	V _{CC} = 5.5V	Outputs High		0.65	1.1	mA
			Outputs Low		2.4	4.2	mA

Switching Characteristics

over recommended operating free air temperature range (Note 2)

Symbol	Parameter	Conditions	DM74ALS05A		Units	
			Min	Max		
t _{PLH}	Propagation Delay Time	$V_{\rm CC}$ = 4.5V to 5.5V	23	54	ns	
	Low to High Level Output	$R_L = 2 k\Omega, C_L = 50 pF$				
t _{PHL}	Propagation Delay Time		4	14	ns	
	High to Low Level Output					

Note 2: See Section 1 for test waveforms and output load.



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