DM74ALS32 Quad 2-Input OR Gate Advanced oxide-isolated, ion-implanted Schottky TTL **General Description** process This device contains four independent gates, each of which Functionally and pin for pin compatible with Schottky performs the logic OR function. and low power Schottky TTL counterpart Improved AC performance over Schottky and low power Features Schottky counterparts Switching specifications at 50 pF Switching specifications guaranteed over full temperature and V_{CC} range **Connection Diagram Dual-In-Line Package** V_{CC} B4 ¥4 в3 Δ4 Δ3 ¥3 14 13 12 11 110 2 3 4 | 5 6 7 A1 **B1** Υ1 A2 B2 ¥2 GND DS00 Order Number DM74ALS32M, DM74ALS32N or DM74ALS32SJ See Package Number M14A, M14D or N14A **Function Table** Y = A + BInputs Output Α в Y L Т L н н L н L н н н Н H = High Logic Level L = Low Logic Level

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Absolute Maximum Ratings (Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	
DM74ALS	0°C to +70°C

Storage Temperature Range	-65°C to +150°C
Typical θ _{JA}	
N Package	89°C/W
M Package	120°C/W

Recommended Operating Conditions

Symbol	Parameter	DM74ALS32			Units	
		Min	Nom	Max	1	
V _{cc}	Supply Voltage	4.5	5	5.5	V	
V _{IH}	High Level Input Voltage	2			V	
VIL	Low Level Input Voltage			0.8	V	
I _{он}	High Level Output Current			-0.4	mA	
I _{OL}	Low Level Output Current			8	mA	
T _A	Free Air Operating Temperature	0		70	°C	

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended operating free air temperature range. All typical values are measured at V_{CC} = 5V, T_A = 25°C.

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	V _{CC} = 4.5V, I _I =	$V_{\rm CC}$ = 4.5V, I _I = -18 mA			-1.5	V
V _{OH}	High Level Output	$I_{OH} = -0.4 \text{ mA}$		V _{CC} – 2			V
	Voltage	$V_{\rm CC} = 4.5 V$ to 5	$V_{\rm CC}$ = 4.5V to 5.5V				
V _{OL}	Low Level Output	$V_{\rm CC} = 4.5V$	I _{OL} = 4 mA		0.25	0.4	V
	Voltage		I _{OL} = 8 mA		0.35	0.5	V
l _i	Input Current @ Max.	$V_{\rm CC} = 5.5 V, V_{\rm IH} = 7 V$				0.1	mA
	Input Voltage						
I _{IH}	High Level Input Current	V _{CC} = 5.5V, V _{II}	V _{CC} = 5.5V, V _{IH} = 2.7V			20	μA
I _{IL}	Low Level Input Current	$V_{\rm CC}$ = 5.5V, $V_{\rm IL}$	$V_{CC} = 5.5V, V_{IL} = 0.4V$			-0.1	mA
lo	Output Drive Current	$V_{\rm CC} = 5.5V$	V _O = 2.25V	-30		-112	mA
I _{cc}	Supply Current	V _{CC} = 5.5V	Outputs High		1.9	4	mA
			Outputs Low		2.6	4.9	mA

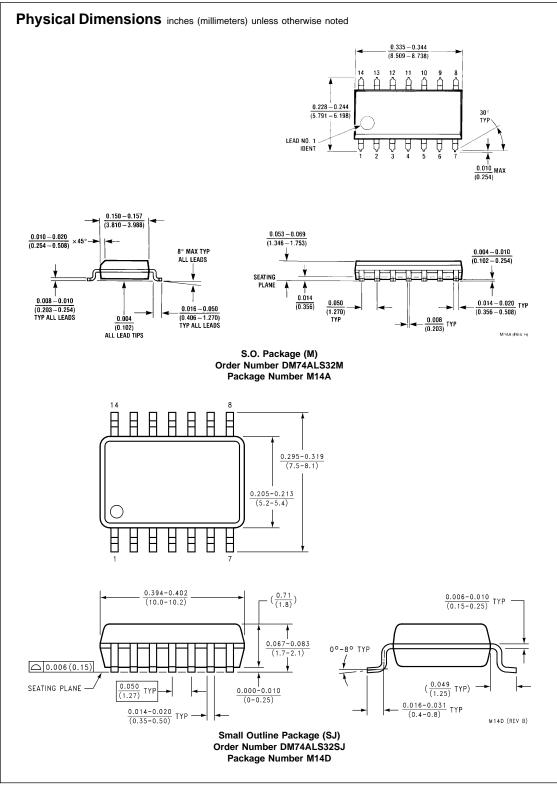
Switching Characteristics

over recommended operating free air temperature range (Note 2).

Symbol	Parameter	Conditions	DM74ALS32		Units	
			Min	Max]	
t _{PLH}	Propagation Delay Time	V _{CC} = 4.5V to 5.5V	3	14	ns	
	Low to High Level Output	$R_L = 500\Omega$				
t _{PHL}	Propagation Delay Time	C _L = 50 pF	3	12	ns	
	High to Low Level Output					

Note 2: See Section 5 for test waveforms and output load.

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