



Final Product Change Notification

202004002F01

Issue Date: 27-Apr-2020

Effective Date: 26-Jul-2020

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

This notice is NXP Company Proprietary.



Change Category

- | | | | | |
|--|--|--|---|---|
| ☐ Wafer Fab Process | ☐ Assembly Process | ☐ Product Marking | ☐ Test Location | ☐ Design |
| ☐ Wafer Fab Materials | ☐ Assembly Materials | ☐ Mechanical Specification | ☐ Test Process | ☐ Errata |
| ☐ Wafer Fab Location | ☐ Assembly Location | ☐ Packing/Shipping/Labeling | ☐ Test Equipment | ☐ Electrical spec./Test coverage |
| ☐ Firmware | ☒ Other - Data Sheet, Reference Manual and Errata Update | | | |

S32K1xx Data Sheet And Reference Manual Update To Rev 13 & S32K1xx Errata Update To Rev 20Apr2020

Description of Change

NXP Semiconductors announces data sheet and reference manual update for the S32K1xx from revision 12 and revision 12.1 respectively to revision 13.

The errata for S32K144 will be updated to revision 20 Apr 2020.

The revision history included in the updated document provides a details description of the changes.

For reference manual changes, please refer to Appendix B, release notes in revision 13.

Please review three important changes listed below in this revision that may require user to update their application.

1) In page 195, under section 11.3.1.8.4_Fields 19-16 that specifies the RAM SIZE will all be changed to RESERVED.

- 2) In page 196, also under section 11.3.1.8.4_Fields 7-0 that specifies device features, BIT 6-0 will be changed to RESERVED.
- 3) In page 776, under section 35.1.3, ECC will be implemented for NVM. Application may need to be updated if user decides to use ECC to detect double-bit ECC errors.

Users will need to stop verifying the SDID registers BIT 19-16 for RAMSIZE and BIT 0-6 for FEATURES if this is currently done.

Data sheet changes:

- 1) In LVR, LVD and POR operating requirements,
 - Updated table title for S32K14xW series
 - Updated values for VLVR for S32K14xW series
- 2) In Absolute maximum ratings, updated TJ for S32K14xW series
- 3) In Thermal operating characteristics, updated TJ for S32K14xW series
- 4) In External System Oscillator electrical specifications, added note 'Minimum value is shown as a reference only ... ' to Vpp_EXTAL.

S32K14x Errata changes:

- 1) Erratum ERR050443 was added.
- 2) Erratum ERR006940 was revised.

S32K11x Errata changes:

- 1) Erratum ERR050443 was added.
- 2) Erratum ERR0011543 was revised.

The S32K1xx data sheet revision 13 and errata revision 20Apr2020 are attached to this notification and can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/s32k-automotive-mcus/s32k-scalable-microcontrollers-for-automotive-general-purpose-and-and-high-reliability-industrial:S32K?tab=Documentation_Tab

Corresponding ZVEI Delta Qualification Matrix ID: SEM-AN-02, SEM-DS-02 and SEM-DS-03.

Reason for Change

The data sheet and reference manual have been updated to provide additional technical clarification.

Identification of Affected Products

Product identification does not change

Product Availability

Sample Information

Not applicable

Production

Planned first shipment 31-Jul-2020

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Disposition of Old Products

Existing inventory will be shipped until depleted

After 27-July-2020, units will be shipped with:

- SDID registers BIT 19-16 for RAMSIZE and BIT 0-6 for FEATURES in RESERVED state.
- Reference ECC for Non-volatile Memory (NVM).

Additional information

Affected products and sales history information: see attached file

Self qualification: [view online](#)



Additional documents: [view online](#)

Timing and Logistics

In compliance with JEDEC J-STD-046, your acknowledgement of this change is expected by 27-May-2020.

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Position NXP Technical Support

e-mail address tech.support@nxp.com

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise. These innovations are used in a wide range of automotive, identification, wireless infrastructure, lighting, industrial, mobile, consumer and computing applications.

You have received this email because you are a designated contact or subscribed to NXP Quality Notifications. NXP shall not be held liable if this Notification is not correctly distributed within your organization.

This message has been automatically distributed. Please do not reply.

[View Notification](#)

[Subscription](#)

[Support](#)

[NXP](#) | [Privacy Policy](#) | [Terms of Use](#)

NXP Semiconductors
High Tech Campus, 5656 AG Eindhoven, The Netherlands

© 2006-2010 NXP Semiconductors. All rights reserved