



Customer Information Notification

202002009I

Issue Date: 29-Apr-2020
Effective Date: 30-Apr-2020
Dear *Gordon Love*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

This notice is NXP Company Proprietary.



Change Category

- | | | | | |
|----------------------------------------------|---------------------------------------------|----------------------------------------------------|-----------------------------------------|---------------------------------------------------------|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input checked="" type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware | <input type="checkbox"/> Other | | | |

Errata Update for
KL28/KL82/KE1xF/KE1xZ

Description

NXP Semiconductors announces that the errata documentations have been updated:

Added ERR050117 for all parts in this notification.

ERR050117, FAC: Execute-only access control feature has been deprecated

KL28 devices (mask 1N52N) has been updated to 6sep2019 revision.

KL82 devices (mask 0N51R) has been updated to 3APR2020 revision

Added ERR009650: QuadSPI: Not all QuadSPI implementations supported

KE1xF devices (mask 0N79P) has been updated to 15Apr2020

Added ERR050246, FlexCAN: Receive Message Buffers may have its Code Field corrupted if the Receive FIFO function is used.

Added ERR050180: pin output issue when power on/off

Added ERR050181: LPIT CVAL cannot be read correctly during timer running

Revised ERR006939: Core: Interrupted loads to SP can cause erroneous behavior.

Revised ERR006940: Core: VDIV or VSQRT instructions might not complete correctly when very short ISRs

are used

Revised ERR009004: Core: ITM can deadlock when global timestamping is enabled

Revised ERR009005: Core: Store immediate overlapping exception return operation might vector to incorrect interrupt

KE1xZ devices (mask 1N36S) has been updated to Rev.2.

Added ERR050180: pin output issue when power on/off

Added ERR050181: LPIT CVAL cannot be read correctly during timer running

Updated ERR010364: LPI2C: LPI2C sends a STOP condition if transmit FIFO is empty on the completion of a master-receive transfer

The revision history included in the updated document provides a detailed description of the changes.

The updated KL28 devices (mask 1N52N) errata can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/kl-series-cortex-m0-plus/kinetis-kl28-72-96-mhz-512kb-flash-usb-ultra-low-power-microcontrollers-mcus-based-on-arm-cortex-m0-plus-core:KL28?tab=Documentation_Tab

The updated KL82 devices (mask 0N51R) errata can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/kl-series-cortex-m0-plus/kinetis-kl8x-72-96-mhz-secure-ultra-low-power-microcontrollers-mcus-based-on-arm-cortex-m0-plus-core:KL8x?fsp=1&tab=Documentation_Tab

The updated KE1xF devices (mask 0N79P) errata can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/ke-series-cortex-m4-m0-plus/kinetis-ke1xf-168mhz-performance-with-can-5v-microcontrollers-based-on-arm-cortex-m4:KE1xF?tab=Documentation_Tab

The updated KE1xZ devices (mask 1N36S) errata can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/ke-series-cortex-m4-m0-plus/up-to-72mhz-5v-main-stream-cm0-plus-mcu-with-nxp-touch-tsi-and-can-control:KE1xZ?tab=Documentation_Tab

Reason

The Erratum have been updated to provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Additional information

Affected products and sales history information: see attached file

Additional documents: [view online](#)



Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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NXP Quality Management Team.

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