

Customer Information Notification

Issue Date: 11-Apr-2020 Effective Date: 12-Apr-2020 Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP. For detailed information we invite you to <u>view this</u> notification online

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2020030321



QUALITY

Change Category

[] Wafer Fab	[] Assembly	[] Product Marking	[] Test	[] Design
Process	Process		Location	
[] Wafer Fab	[] Assembly	[] Mechanical Specification	[]Test Process	s [] Errata
Materials	Materials			
[] Wafer Fab	[] Assembly	[]	[] Test	[] Electrical spec./Test
Location	Location	Packing/Shipping/Labeling	Equipment	coverage
[] Firmware	[X] Other - Data Sheet Update - Corrections and Clarifications			

MC33772B Data Sheet Update to Rev 6.0 (Corrections and Technical Clarifications)

Description

NXP Semiconductors announces the data sheet update to revision 6.0 for the MC33772B Battery Cell Controller IC devices associated with this notification. The revision history included in the updated document provides a detailed description of the changes. Changes are summarized below.

Data Sheet Changes:

1. Table 4: Correct typo in communication type corresponding to given values (typo correction)

2. Table 7: Add missing footnote call on VCVFV and VLEAK line. Footnote existed but was not called on these lines (typo correction)

3. Table 7, Footnote 18: Correct document reference name/number (informative)

4. Table 9: Reword to clarify communication behavior in Normal, Diagnostic, Idle and Sleep mode (description improvement)

5. Section 9.4.2, Section 12.1 and Tables 64, 65 and 66: Clarify note about Vana fault (description improvement)

6. Section 9.13: Add note regarding current sense in sleep mode and cyclic acquisition (additional recommendations)

7. Section 9.17: Clarify CBx_CFG bits behavior (description improvement)

8. Section 9.18: Add step in "Cell balance fault diagnostics" (procedure modification, already implemented in the Safety Manual)

9. Section 11: Reformat register map (description improvement)

- 10. Section 11: Clarify status for reserved and not used bits (description improvement)
- 11. Table 92: Update list of "reserved" registers (informative)
- 12. Section 13.2.3: Correct maximum VPWR voltage to sustain hot plug (informative)

13. Section 13.2.1: Add paragraph regarding CTREF variations (optional application improvement - optional diodes - no system impact)

14. Table 102: Correct CTPL value (EVB hardware change, no system impact)

15. Other minor typo corrections throughout the document

** Documentation change only for customer clarifications - absolutely no changes to the device / product **

New MC33772B rev 6.0 data sheet may be obtained from the secure DocStore portal:

https://www.docstore.nxp.com/products?path=/content/docstore/product-hierarchy/Automotive-Battery-Management/MC33771--MC33772--MC33664/Datasheet&folderuuid=10722735-c3a3-47dd-a794-8eec20db8c66

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02 Reason

The data sheet has been updated to correct errors, and provide additional technical clarification on some device features, as well as on the register map.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Additional information

Affected products and sales history information: see attached file

Additional documents: view online

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please <u>contact NXP "Global Quality</u> Support Team".

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For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly: Name Amélie Meyer Position Applications Engineer

e-mail address amelie.meyer@nxp.com

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NXP Quality Management Team.

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