

Automotive P-Channel 30 V (D-S) 175 °C MOSFET



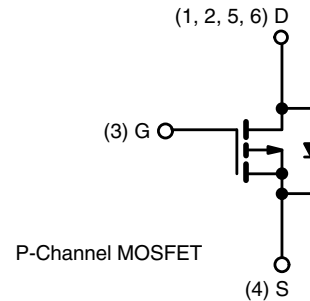
Marking Code: 9F

PRODUCT SUMMARY	
V_{DS} (V)	-30
$R_{DS(on)}$ (Ω) at $V_{GS} = -4.5$ V	0.021
$R_{DS(on)}$ (Ω) at $V_{GS} = -2.5$ V	0.033
I_D (A)	-8
Configuration	Single
Package	TSOP-6

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE


RoHS
COMPLIANT
HALOGEN
FREE


ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-source voltage	V_{DS}	-30	V	
Gate-source voltage	V_{GS}	± 12		
Continuous drain current	I_D	$T_C = 25$ °C	-8	A
		$T_C = 125$ °C	-7	
Continuous source current (diode conduction)	I_S	-4.5		
Pulsed drain current	I_{DM}	-32		
Single pulse avalanche current	I_{AS}	-19.5		
Single pulse avalanche Energy	E_{AS}	19	mJ	
Maximum power dissipation ^a	P_D	$T_C = 25$ °C	5	W
		$T_C = 125$ °C	1.6	
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-ambient	R_{thJA}	110	°C/W	
Junction-to-foot (drain)	R_{thJF}	30		

Notes

- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %
- When mounted on 1" square PCB (FR4 material)



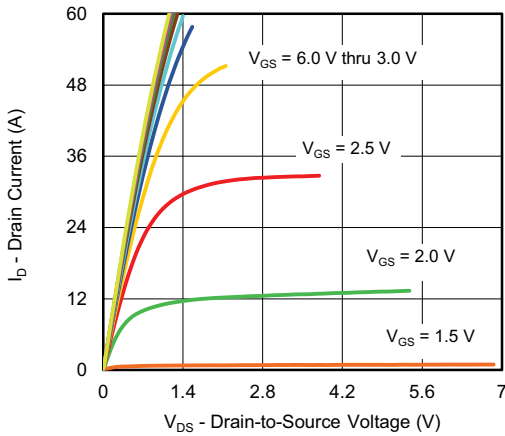
SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = -250 μA		-30	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA		-0.6	-1	-1.4	
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 12 V		-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = -30 V	-	-	-1	μA
		V _{GS} = 0 V	V _{DS} = -30 V, T _J = 125 °C	-	-	-50	
		V _{GS} = 0 V	V _{DS} = -30 V, T _J = 175 °C	-	-	-150	
On-state drain current ^a	I _{D(on)}	V _{GS} = -10 V	V _{DS} = -5 V	-10	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = -4.5 V	I _D = -5 A	-	0.017	0.021	Ω
		V _{GS} = -4.5 V	I _D = -5 A, T _J = 125 °C	-	-	0.030	
		V _{GS} = -4.5 V	I _D = -5 A, T _J = 175 °C	-	-	0.034	
		V _{GS} = -2.5 V	I _D = -4 A	-	0.027	0.034	
Forward transconductance ^b	g _{fs}	V _{DS} = -15 V, I _D = -5 A		-	24	-	S
Dynamic ^b							
Input capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = -20 V, f = 1 MHz	-	3032	3950	pF
Output capacitance	C _{oss}			-	220	285	
Reverse transfer capacitance	C _{rss}			-	217	285	
Total gate charge ^c	Q _g	V _{GS} = -4.5 V	V _{DS} = -15 V, I _D = -7.9 A	-	29	41	nC
Gate-source charge ^c	Q _{gs}			-	5.7	-	
Gate-drain charge ^c	Q _{gd}			-	8.4	-	
Gate resistance	R _g	f = 1 MHz		2.2	5.6	9	Ω
Turn-on delay time ^c	t _{d(on)}	V _{DD} = -15 V, R _L = 1.9 Ω I _D ≅ -7.9 A, V _{GEN} = -4.5 V, R _g = 1 Ω		-	20	28	ns
Rise time ^c	t _r			-	51	72	
Turn-off delay time ^c	t _{d(off)}			-	71	100	
Fall time ^c	t _f			-	68	96	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed current ^a	I _{SM}			-	-	-32	A
Forward voltage	V _{SD}	I _F = -5 A, V _{GS} = 0 V		-	-0.8	-1.2	V

Notes

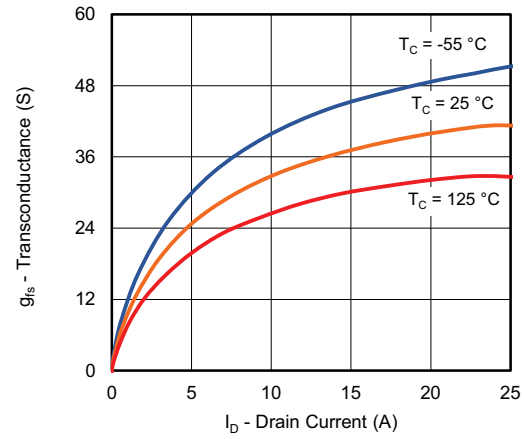
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- Guaranteed by design, not subject to production testing
- Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

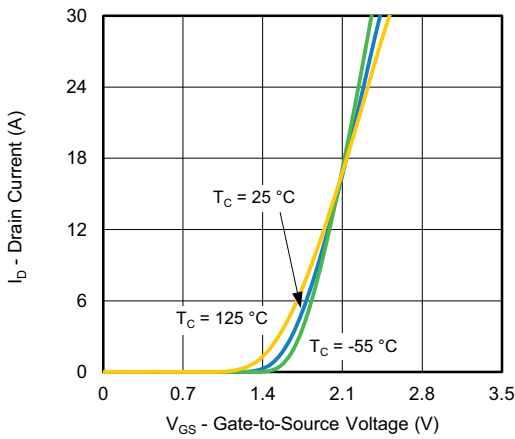
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



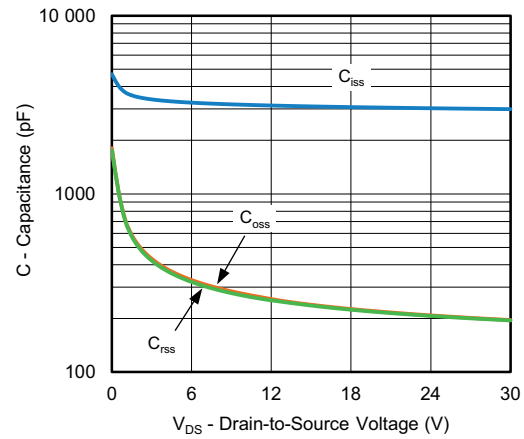
Output Characteristics



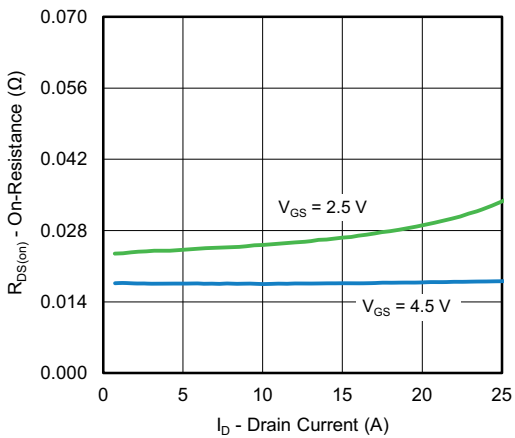
Transconductance



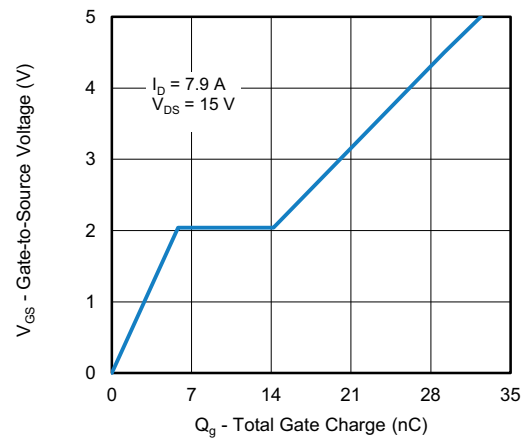
Transfer Characteristics



Capacitance



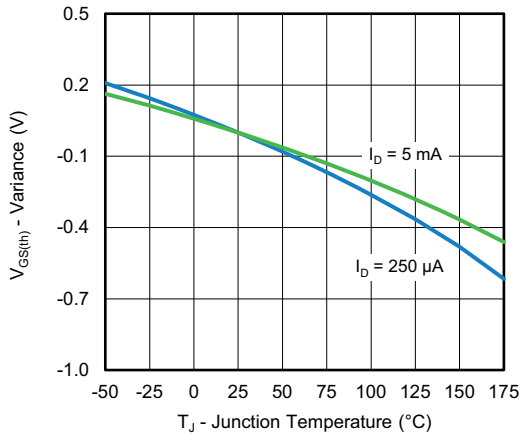
On-Resistance vs. Drain Current



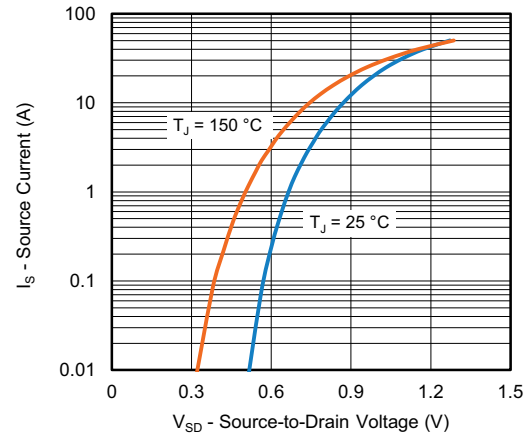
Gate Charge



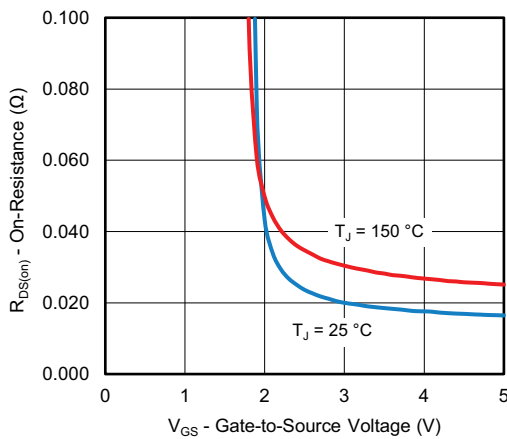
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



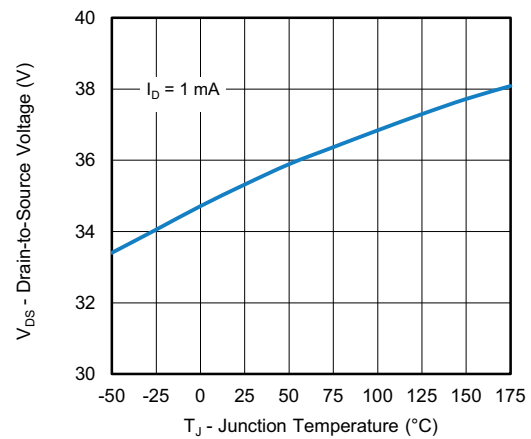
Threshold Voltage



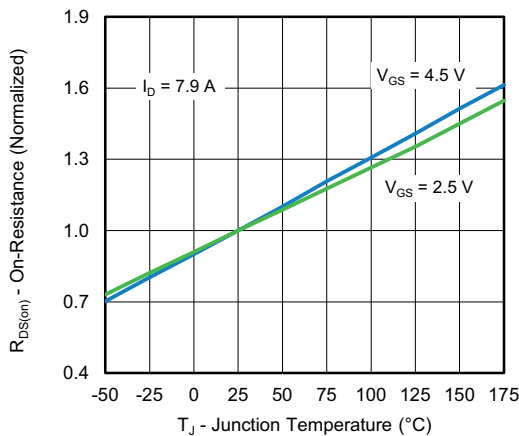
Source Drain Diode Forward Voltage



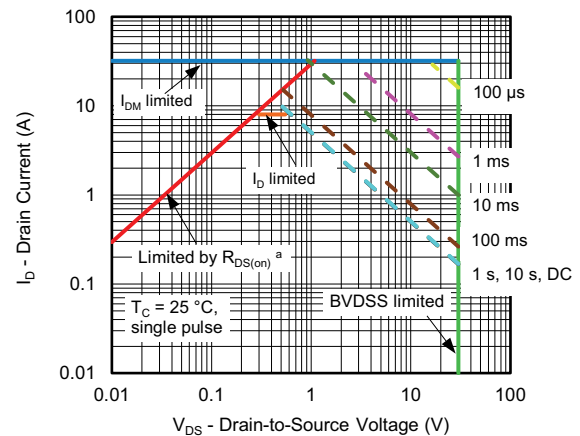
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



On-Resistance vs. Junction Temperature

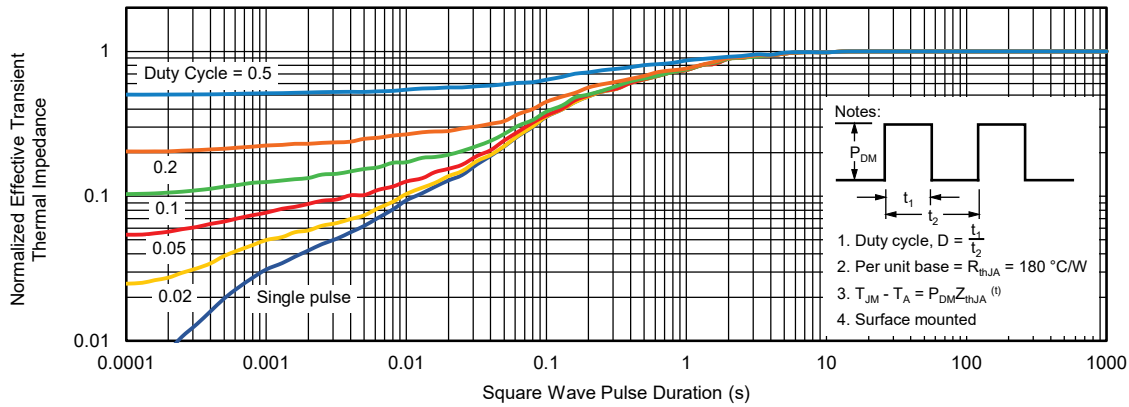


Safe Operating Area

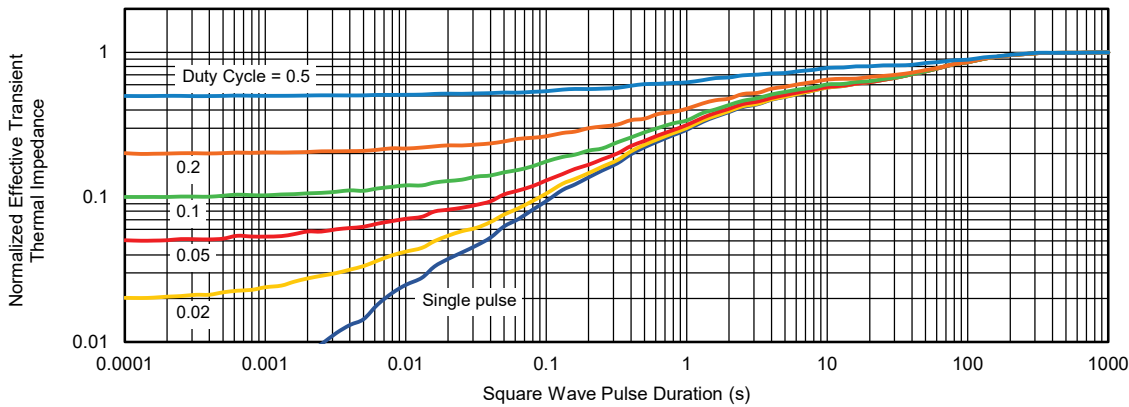
Note

a. $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^\circ\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Case ($25\text{ }^\circ\text{C}$)
 are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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