

Current Transducers HY 5 to 25-P

For the electronic measurement of currents: DC, AC, pulsed, mixed, with a galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).





Electrica	al data				
Primary nomina r.m.s. current $I_{PN}(A)$	Primary current measuring range I _P (A)	Primary conductor (mm)		Туре	
5	± 15	Ø 0.7		HY 5-P	
10	± 30	Ø 1.1		HY 10-P	
12.5	± 37.5	Ø 1.4		HY 12-P	
15	± 45	Ø 1.4		HY 15-P	
20	± 60	$2 \times \emptyset 1.2^{1)}$		HY 20-P	
25	±75	2 x Ø 1.4 ¹⁾		HY 25-P	
V _C	Supply voltage (± 5 %) ⁶⁾			±12±1	5 V
I _c	Current consumption 2			±10	mA
Îp	Overload capability (1 ms)			50 x I _{PN}	
$\mathbf{V}_{_{d}}^{^{\!$	R.m.s. voltage for AC isola	tion test, 50/60H	z, 1 mn	2.5	kV
V _b	R.m.s. rated voltage, safe s			500 ²⁾	V
R IS	Isolation resistance @ 500	•		> 1000	$M\Omega$
V _{OUT}	Output voltage @ ± I _{PN} , R _L	$= 10 \text{ k}\Omega, T_{A} = 25$	5°C	±4	V
R _{OUT}	Output internal resistance	· A		100	Ω
R	Load resistance			> 1	kΩ
Accurac	y - Dynamic perform	ance data			
X	Accuracy @ I_{PN} , $T_{A} = 25^{\circ}C$	(without offset)		< ±1	%
$\mathbf{e}_{\scriptscriptstyle oldsymbol{oldsymbol{arepsilon}}}$	Linearity 3) $(0 \pm \hat{I}_{PN})$			< ±1	% of I _{PN}
V _{OE}	Electrical offset voltage, T _A			$< \pm 40$	mV
V _{OH}	Hysteresis offset voltage @				
	after an excursion of 1 x I_{PN}			< ±15	mV
V _{OT}	Thermal drift of \mathbf{V}_{OE}		typ.	±1.5	mV/K
TO 0	T		max.	±3	mV/K
TCe _G	Thermal drift of the gain (%			< ±0.1	%/K
t,	Response time @ 90% of	I _P		< 3	μs
di/dt	di/dt accurately followed	\ -ID\		> 50	A/µs
f	Frequency bandwidth 4) (- 3	aR)		DC 50	kHz

Notes:	1)	Conductor terminals are soldered together.
I VOLCO.		Conductor terminals are soldered together.

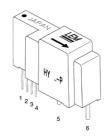
²⁾ Pollution class 2, overvoltage category III.

Ambient operating temperature

Ambient storage temperature

- 3) Linearity data exclude the electrical offset.
- 4) Please refer to derating curves in the technical file to avoid excessive core heating at high frequency.
- 5) Please consult characterisation report for more technical details and application advice.
- ⁶⁾ Operating at ±12V ≤ Vc < ±15V will reduce measuring range.

$I_{PN} = 5...25 A$



Features

- Hall effect measuring principle
- Galvanic isolation between primary and secondary circuit
- Isolation voltage 2500 V~
- Compact design for PCB mounting
- Low power consumption
- Extended measuring range (3 x I_{PN})
- Insulated plastic case recognized according to UL 94-V0.

Advantages

- Easy mounting
- Small size and space saving
- Only one design for wide current ratings range
- High immunity to external interference.

Applications

°C

°C

g

-10 .. +80

-25 .. +85

EN50178

< 14

- General purpose inverters
- AC variable speed drives
- Static converters for DC motor drives
- Battery supplied applications
- Uninterruptible Power Supplies (UPS)
- Switched Mode Power Supplies (SMPS).

030402/4

General data

Mass

Standards 5)

 $\mathbf{T}_{\mathrm{S}}^{\mathrm{A}}$



