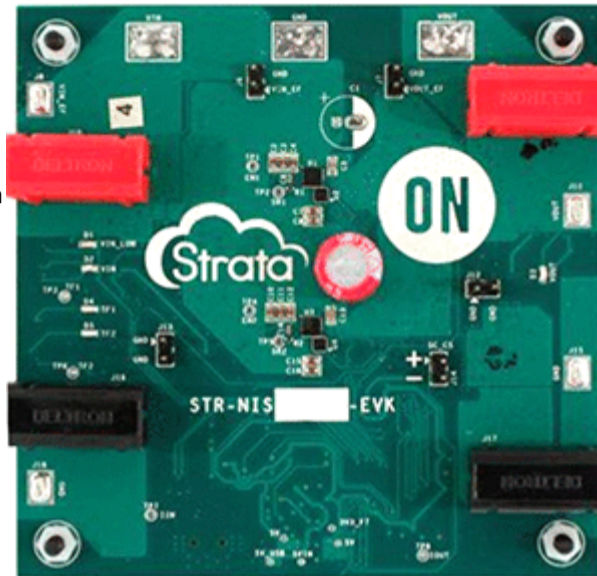




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# STR-NIS5020-GEVB: Strata Enabled NIS5020 12V, 14 mΩ, 10A eFuse

The STR-NIS5020-GEVB provides an evaluation board for the NIS5020 eFuse within the Strata Development Environment. The NIS5020 offers a programmable trip current limit, a programmable slew rate, and thermal protection features. The evaluation board provides an on board short circuit load to test the short circuit protection features of the eFuse. This evaluation board must be used with Strata to control the eFuse and monitor telemetry such as input/output voltage, input/output current, and temperature. See the Strata webpage at [www.onsemi.com/strata](http://www.onsemi.com/strata) for more details!



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### Features and Applications

#### Features

- Vin Range from 9.2V to 18V
- Multiple overload current options
- 2 independently controlled eFuses that can be placed in parallel
- Programmable slew rate

#### Applications

- Motherboards
- Hard Drives
- Servers
- Fan Drives

### Evaluation/Development Tool Information

Product	Status	Compliance	Short Description	Parts Used	Action
<a href="#">STR-NIS5020-GEVB</a>	Active	Pb-free	Strata Enabled NIS5020 12V, 14 mΩ, 10A eFuse	<a href="#">NIS5020MT1TXG</a>	<a href="#">Contact Local Sales Office Inventory</a>

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### Technical Documents

Type	Document Title	Document ID/Size	Rev
Software	Strata Setup Software for Microsoft Windows	<a href="#">Strata setup Windows - 87664 KB</a>	2.4.0
Eval Board: BOM	STR-NIS5020-GEVB Bill of Materials (ROHS Compliant)	<a href="#">STR-NIS5020-GEVB_BOM_ROHS - 137 KB</a>	1
Eval Board: Gerber	STR-NIS5020-GEVB Gerber Layout Files (Zip Format)	<a href="#">STR-NIS5020-GEVB_GERBER - 427 KB</a>	1
Eval Board: Schematic	STR-NIS5020-GEVB Schematic	<a href="#">STR-NIS5020-GEVB_SCHEMATIC - 321 KB</a>	1
Video	Isolation & Protection Devices   Strata	<a href="#">WVD17375/D</a>	
Eval Board: Manual	STR-NIS5020-GEVB User Guide	<a href="#">STR-NIS5020-GEVB_user_guide - 800 KB</a>	0

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