# Resettable PPTC Fuses - 0805 multicomp PRO



Dimensions : Millimetres

#### **Features**

• Fast tripping, 0805 Size

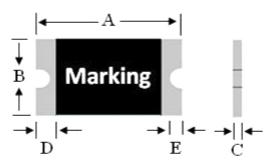
· Surface mountable, Solid state

Holding Current: 3A, @25°C

• Maximum Voltage: 6V

Operating Temperature: -40°C to +85°C

• Agency Approvals: UL E345393, TUV R50450109



**RoHS** 

**Compliant** 

Part NO.	Marking	Α		В		С		D	E
Part NO.		Min	Max	Min	Max	Min	Max	Min	Min
MP005226	Н	2	2.2	1.2	1.5	0.5	1.1	0.2	0.1

# **Electrical Specifications**

	Vmax (V)	IMAX (A)	Ін (А)	Iτ (A)	P <sub>D</sub> (W)	Maximum Time-to-Trip		Resistance	
Part NO.						Current	Time	RMIN	R1max
						(A)	(Sec)	(Ω)	(Ω)
MP005226	6	50	3	6	1.2	8	5	0.004	0.03

# Thermal Derating Chart-I<sub>H</sub> (A)

Dort NO	Maximum holding current at assigned ambient temperature (A)								
Part NO.	-40°C	-20°C	0°C	25°C	40°C	50°C	60°C	70°C	85°C
MP005226	4.29	3.75	3.53	3	2.65	2.5	2.4	1.94	1.36

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## **Physical Characteristics**

Terminal Pad Materials	Tin-Plated Nickle-copper			
Soldering Characteristics	EIA Specification RS 186-9E, ANSI/J-STD-002			
Moisture Sensitivity	Level 2a, per IPC/JEDEC J-STD 020C			

# Test Procedures And Requirements

Test Item	Test Conditions	Accept/Reject Criteria
Initial Resistance	In still air at 25°C	Rmin≤R≤R1max
Time to Trip	Specified current V <sub>MAX</sub> , 25°C	t≤ Maximum Time to Trip
Holding Current	1 hour at IH, 25°C	No trip
Trip Endurance	VMAX, IMAX, 1 hour	No arcing or burning

## Solder Reflow Profiles And Pad Layout Dimensions

Solder pad layout dimensions (mm)				
Normal Value	F	G	Н	
Normal value	1	1.2	1.5	
Profile Feature Pb-Free				

Average ramp up rate (Tsmax to Tp) 3°C/ max.

#### Preheat

- Temperature min (Тѕмік) 150°С
- Temperature max (Tsmax) 200°C
- Time (tsmin to tsmax) 60 to 120 seconds

#### Time maintained above

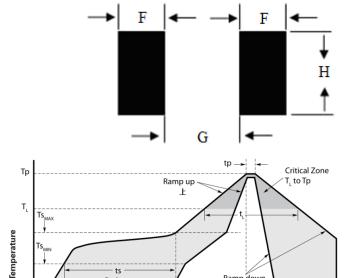
- Temperature (TL) 217°C
- Time(tL) 60 to 150 seconds

Peak/Classification temperature (TP) 260°C

Time within 5°C of actual peak temperature Time (tP) 30 seconds max

Ramp down rate 3°C/second max

Time 25°C to peak temperature 8 minutes max



Preheat

t 25°C to Peak

Time

Recommended reflow methods: IR, hot air oven, N2 environment for lead-free.

Devices are not designed to be wave soldered to the bottom side of the board.

Recommended maximum paste thickness is 0.25mm (0.01inch).

Devices can be cleaned using standard industry methods and aqueous solvents.

Devices can be reworked using the standard industry practices .Please also avoid direct contact to the device.

If reflow temperature exceed the recommended profile, devices may not meet the performance requirements.





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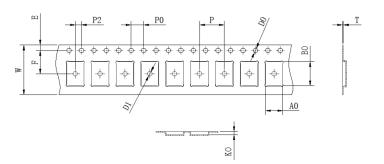
### **Packing Information**

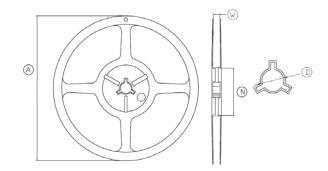
#### **Tape Specifications**

W	8 ±0.3
P0	4 ±0.1
P1	4 ±0.1
P2	2 ±0.1
A0	1.62 ±0.1
B0	2.32 ±0.1
D0	1.5 +0.1/-0
D1	1 +0.1/-0
F	3.5 ±0.1
E	1.75 ±0.1
Т	0.25 ±0.05
K0	0.9 ±0.1
	1.05 ±0.1
	1.5 ±0.1

#### **Reel Dimensions**

Α	178 ±1
D	13.3 ±0.3
W	8.5+1/-0.2
N	59 ±1





# **Storage**

The maximum ambient temperature shall not exceed 40°C. Storage temperatures higher than 40°C could result in the deformation of packaging materials. The maximum relative humidity recommended for storage is 70%. High humidity with high temperature can accelerate the oxidation of the solder plating on the termination and reduce the solderability of the components. Sealed plastic bags with desiccant shall be used to reduce the oxidation of the termination and shall only be opened prior to use.

#### Warning

- Use PPTC beyond the maximum ratings or improper use may result in device damage, electrical arcing and flame.
- PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be
  used when repeated fault conditions or prolonged trip events are anticipated.
- Use PPTC with a large inductance in circuit will generate a circuit voltage above the rated voltage of the PPTC.
- · Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.
- Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the
  performance of the devices.
- Users should independently evaluate the suitability of the product and test each product selected for their own application.

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