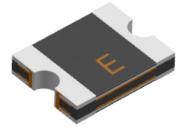
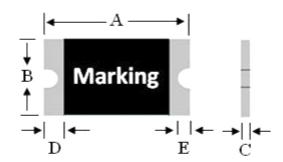
Resettable PPTC Fuses - 1210 MulticompPRO



Features

• Fast tripping,1210 Size

- Surface mountable, Solid state
- Holding Current: 4.5A to 7.5A, @25°C
- Maximum Voltage: 6V and 12V
- Operating Temperature: -40°C to +85°C
- Agency Approvals: UL E345393, TUV R50449463



RoHS

Compliant

Dimensions : Millimetres

Part NO.	Marking	A		В		С		D	E
Part NO.	Marking	Min	Max	Min	Max	Min	Max	Min	Min
MP005236	N		2.42		2.35 2.8	0.2		0.05	1.1
MP005237	S			2.25					
MP005239		3	3.43	2.35	2.0	0.3	1.1	0.25	1.1
MP005238									

Electrical Specifications

						Maximum T	ime-to-Trip	Resis	tance
Part NO.	VMAX (V)	Імах (А)	Iн (A)	Iт (A)	P ⊳ (W)	Current	Time	Rміn	R1мах
						(A)	(Sec)	(Ω)	(Ω)
MP005236	10	50	4.5	9		22.5	2	0.001	0.013
MP005237	12		6	12	1.4	30		0.0009	0.01
MP005239	6	50	7.5	15	1.4	37.5	2	0.0006	0.007
MP005238	12		C.1	.5 15		57.5		0.0006	0.007

Thermal Derating Chart-I_H (A)

Part NO.		Maximum holding current at assigned ambient temperature (A)							
Fart NO.	-40°C	-20°C	0°C	25°C	40°C	50°C	60°C	70°C	85°C
MP005236	6.66	6	5.22	4.5	3.96	3.48	3.17	2.76	2.16
MP005237	8.65	7.91	6.93	6	5.23	4.45	4	3.63	2.85
MP005239	10.5	9.65	8.5	7.5	6.4	5.3	4.8	4.45	4.42
MP005238	10.5	9.00	0.0	6.1	0.4	0.0	4.0	4.40	4.42

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Physical Characteristics

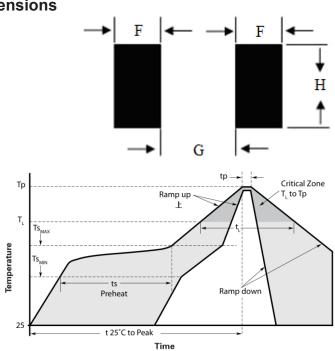
Terminal Pad Materials	Tin-Plated Nickle-copper			
Soldering Characteristics	EIA Specification RS 186-9E, ANSI/J-STD-002			
Moisture Sensitivity	Level 2a, per IPC/JEDEC J-STD 020C			

Test Procedures And Requirements

Test Item	Test Conditions	Accept/Reject Criteria
Initial Resistance	In still air at 25°C	Rmin≤R≤R1max
Time to Trip	Specified current VMAX, 25°C	t≤ Maximum Time to Trip
Holding Current	1 hour at IH, 25°C	No trip
Trip Endurance	Vmax, Imax, 1 hour	No arcing or burning

Solder Reflow Profiles And Pad Layout Dimensions

Solder pad layout dimensions (mm)				
Normal Value	F	G	Н	
	1	2	2.5	
Profile Feature		Pb-Free		
Average ramp up rate (Tsmax to Tp) 3°C/ max.				
 Preheat Temperature min (TSMIN) 150°C Temperature max (TSMAX) 200°C Time (tSMIN to tSMAX) 60 to 120 seconds 				
Time maintained above Temperature (TL) 217°C Time(tL) 60 to 150 seconds 				
Peak/Classification temperature (TP) 260°C				
Time within 5°C of actual peak temperature Time (tP) 30 seconds max				
Ramp down rate3°C/second max				
Time 25°C to peak temperature 8 minutes max				



Recommended reflow methods: IR, hot air oven, N2 environment for lead-free.

Devices are not designed to be wave soldered to the bottom side of the board.

Recommended maximum paste thickness is 0.25mm (0.01inch).

Devices can be cleaned using standard industry methods and aqueous solvents.

Devices can be reworked using the standard industry practices .Please also avoid direct contact to the device.

If reflow temperature exceed the recommended profile, devices may not meet the performance requirements.

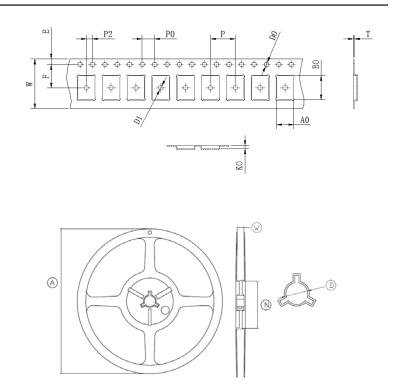
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Resettable PPTC Fuses - 1210 MulticompPRO

Packing	Information

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Tape Sp	ecifications
W	8 ± 0.3
P0	4 ± 0.1
Р	4 ± 0.1
P2	2 ± 0.1
A0	2.85 ± 0.1
B0	3.58 ± 0.1
D0	1.5 +0.1/-0
D1	1 +0.1/-0
F	3.5 ±0.1
E	1.75 ±0.1
Т	0.25±0.05
K0	1 ± 0.1
	1.4 ± 0.1
Reel Dir	nensions
А	178 ±1
D	13.3 ±0.3
W	8.5+1/-0.2
Ν	59 ±1



Storage

The maximum ambient temperature shall not exceed 40°C. Storage temperatures higher than 40°C could result in the deformation of packaging materials. The maximum relative humidity recommended for storage is 70%. High humidity with high temperature can accelerate the oxidation of the solder plating on the termination and reduce the solderability of the components. Sealed plastic bags with desiccant shall be used to reduce the oxidation of the termination and shall only be opened prior to use

Warning

- Use PPTC beyond the maximum ratings or improper use may result in device damage, electrical arcing and flame.
- PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.
- Use PPTC with a large inductance in circuit will generate a circuit voltage above the rated voltage of the PPTC.
- · Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.
- Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices.
- Users should independently evaluate the suitability of the product and test each product selected for their own application.

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