



Customer Information Notification

202008022I

Issue Date: 19-Aug-2020

Effective Date: 20-Aug-2020

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

This notice is NXP Company Proprietary.



Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Test Location	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Process	<input checked="" type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Equipment	<input type="checkbox"/> Electrical spec./Test coverage
<input type="checkbox"/> Firmware	<input type="checkbox"/> Other			

PF8100_PF8200 Errata
Update Rev 2.0 for Output
Overshoot / Undershoot

Description

NXP Semiconductors announces an errata update for the PF8100 and PF8200 devices, which could potentially be affected by an observed errata condition. The errata sheet describes existing deviations from the data sheet identified on current production devices. Potential implications on customer applications are described. The updated document provides a detailed description of the changes. Changes are summarized below.

Errata Update

ER101: Output overshoot / undershoot when enabling another regulator

- Errata has been updated to capture additional conditions associated with ER101.
- Complete behavior including workaround is described in the errata.
- Severity Level: Medium

The errata ES_PF8100_PF8200 revision 2.0 is attached to this notice and can be found at:

https://www.nxp.com/docs/en/errata/ES_PF8100_PF8200_2.pdf

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02

Reason

Errata update has been released to capture additional conditions associated with ER101, which could potentially affect some PF8100 and PF8200 device configurations.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

Customer application should take into account the identified errata.

No impact on form, fit, function, reliability or quality - no change to existing device.

Data Sheet Revision

No impact to existing datasheet

Additional information

Affected products and sales history information: see attached file

Additional documents: [view online](#)



Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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