



Customer Information Notification

201912034I

Issue Date: 03-Aug-2020

Effective Date: 04-Aug-2020

Dear *Gordon Love*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

This notice is NXP Company Proprietary.



Change Category

- | | | | | |
|--|--|--|---|---|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Location | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Process | <input type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware | <input checked="" type="checkbox"/> Other - Datasheet and Reference Manual | | | |

Datasheet and Reference Manual Update for MC9S08PA/PT/PL Series

Description

NXP Semiconductors announces that the MC9S08PA/PT/PL Series Datasheet and Reference Manual have been updated to below new revision.

Datasheet: MC9S08PA4_Rev.10, MC9S08PA16_Rev.4, MC9S08PA60_Rev.4, MC9S08PT16_Rev4, MC9S08PT60_Rev.6, MC9S08PL16_Rev.4

Reference Manual: MC9S08PA4RM_Rev.7, MC9S08PA16RM_Rev.3, MC9S08PA60RM_Rev.3, MC9S08PT16RM_Rev.4, MC9S08PT60RM_Rev.5, MC9S08PL4RM_Rev.5, MC9S08PL16RM_Rev.5, MC9S08PL60RM_Rev.5 .

The revision history included in the updated document provides a detailed description of the changes.

The updated MC9S08PA Series Datasheet and Reference Manual can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/additional-architectures/8-bit-s08-mcus/8-bit-5v-full-featured-mcu-with-eeeprom:S08PA?tab=Documentation_Tab

The updated MC9S08PT Series Datasheet and Reference Manual can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/additional-architectures/8-bit-s08-mcus/8-bit-5v-full-featured-mcu-with-eeeprom-and-tsi:S08PT?tab=Documentation_Tab

The updated MC9S08PL Series Datasheet and Reference Manual can be found at:
https://www.nxp.com/products/processors-and-microcontrollers/additional-architectures/8-bit-s08-mcus/8-bit-5v-cost-effective-mcu-with-eeeprom:S08PL?&tab=Documentation_Tab

Reason

The Datasheet and Reference Manual have been updated to provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Additional information

Affected products and sales history information: see attached file

Additional documents: [view online](#)



Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name Kan Liu
Position Product Engineer
e-mail address Kan.liu@nxp.com

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

Customer Focus, Passion to Win.

NXP Quality Management Team.

About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise. These innovations are used in a wide range of automotive, identification, wireless infrastructure, lighting, industrial, mobile, consumer and computing applications.

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