



## Customer Information Notification

2020080071

**Issue Date:** 12-Aug-2020

**Effective Date:** 13-Aug-2020

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from NXP.

For detailed information we invite you to [view this notification online](#)

**This notice is NXP Company Proprietary.**



# QUALITY

### Management Summary

The RT600 errata sheet has been updated to rev 1.3 with the GPIO.1 and ADC.1 errata.

#### Change Category

- |  |   |  |   |   |
|--|---|--|---|---|
| <input type="checkbox"/> Wafer Fab Process   | <input type="checkbox"/> Assembly Process   | <input type="checkbox"/> Product Marking           | <input type="checkbox"/> Test Location  | <input type="checkbox"/> Design                         |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification  | <input type="checkbox"/> Test Process   | <input checked="" type="checkbox"/> Errata              |
| <input type="checkbox"/> Wafer Fab Location  | <input type="checkbox"/> Assembly Location  | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Equipment | <input type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware            | <input type="checkbox"/> Other              |  |   |   |

RT600 Errata Sheet  
Update to rev 1.3 with  
GPIO.1 and ADC.1  
Errata

### Description

The RT600 errata sheet has been updated to rev 1.3 to include the GPIO.1 and ADC.1 errata.

GPIO.1: During initial power-up, a brief pull-up pulse could occur on the port pins.

ADC.1: ADC misses software and hardware triggers when there is no ADC clock.

See the errata sheet for details. The updated errata sheet is available on the NXP website:

<https://www.nxp.com.cn/docs/en/errata/ES-RT600.pdf>

#### Reason

This customer information notice has been published to inform customers of the latest list of errata.

#### Identification of Affected Products

Product identification does not change

## Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Errata documentation update only.

### Data Sheet Revision

No impact to existing datasheet

## Additional information

Affected products and sales history information: see attached file

Additional documents: [view online](#)

## Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact NXP "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

**Name** Tim Camenzind

**Position** Quality Manager - LPC Microcontroller Products

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Customer Focus, Passion to Win.

NXP Quality Management Team.

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