

# 200902845 Errata Revision 0.6 for EFR32MG21 and Revision 0.5 for EFR32BG21

Bulletin Issue Date: 9/1/2020

Effective Date: 9/1/2020

## **Description of Change**

Silicon Labs announces Errata revision 0.6 for EFR32MG21 and Errata revision 0.5 for EFR32BG21 Wireless SoCs. The following updates have been made to the errata documents.

Updated WDOG\_E301 – Clear Command is Lost Upon EM2 Entry Added I2C\_E303 – I2C Fails to Indicate new Incoming Data

See the Revision History of the errata documents for a complete list of changes.

## **Reason for Change**

Errata document revised for updated errata.

### **Product Identification**

Existing Part # EFR32BG21A010F512IM32-B EFR32BG21A010F512IM32-BR EFR32BG21A010F768IM32-B EFR32BG21A010F768IM32-BR EFR32BG21A010F1024IM32-B EFR32BG21A010F1024IM32-BR EFR32BG21A020F512IM32-B EFR32BG21A020F512IM32-BR EFR32BG21A020F768IM32-B EFR32BG21A020F768IM32-BR EFR32BG21A020F1024IM32-B EFR32BG21A020F1024IM32-BR EFR32BG21A010\*\*\*\*\*IM32-B EFR32BG21A010\*\*\*\*\*IM32-BR EFR32MG21A010F512IM32-B EFR32MG21A010F512IM32-BR EFR32MG21A010F768IM32-B EFR32MG21A010F768IM32-BR EFR32MG21A010F1024IM32-B EFR32MG21A010F1024IM32-BR EFR32MG21A020F512IM32-B EFR32MG21A020F512IM32-BR EFR32MG21A020F768IM32-B EFR32MG21A020F768IM32-BR EFR32MG21A020F1024IM32-B EFR32MG21A020F1024IM32-BR EFR32MG21A010\*\*\*\*\*IM32-B EFR32MG21A010\*\*\*\*\*IM32-BR EFR32MG21A020\*\*\*\*\*IM32-B EFR32MG21A020\*\*\*\*\*IM32-BR This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

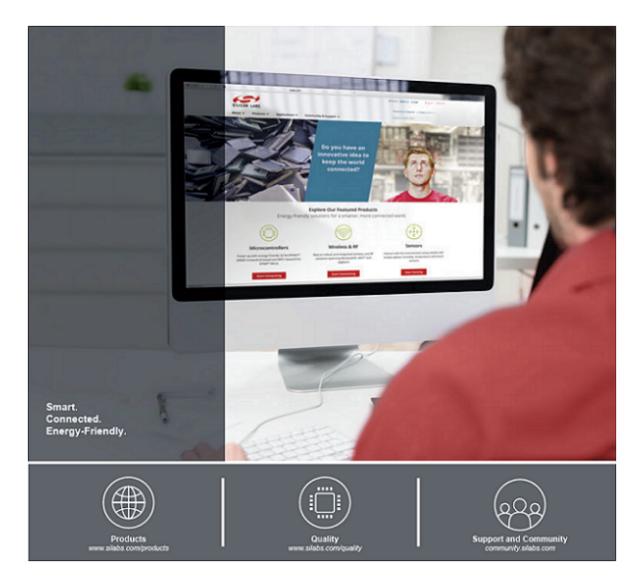
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#### **Customer Actions Needed:**

Review updated errata document.

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